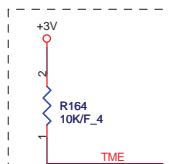
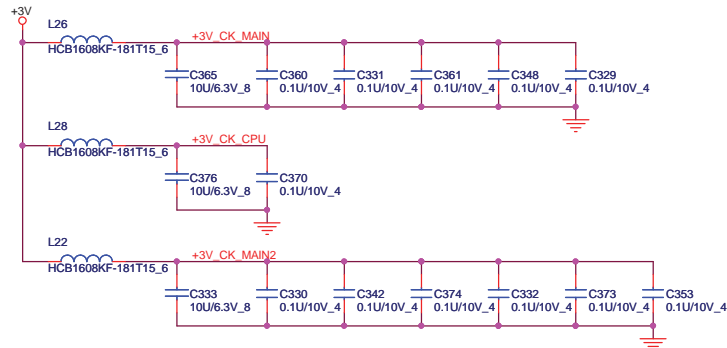
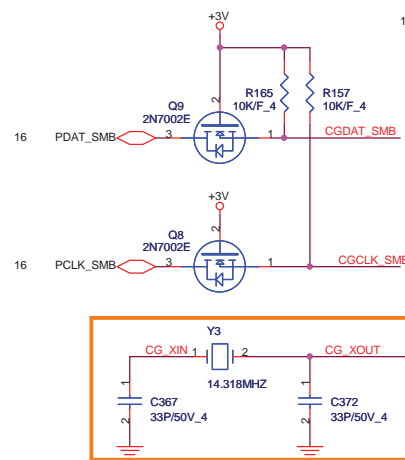




3,4,5,6,8,9,14,17,27,28 +1.05V  
4,6,9,10,11,12,13,14,15,16,17,18,19,20,21,22,23,24,28,30 +3V

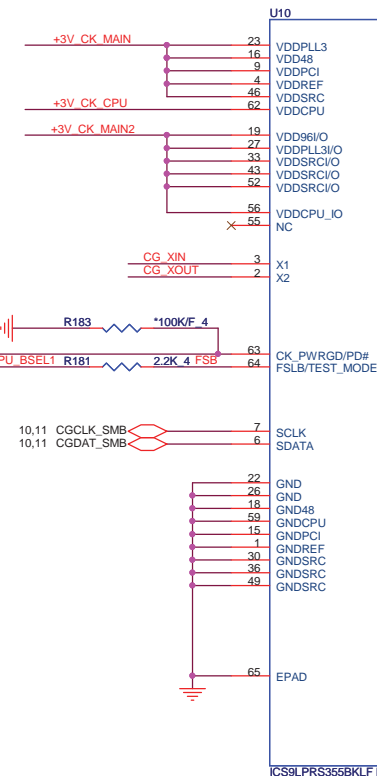
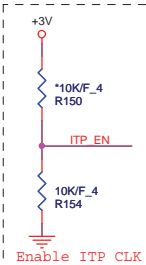
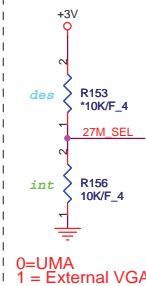


0=overclocking  
of CPU and  
SRC Allowed  
1 = overclocking  
of CPU and SRC  
not Allowed

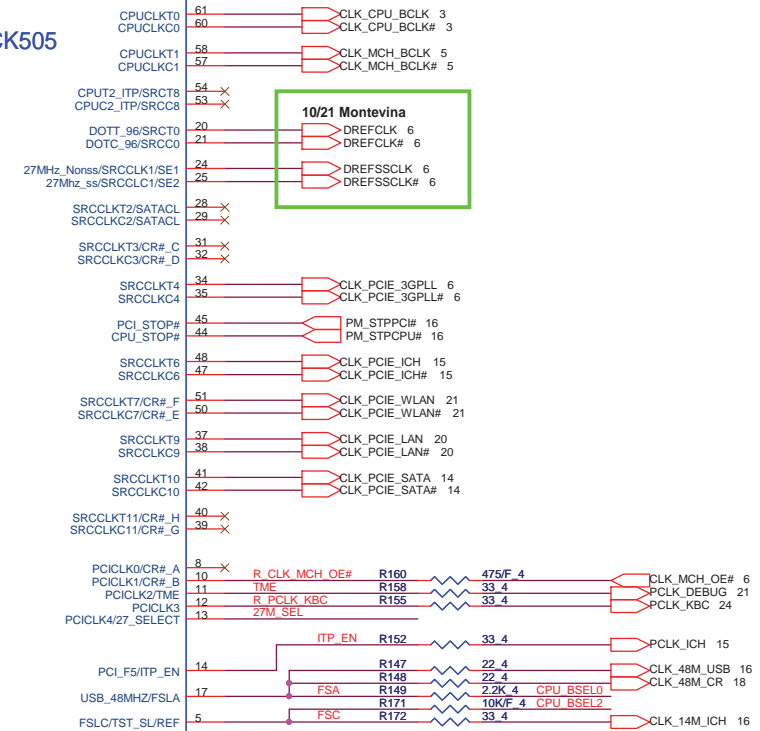


DB:Change from 27P to 33P(TXC suggestion)

27M_SEL PIN13	PIN20	PIN21	PIN24	PIN25
0=UMA	DOT96T	DOT96C	SRCT1/LCDT_100	SRCT1/LCDT_100
1 = External VGA	SRCT0	SRCC0	27Mout-NSS	27Mout-SS



CK505

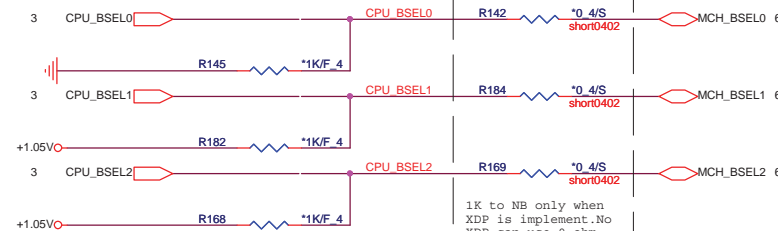


CK505 QFN64

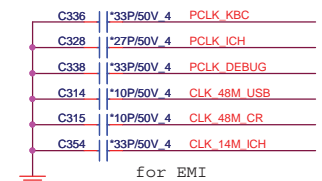
Silego SLG8SP513VTR AL8SP513000  
Realtek RTM875N-606-VD-GR AL000875000  
ICS ICS9LPRS355BKLF ALPRS355000



### CPU Clock select



FSC	FSB	FSA	CPU	SRC	PCI
1	0	1	100	100	33
0	0	1	133	100	33
0	1	1	166	100	33
0	1	0	200	100	33
0	0	0	266	100	33
1	0	0	333	100	33
1	1	0	400	100	33
1	1	1	RSVD	100	33



**PROJECT :AX3**  
Quanta Computer Inc.

Size Custom Document Number  
**Clock Generator**

Date: Friday, November 27, 2009 Sheet 2 of 30

Rev 1A

<http://laptop-motherboard-schematic.blogspot.com/>

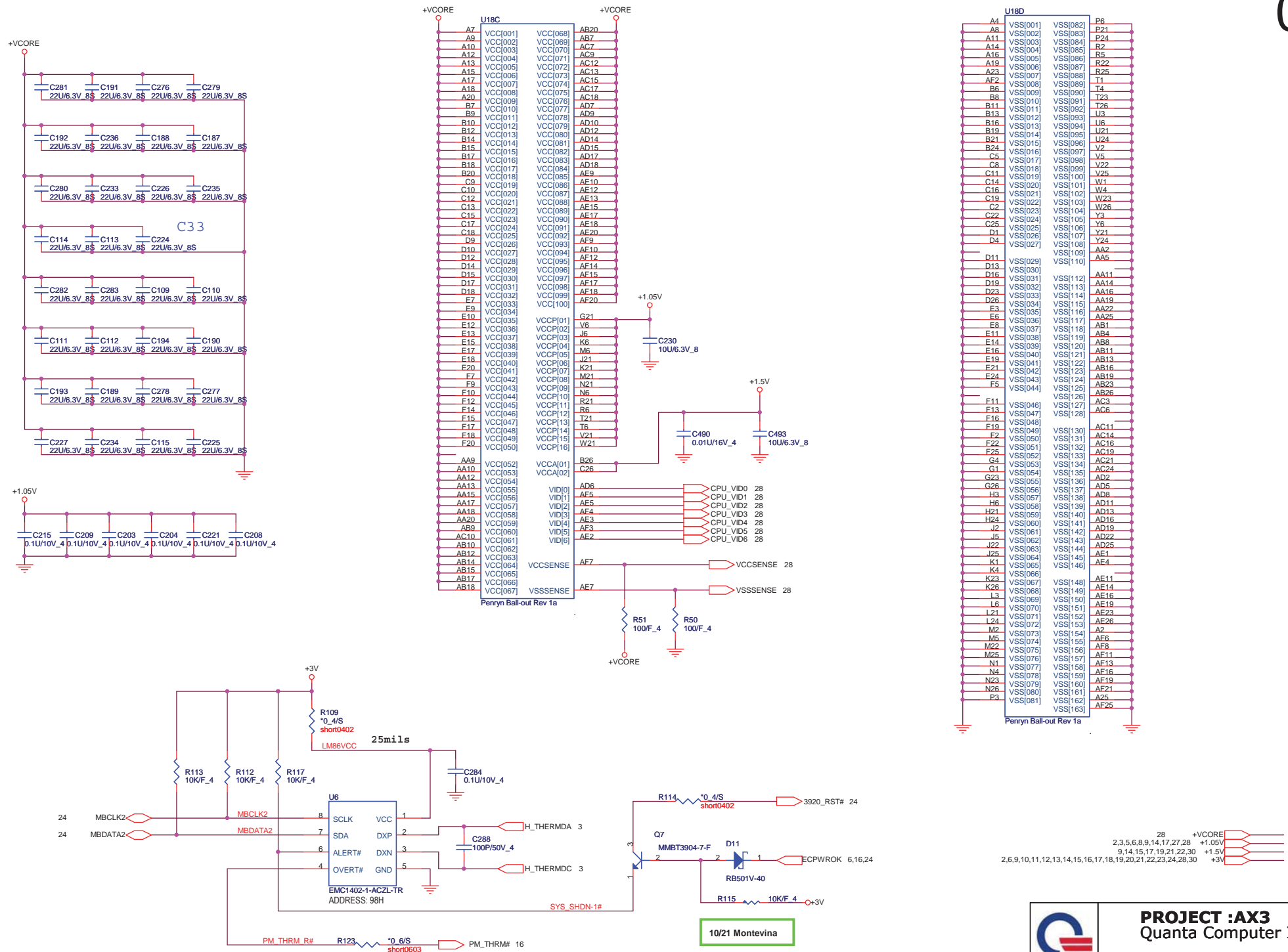
```
CPU socket
LTS DG0^8000000
MLX DGT^8000000
SUY DGT^8000011
FOX DGT^8000021
```

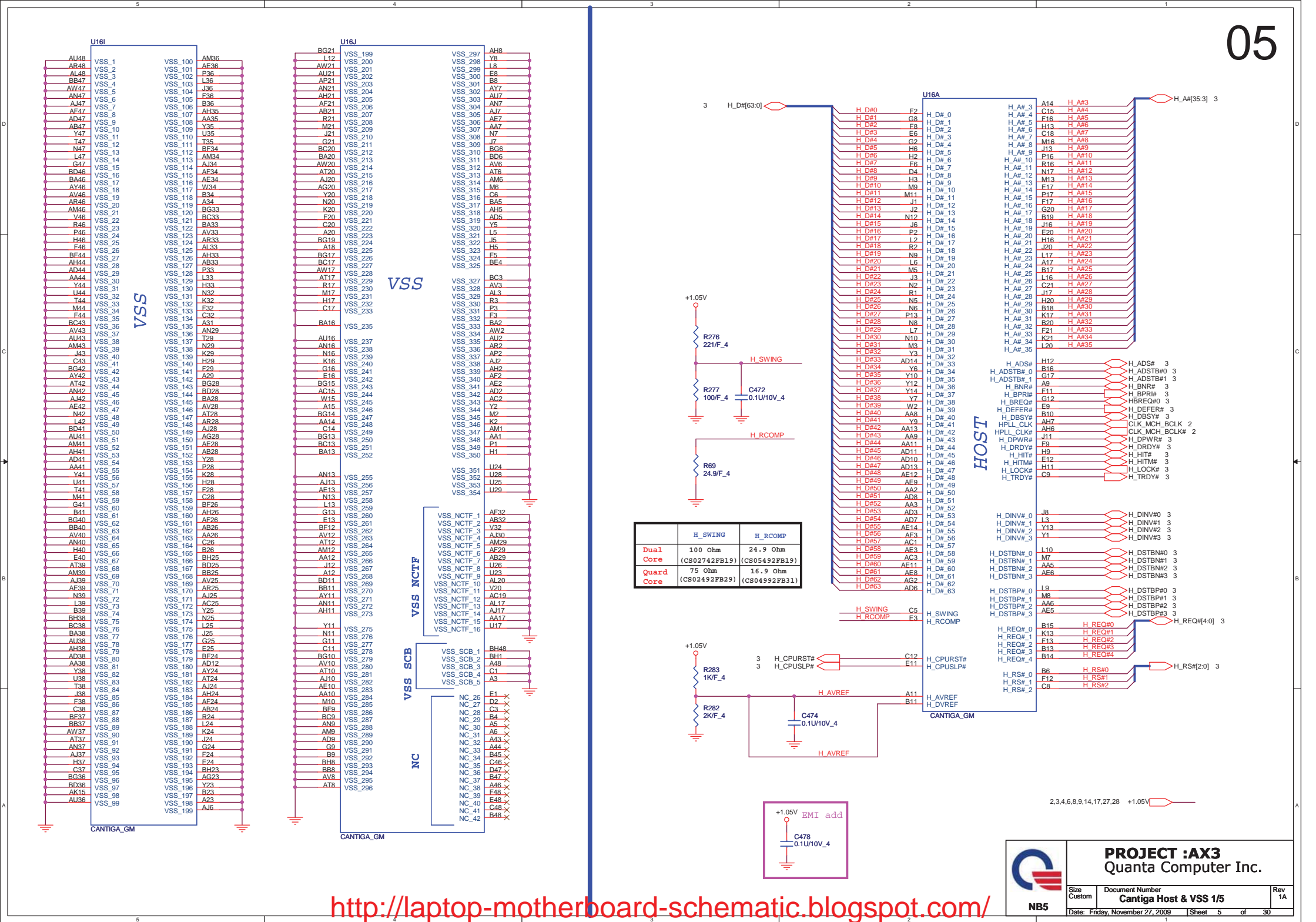
2,4,5,6,8,9,14,17,27,28 +1.05V



**PROJECT :AX3**  
Quanta Computer Inc.

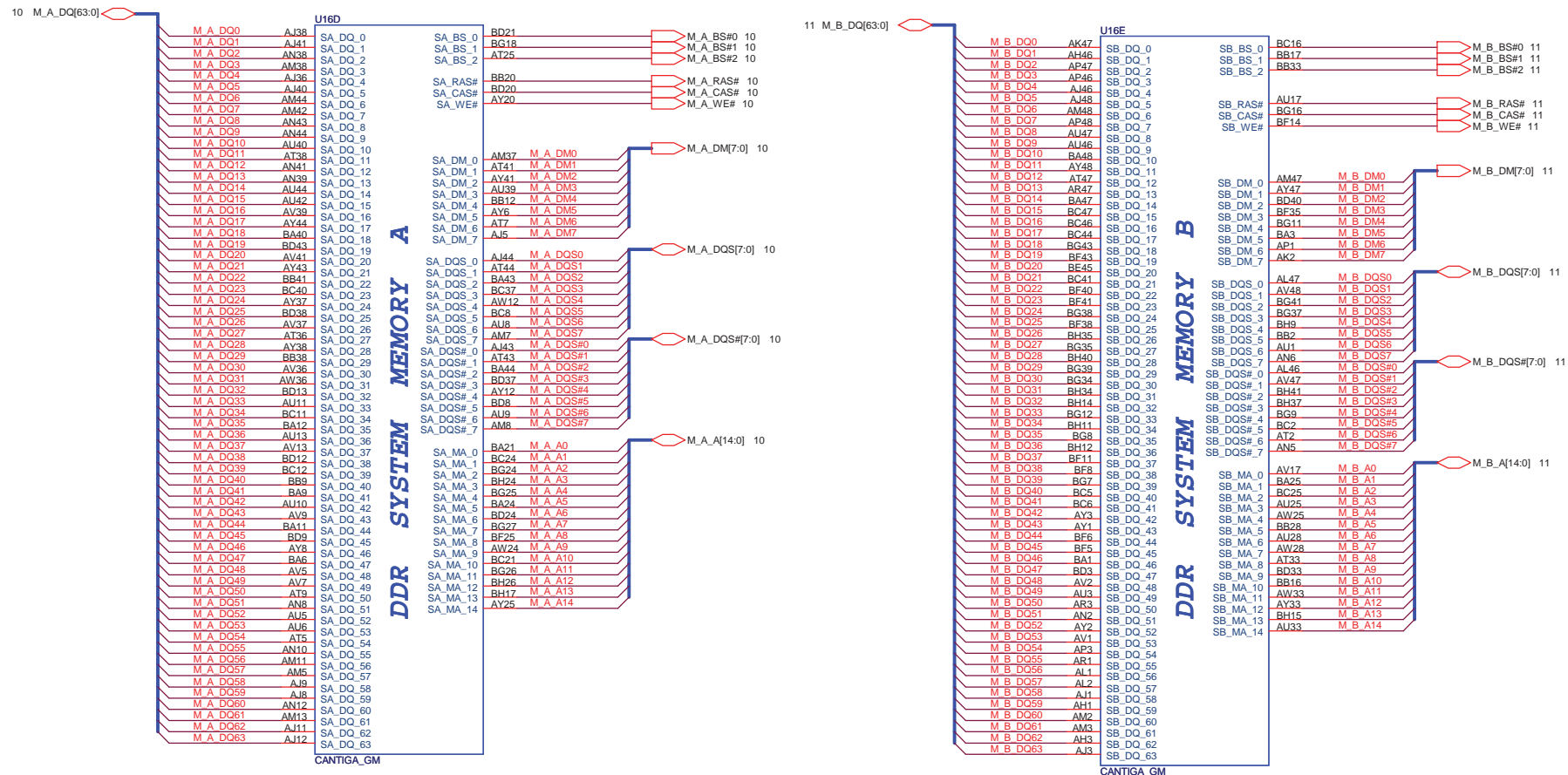
Size Custom	Document Number <b>Penryn Host 1/2</b>	Rev 1A
Date: Friday, November 27, 2009		Sheet 3 of 30

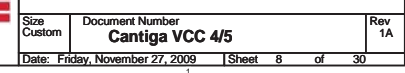






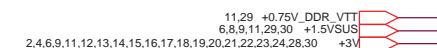


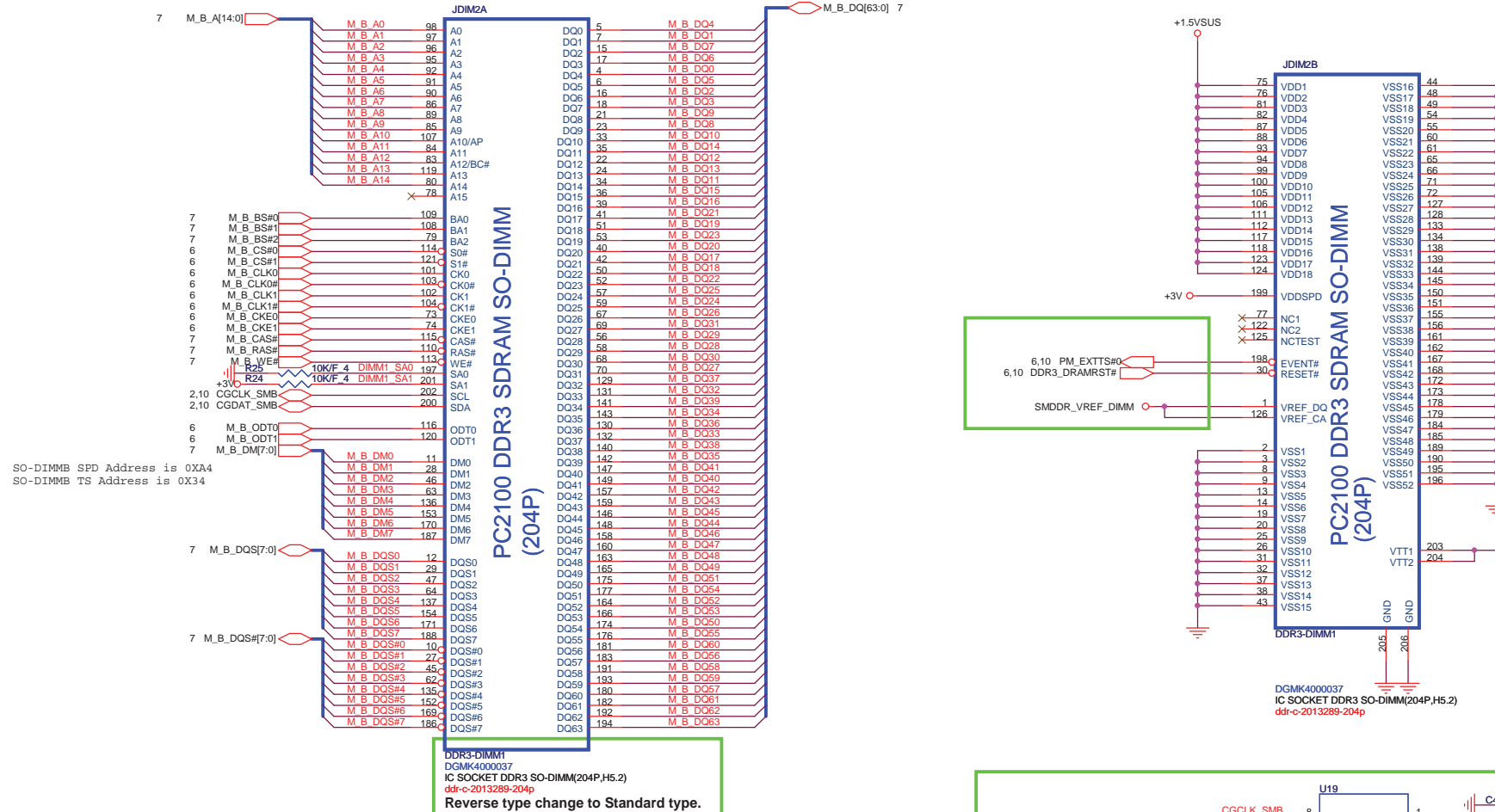






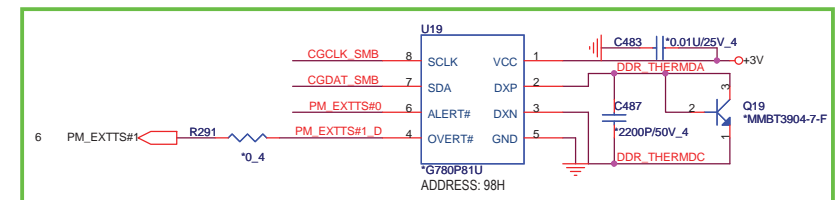
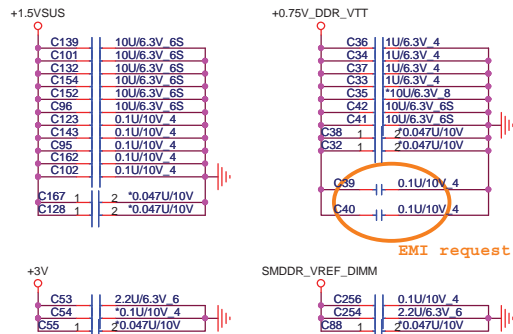






### Place these Caps near So-Dimm1.

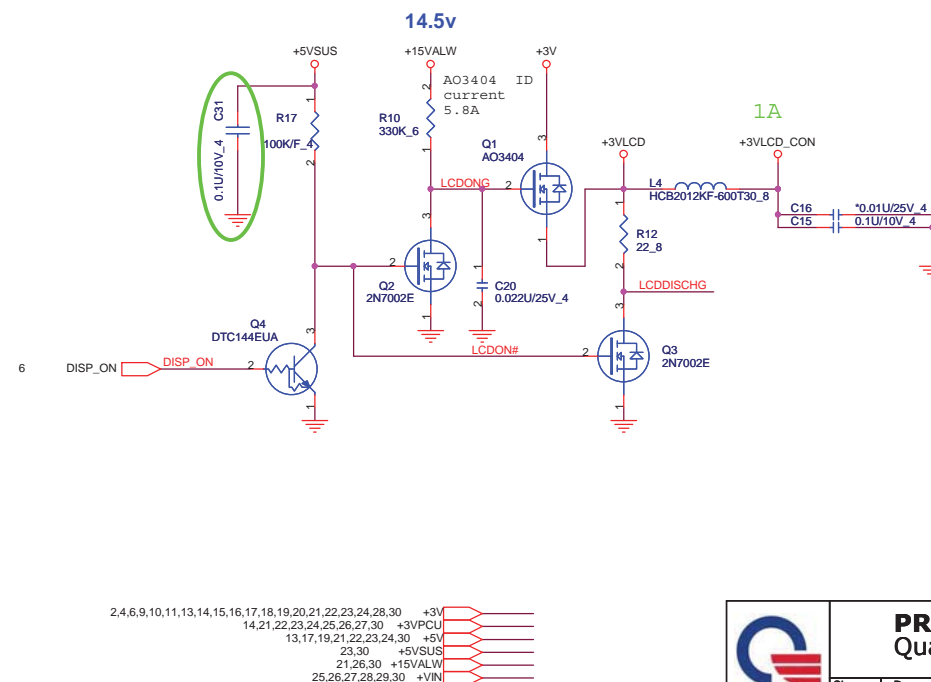
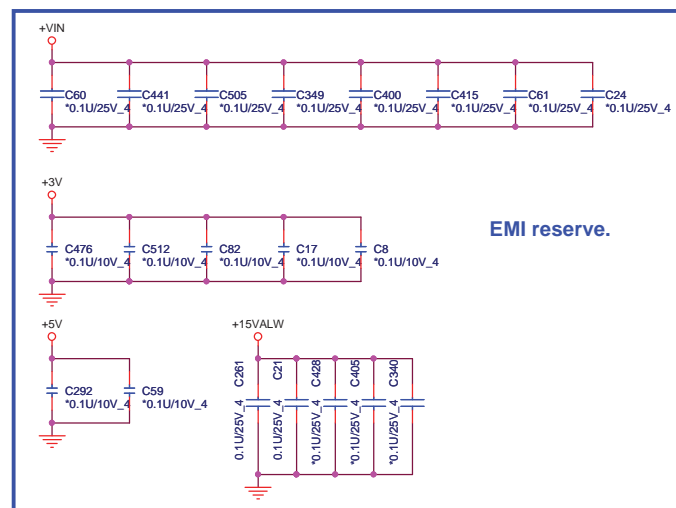
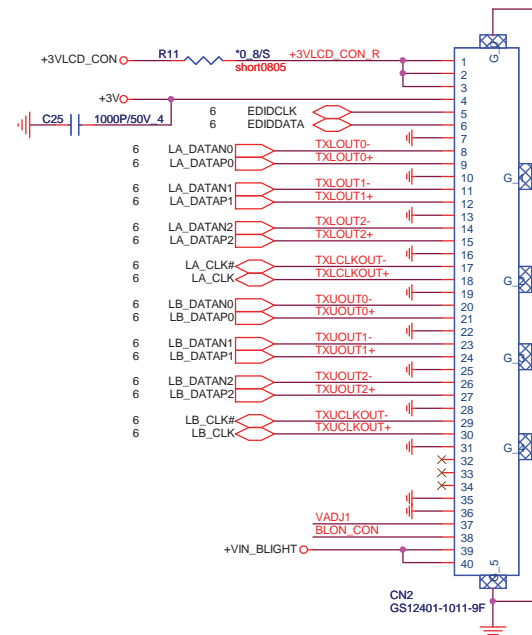
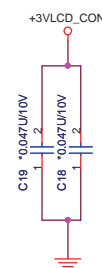
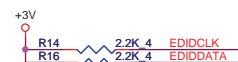
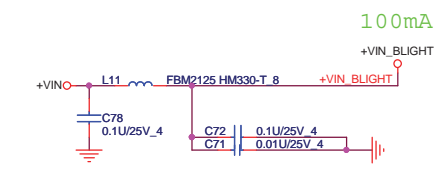
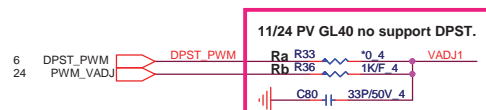
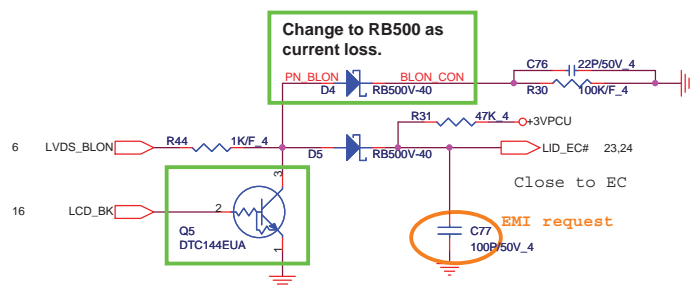
Some Projects replace 10UF 0805 by 4.7UF 0603  
It can cost down 30%



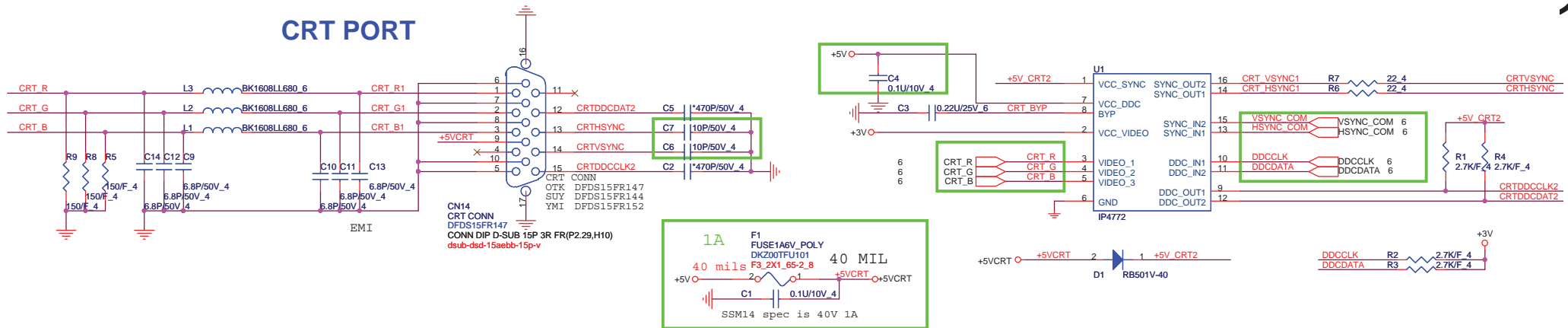
**PROJECT :AX3**  
Quanta Computer Inc.

Size Custom Document Number  
DDR3 DIMM-1  
Date: Friday, November 27, 2009 Sheet 11 of 30 Rev 1A

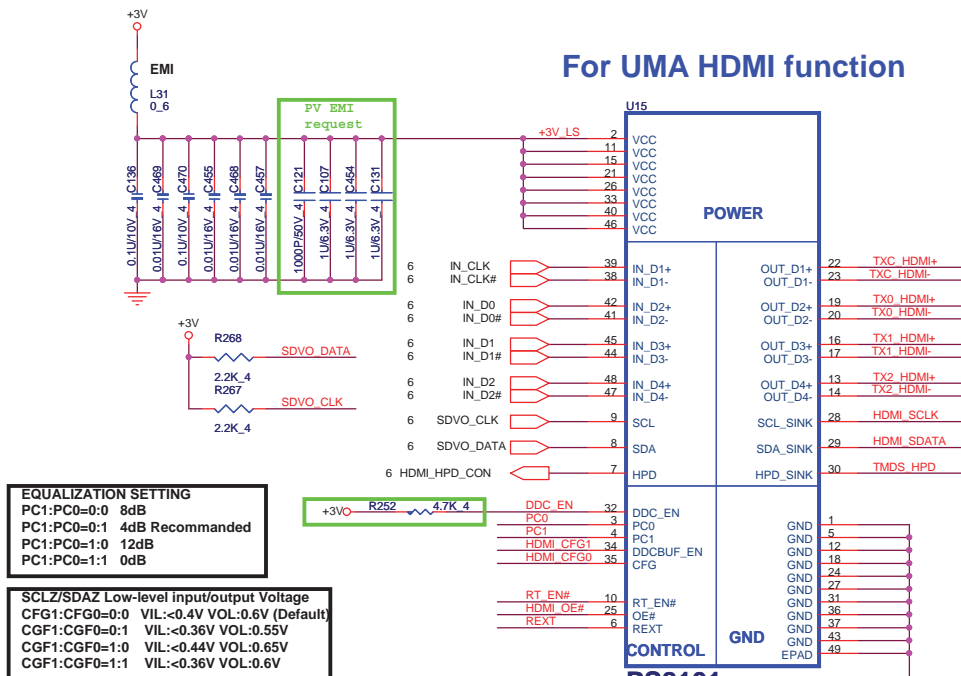
## LID Switch



## CRT PORT



## For UMA HDMI function

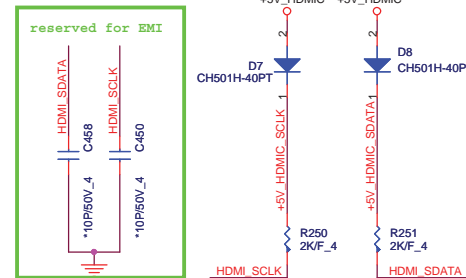


9/16 : PIM: need use ALP411LS000 or ALP411LS004 for capella  
CHR : need Na R1182, add R1027 for capella

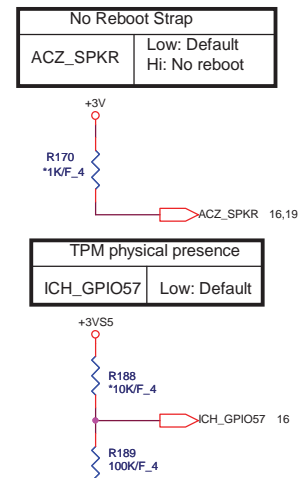
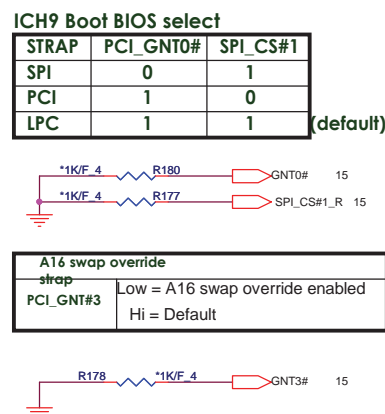
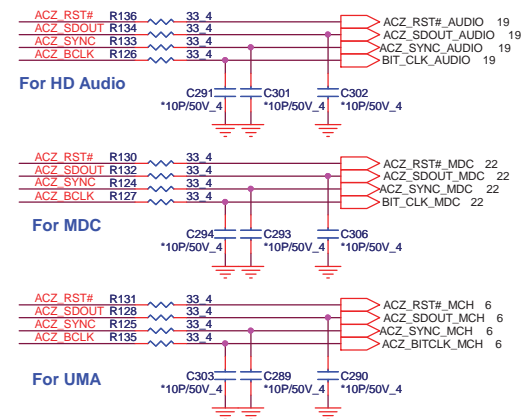
Vendor: PDT P/N: AL008101001

Vendor: CHR P/N: AL007318002

Vendor: PIM P/N: ALP411LS002

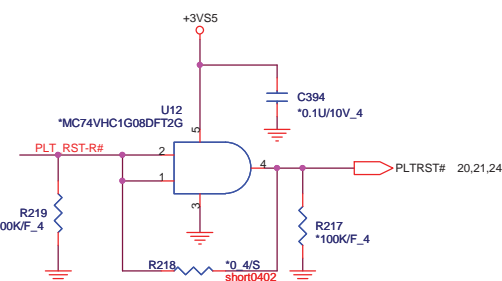
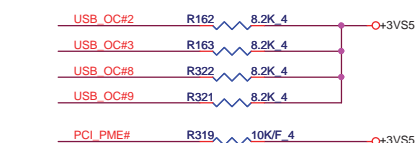
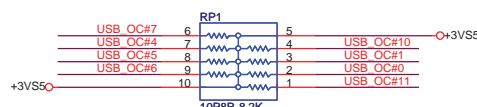
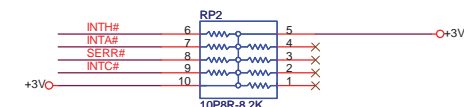
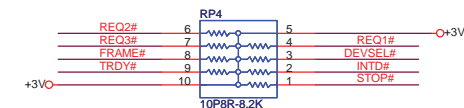
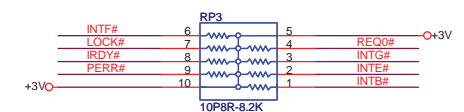
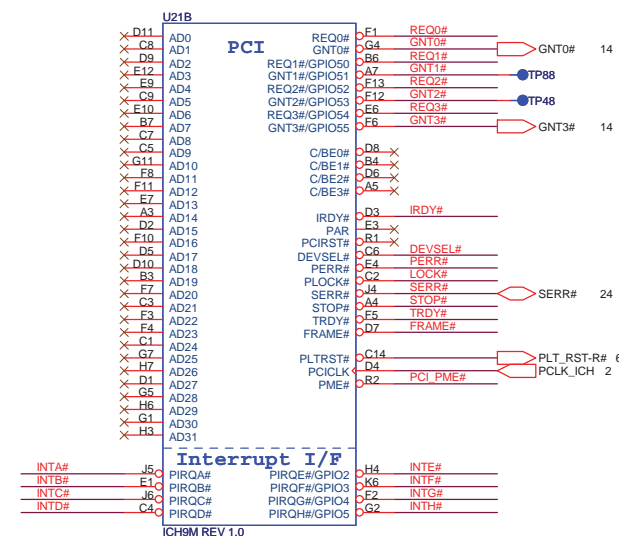
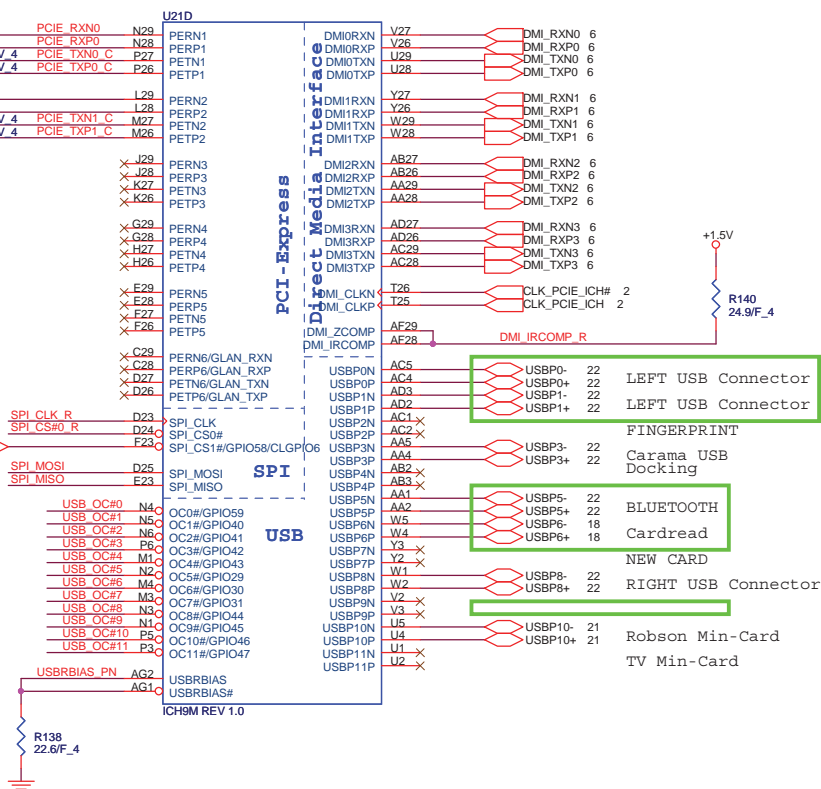
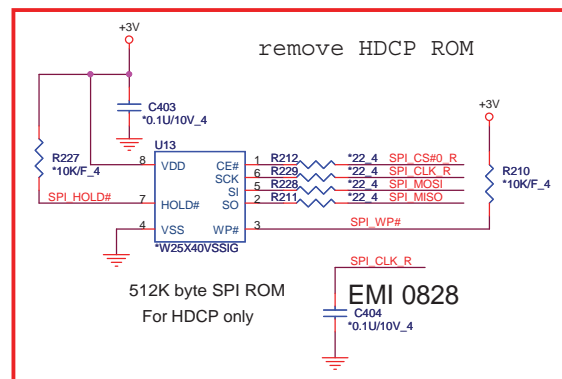







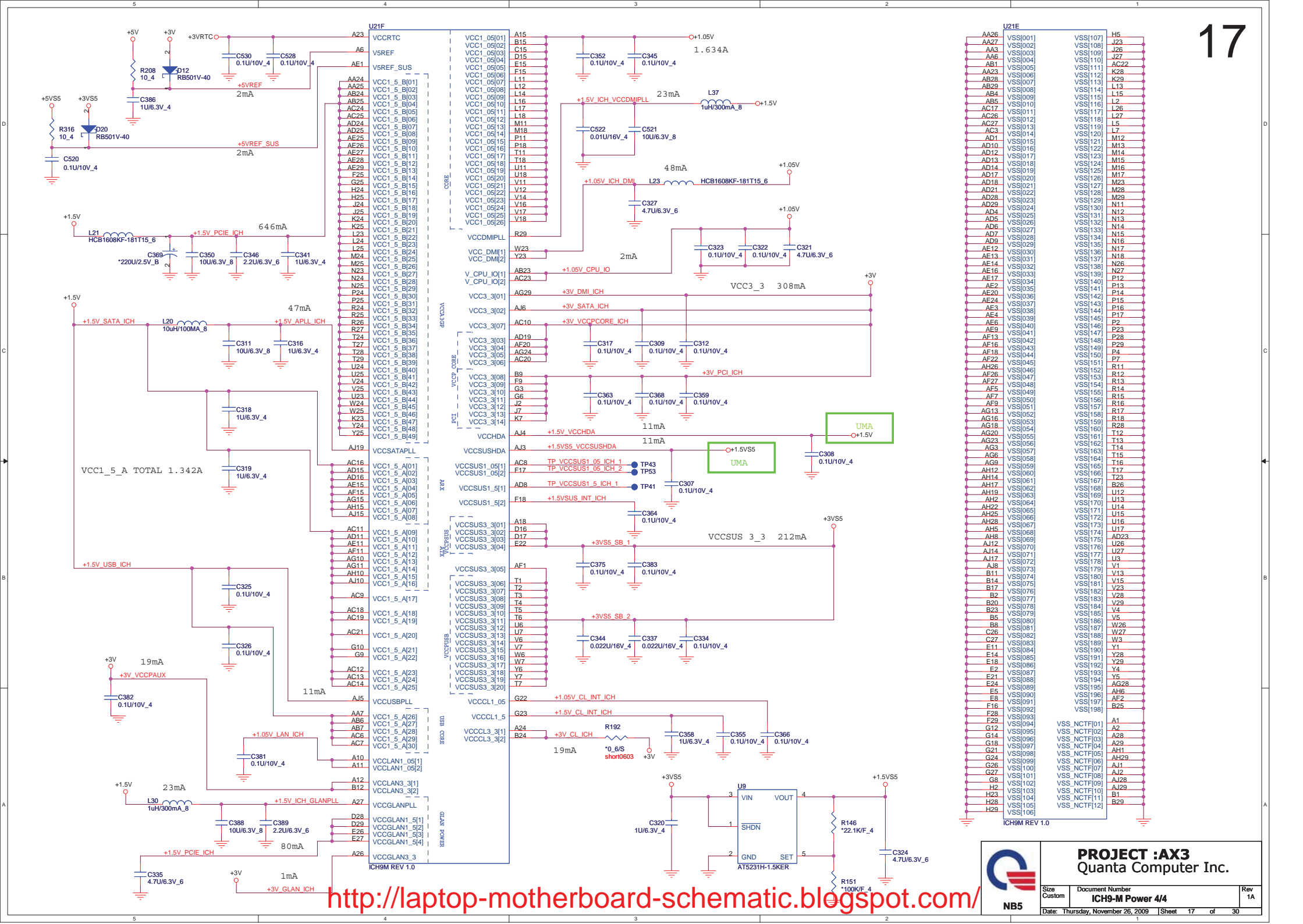
MINI CARD PCI-E (WLAN)

PCIE-LAN

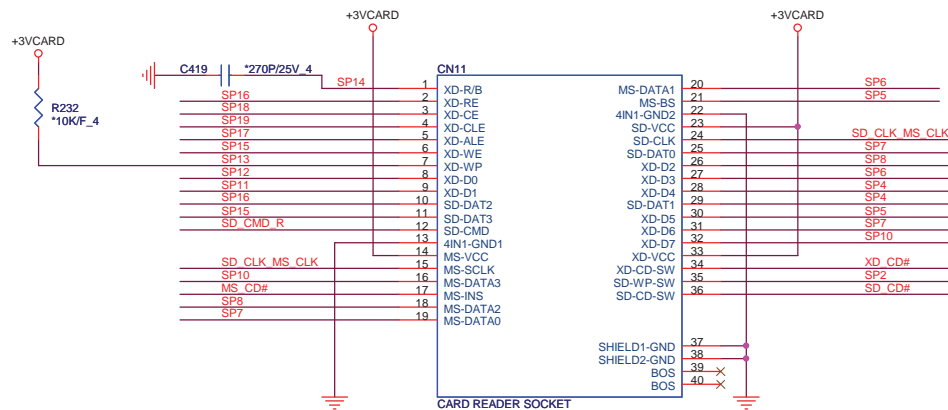
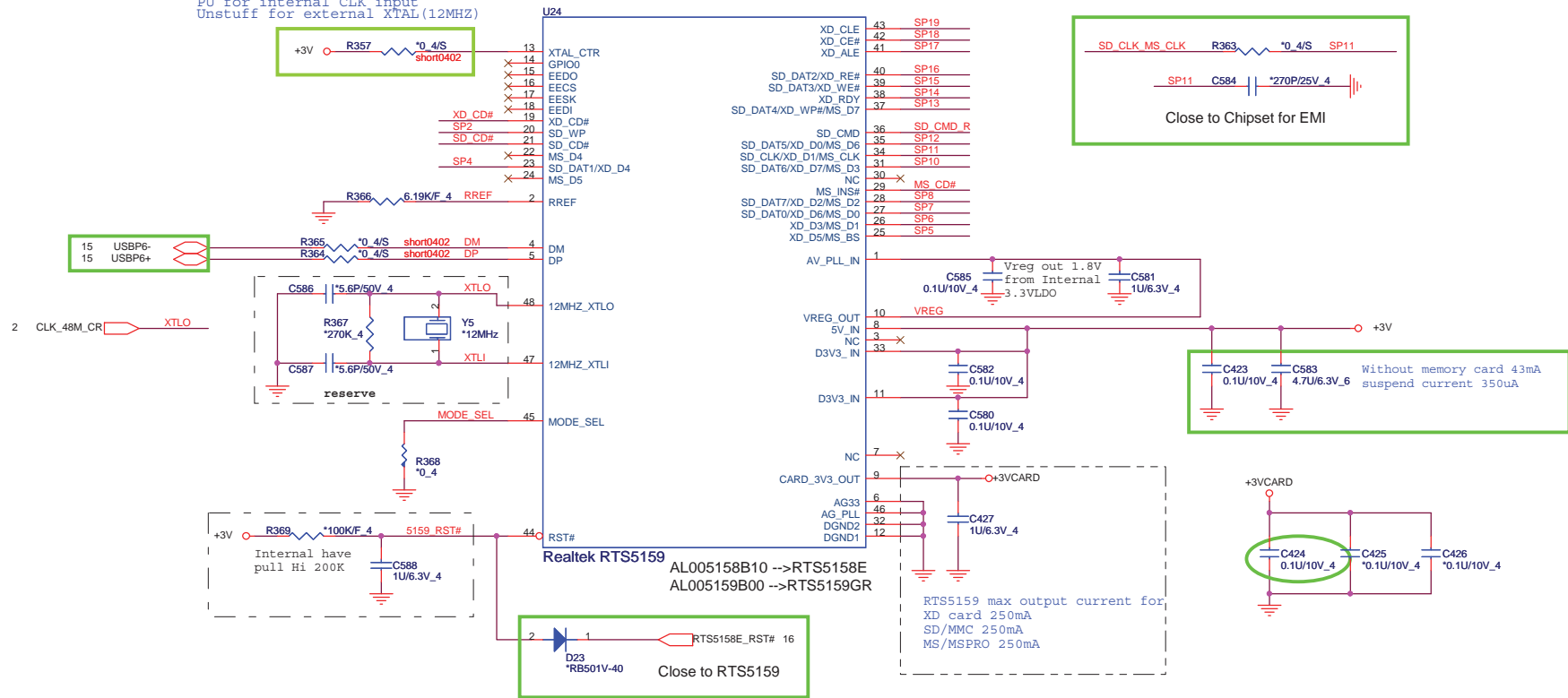




 <b>NB5</b>	<b>PROJECT :AX3</b> <b>Quanta Computer Inc.</b>		
	Size Custom	Document Number <b>ICH9-M GPIO/Board ID 3/4</b>	Rev <b>1A</b>
Date: Friday, November 27, 2009		Sheet 16 of 30	

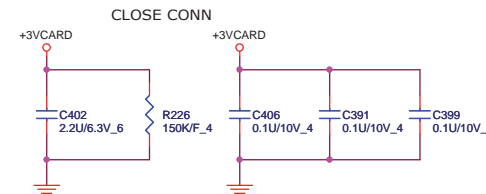


PU for internal CLK input  
Unstuff for external XTAL(12MHZ)

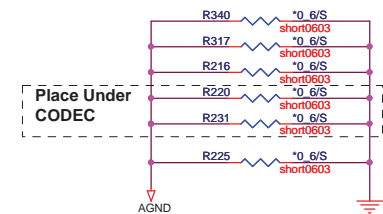
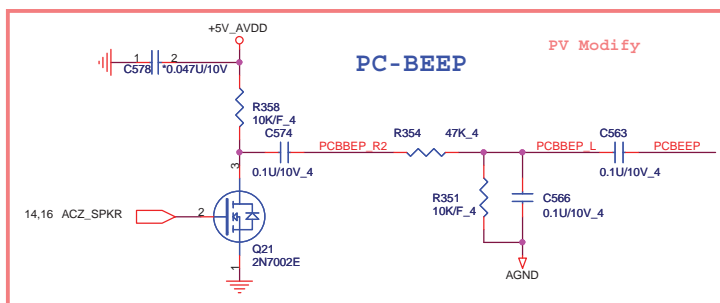
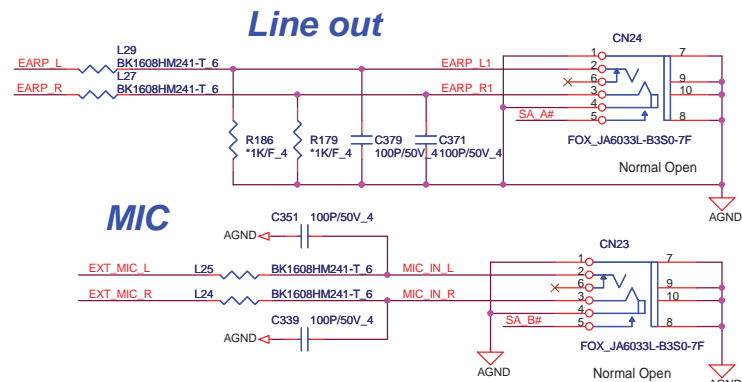
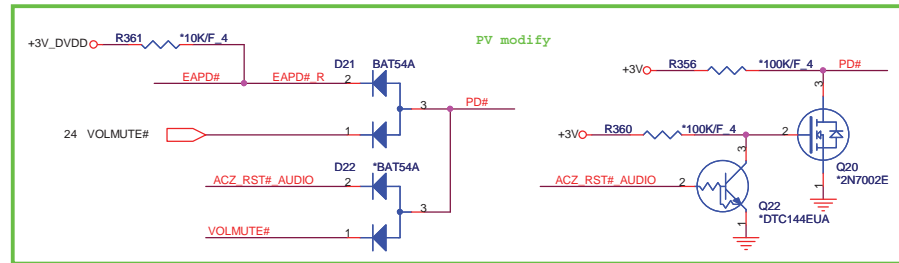
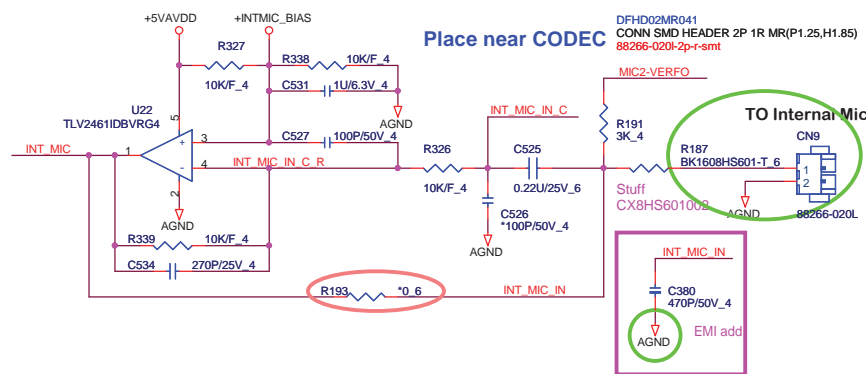
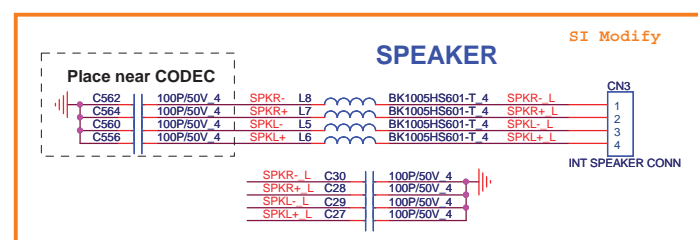
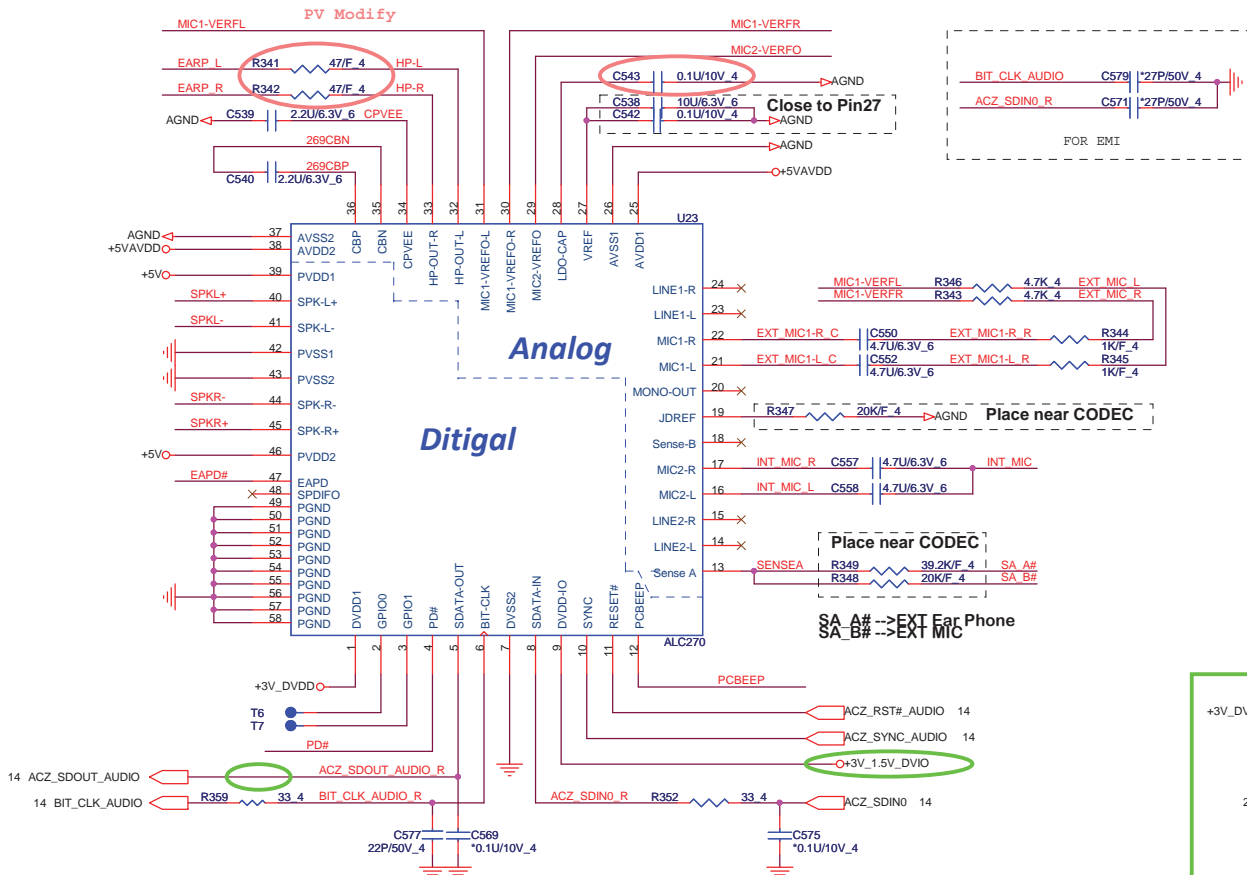
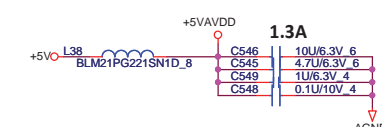
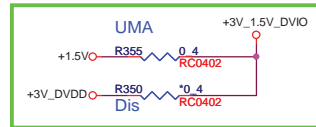
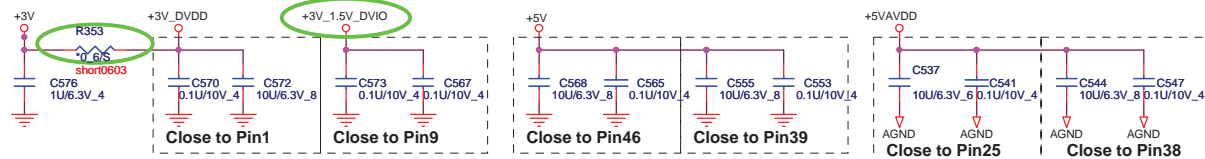


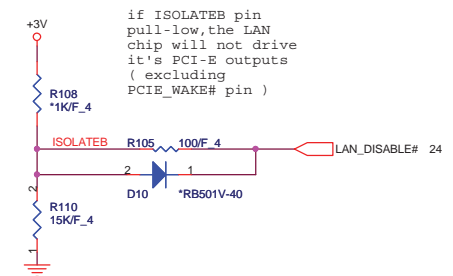
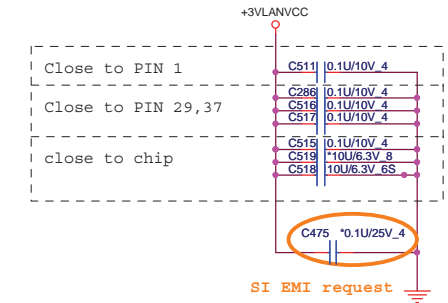
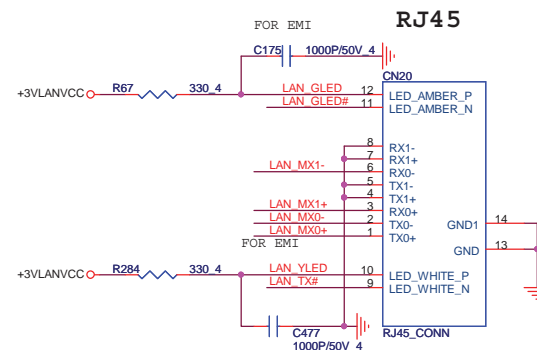
Note:

SD/MMC	MS	XD
SP0		
SP1	SD WP	XD CD#
SP2	SD CD#	
SP3	SD DAT1	XD D4
SP4	MS BS	XD D5
SP5	MS D1	XD D3
SP6	SD DAT0	MS D0
SP7	SD DAT7	MS D2
SP8	SD DAT4	XD D2
SP9	MS INS#	
SP10	SD DAT6	MS D3
SP11	SD CLK	MS SCLK
SP12	SD DAT5	XD D0
SP13	SD DAT4	XD WP#
SP14	SD DAT3	XD R/B#
SP15	SD DAT2	XD WE#
SP16	SD DAT1	XD RE#
SP17		XD ALE
SP18		XD CE#
SP19		XD CLE



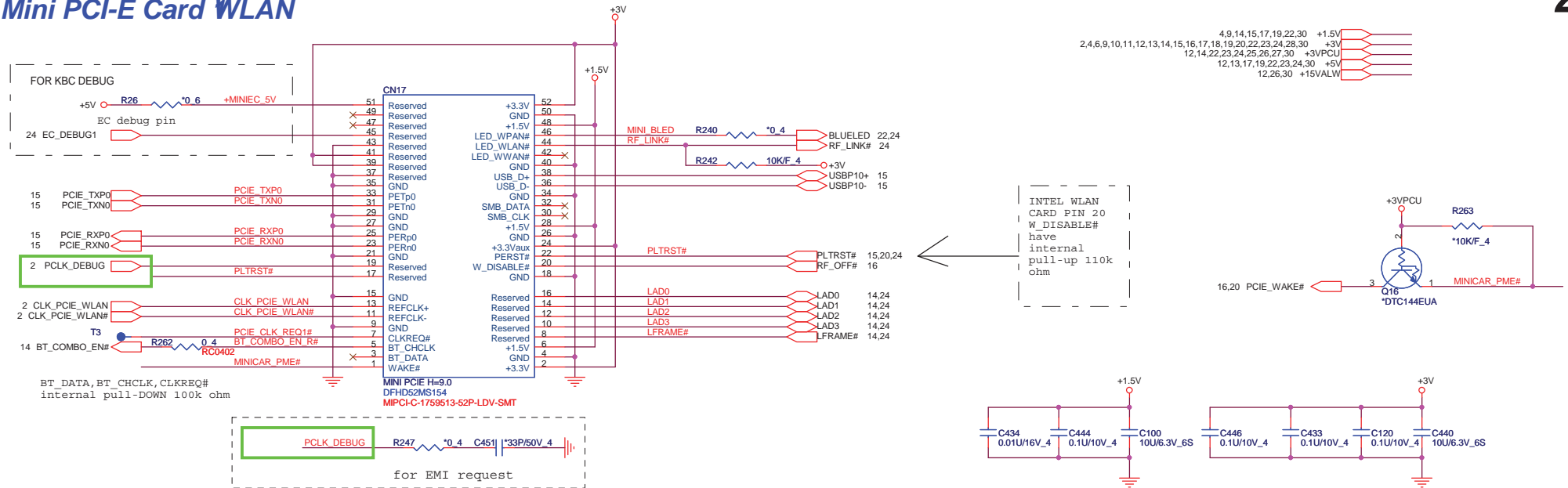




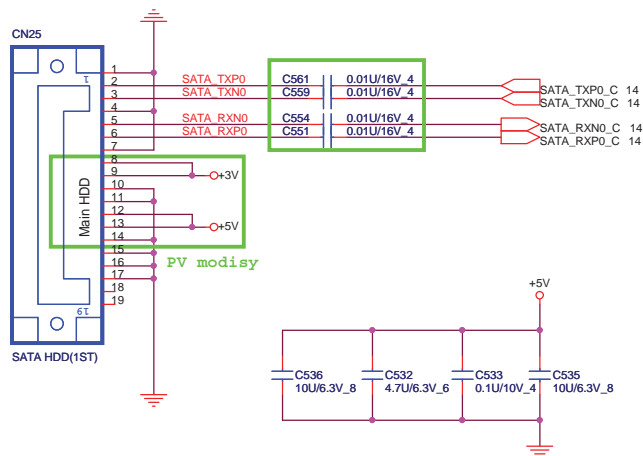
[illegible]

2,4,6,9,10,11,12,13,14,15,16,17,18,19,21,22,23,24,28,30 +3V  
30 +3VLANVCC

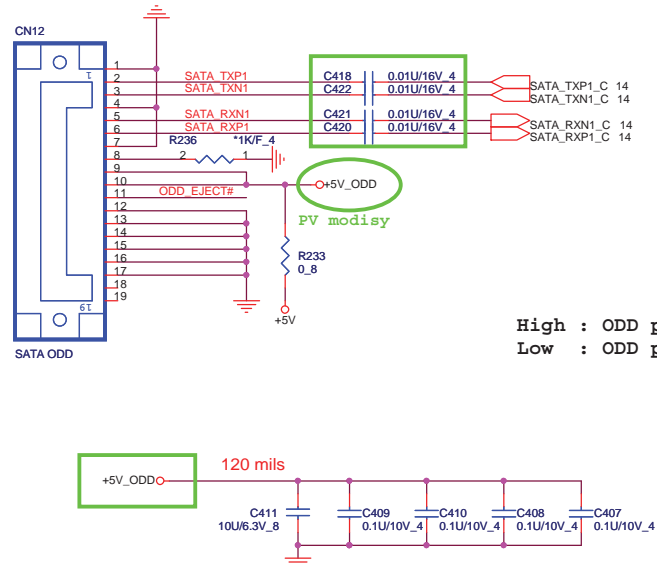
## Mini PCI-E Card WLAN



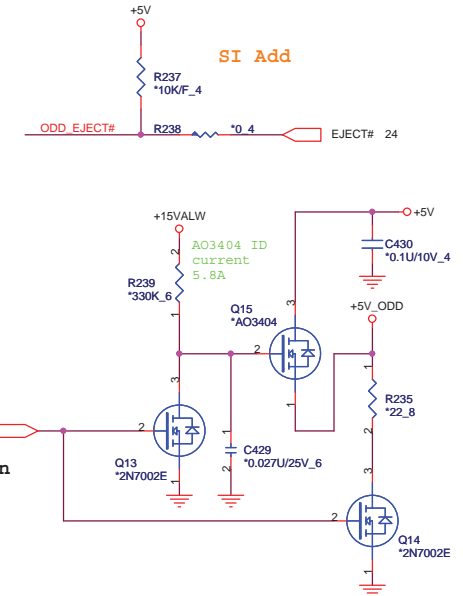
## SATA HDD CONNECTOR



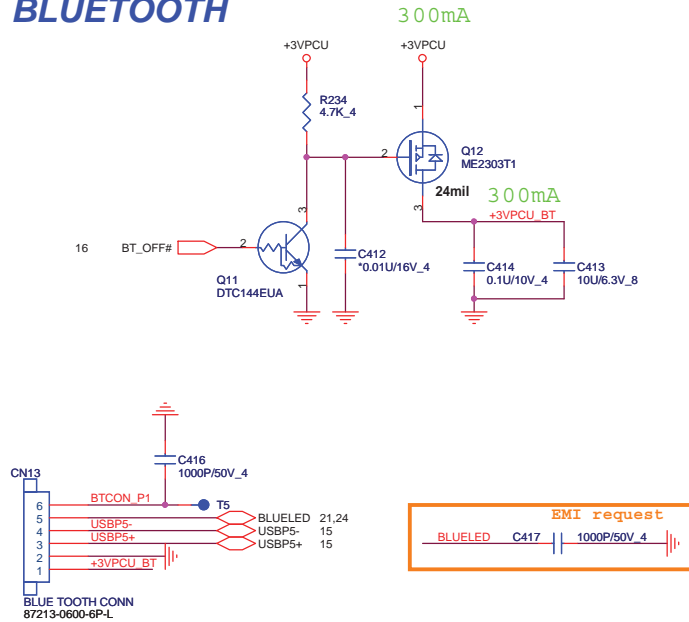
## SATA ODD CONNECTOR



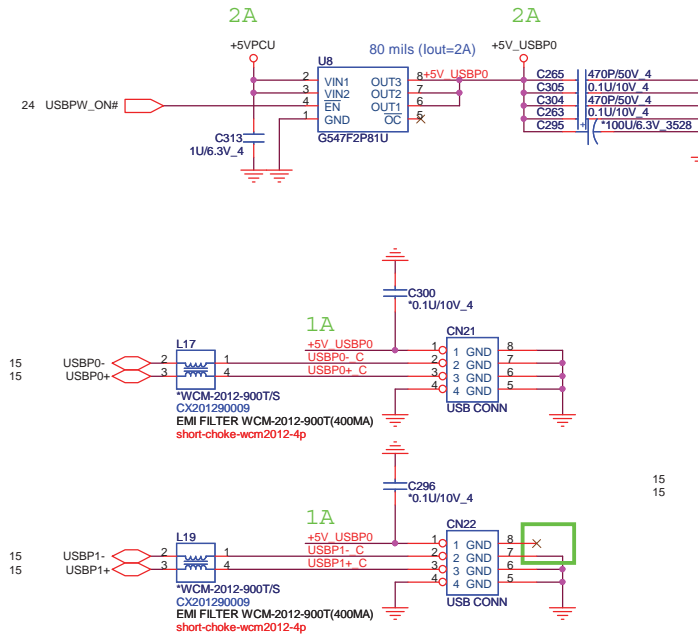
High : ODD power down  
Low : ODD power on



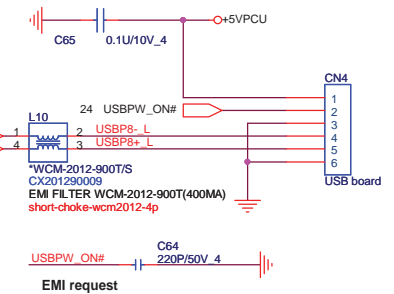
## BLUETOOTH



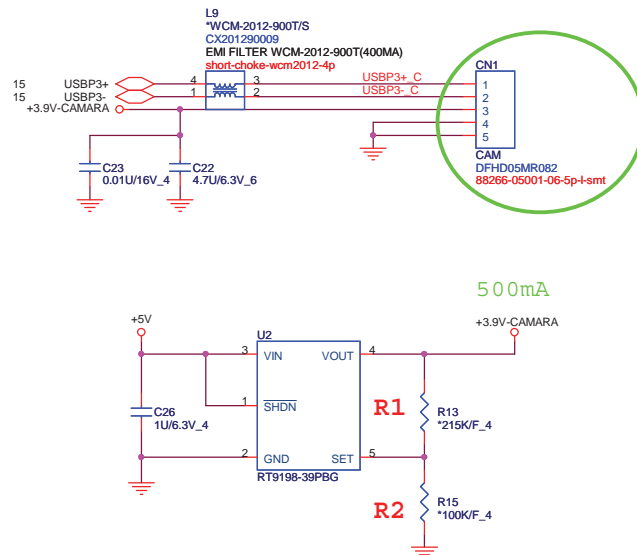
## LEFT SIDE USBX1



## Right SIDE USBX1

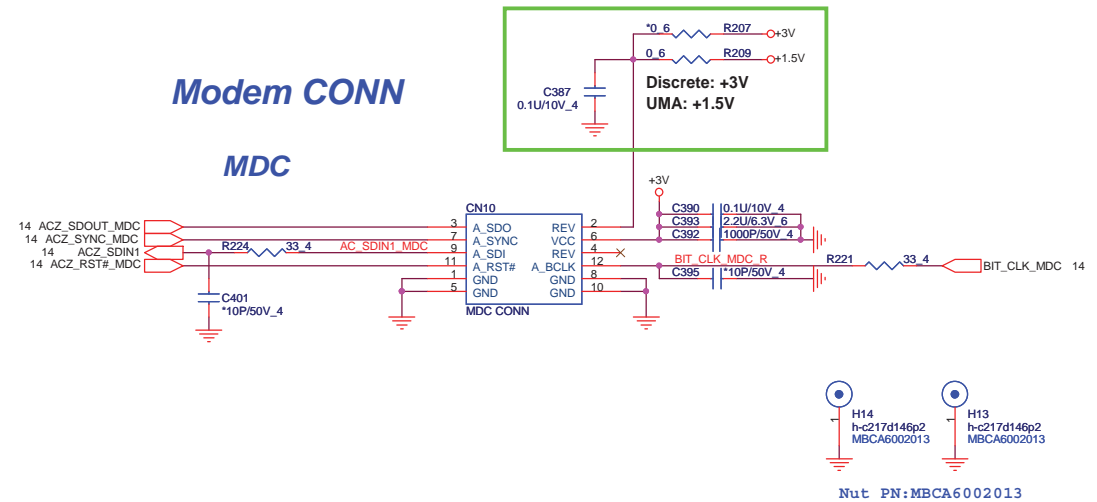


## CAMERA

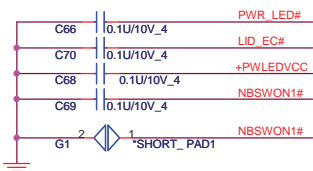
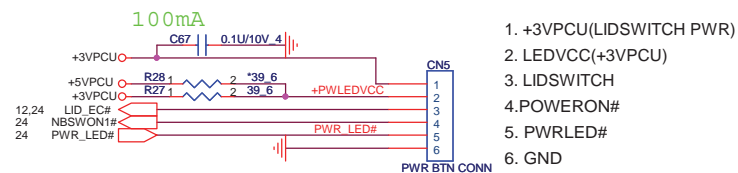
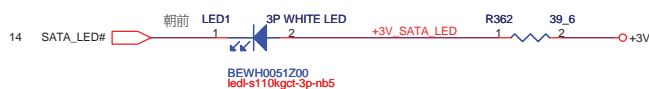


## Modem CONN

### MDC

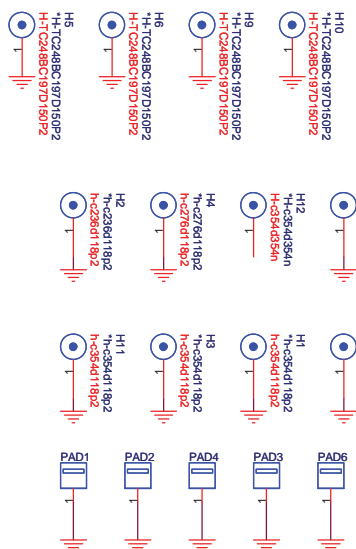


## POWER BOTTON CONNECT

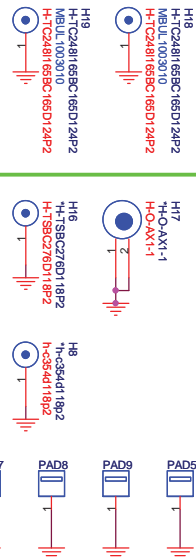
**SATA LED**

## HOLE & PAD

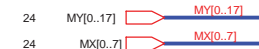
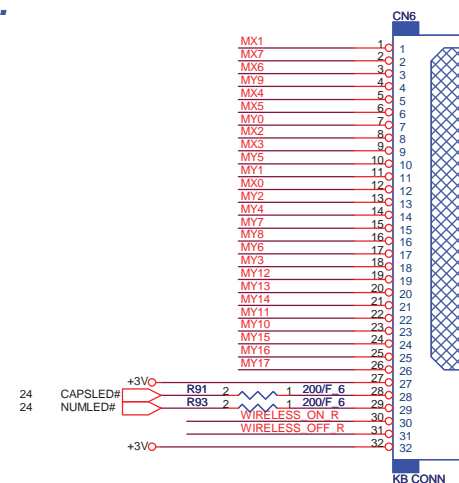
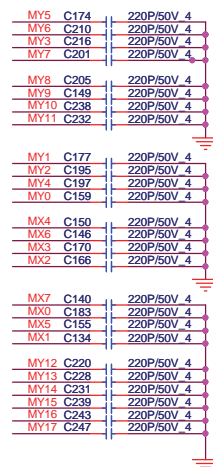
## CPU



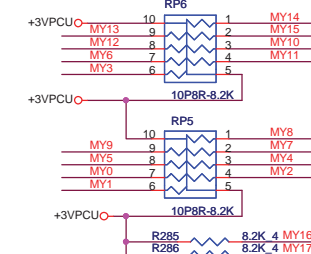
## MCH



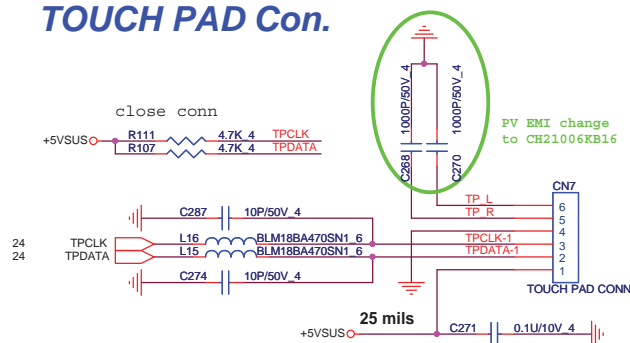
## KEYBOARD Con.



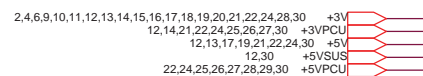
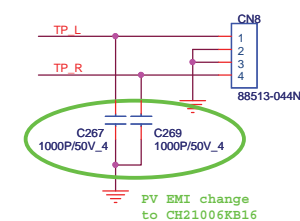
## KEYBOARD PULL-UP



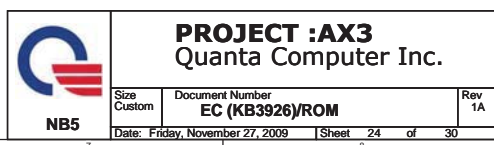
**TOUCH PAD Con.**

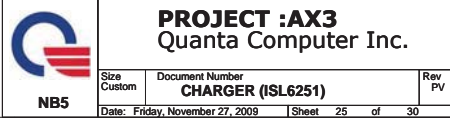


**To TOUCH PAD SW board**



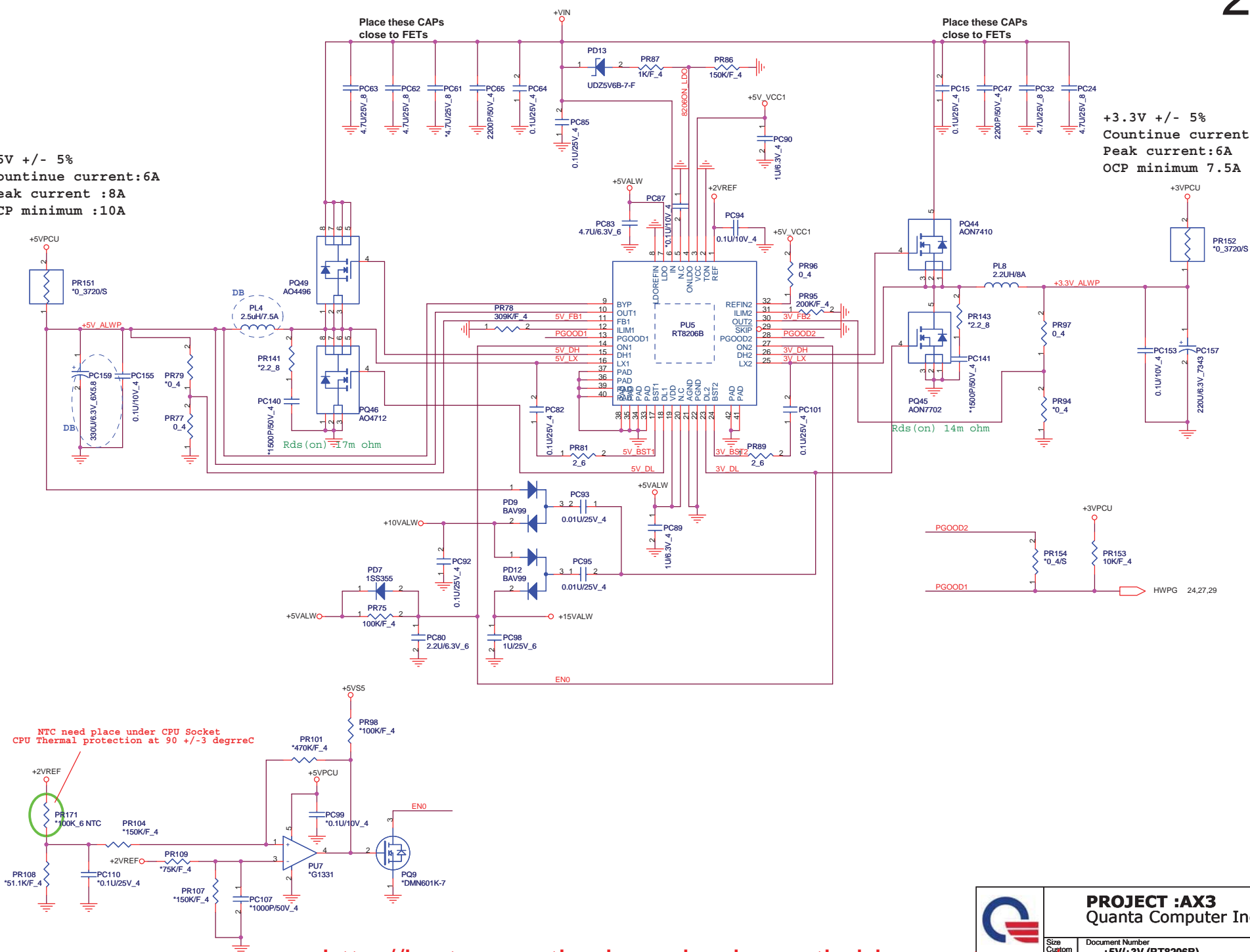


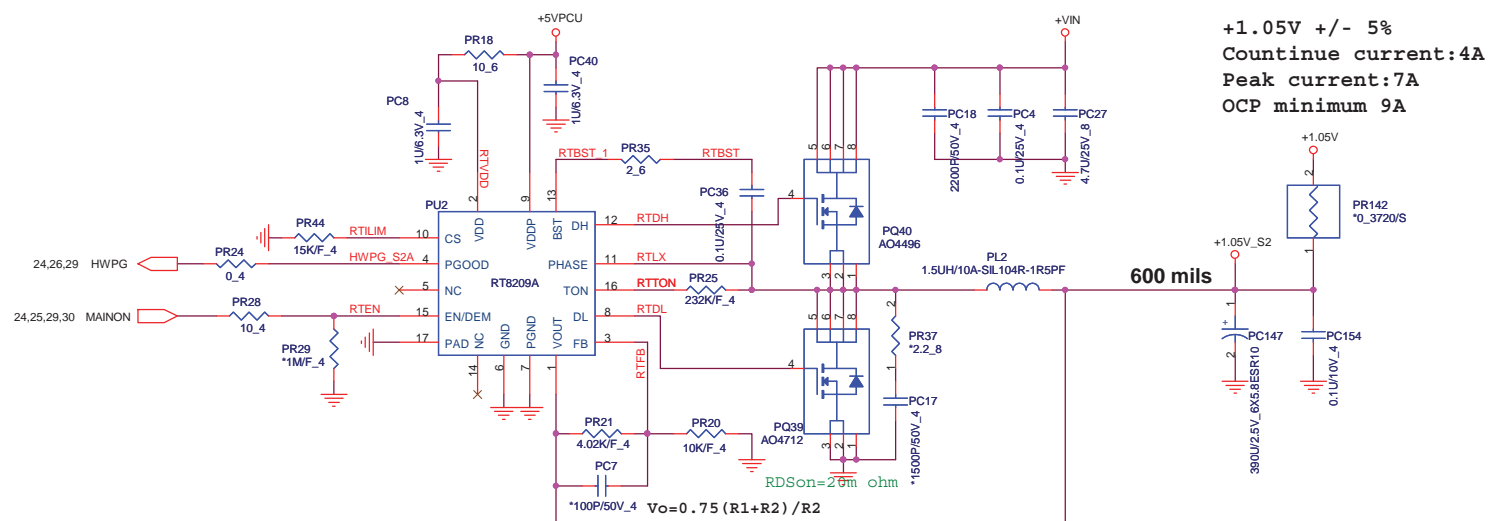




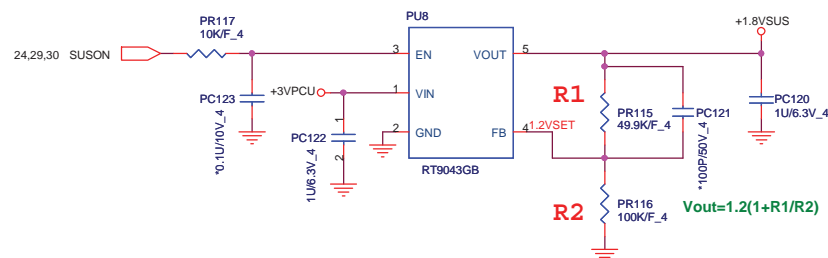
+5V +/- 5%  
Countinue current:6A  
Peak current :8A  
OCP minimum :10A

+3.3V +/- 5%  
Countinue current:5A  
Peak current:6A  
OCP minimum 7.5A





**+1.8 Volt +/- 5%**  
**Countinue current:150mA**  
**Peak current: 600mA**



**PROJECT :AX3**  
**Quanta Computer Inc.**

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