

Appendix B:Schematic Diagrams

This appendix has circuit diagrams of the *C5505/C5505Q/C5505C/C5505Q-C* notebook's PCB's. The following table indicates where to find the appropriate schematic diagram.

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Table B - 1
**Schematic
Diagrams**

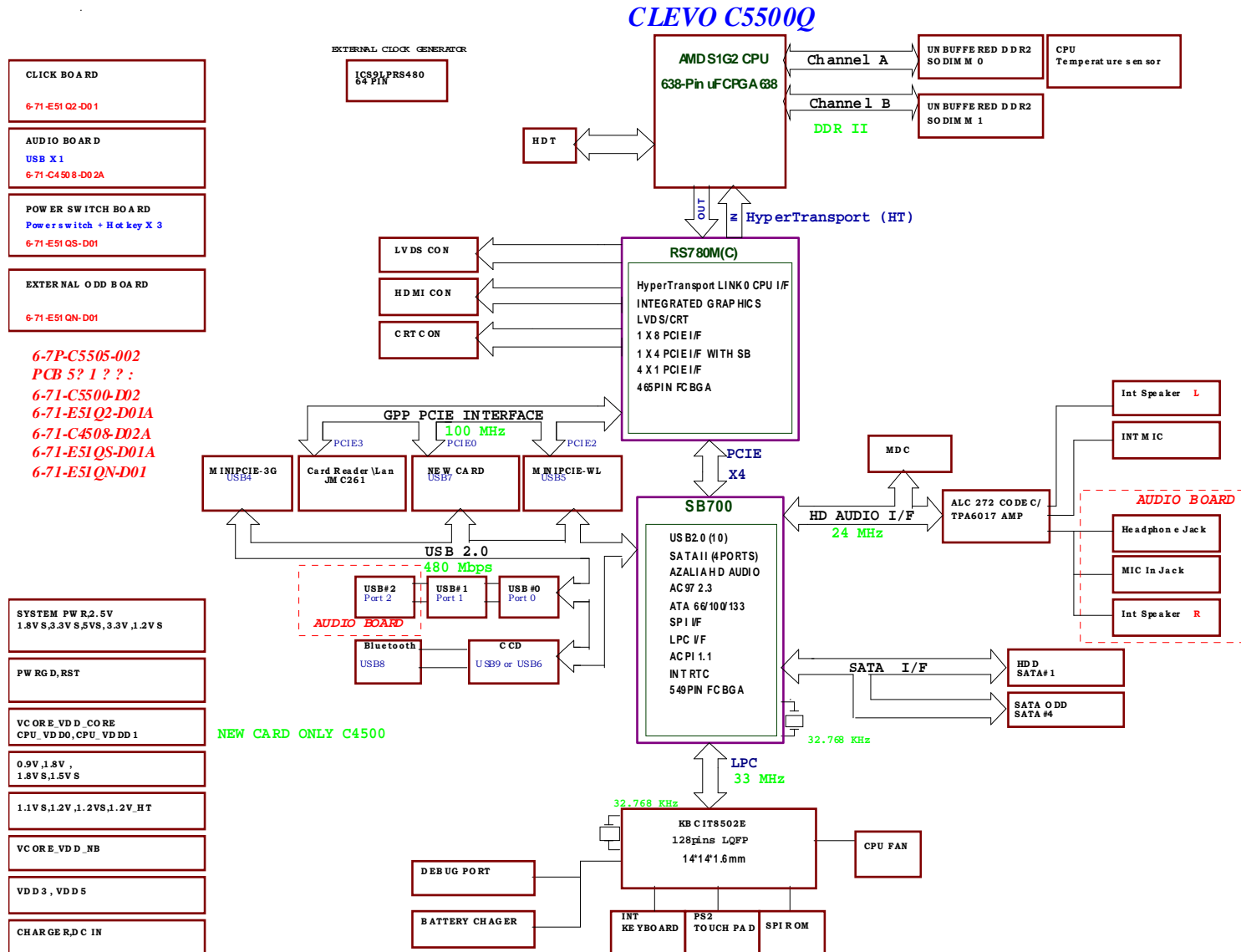


Version Note

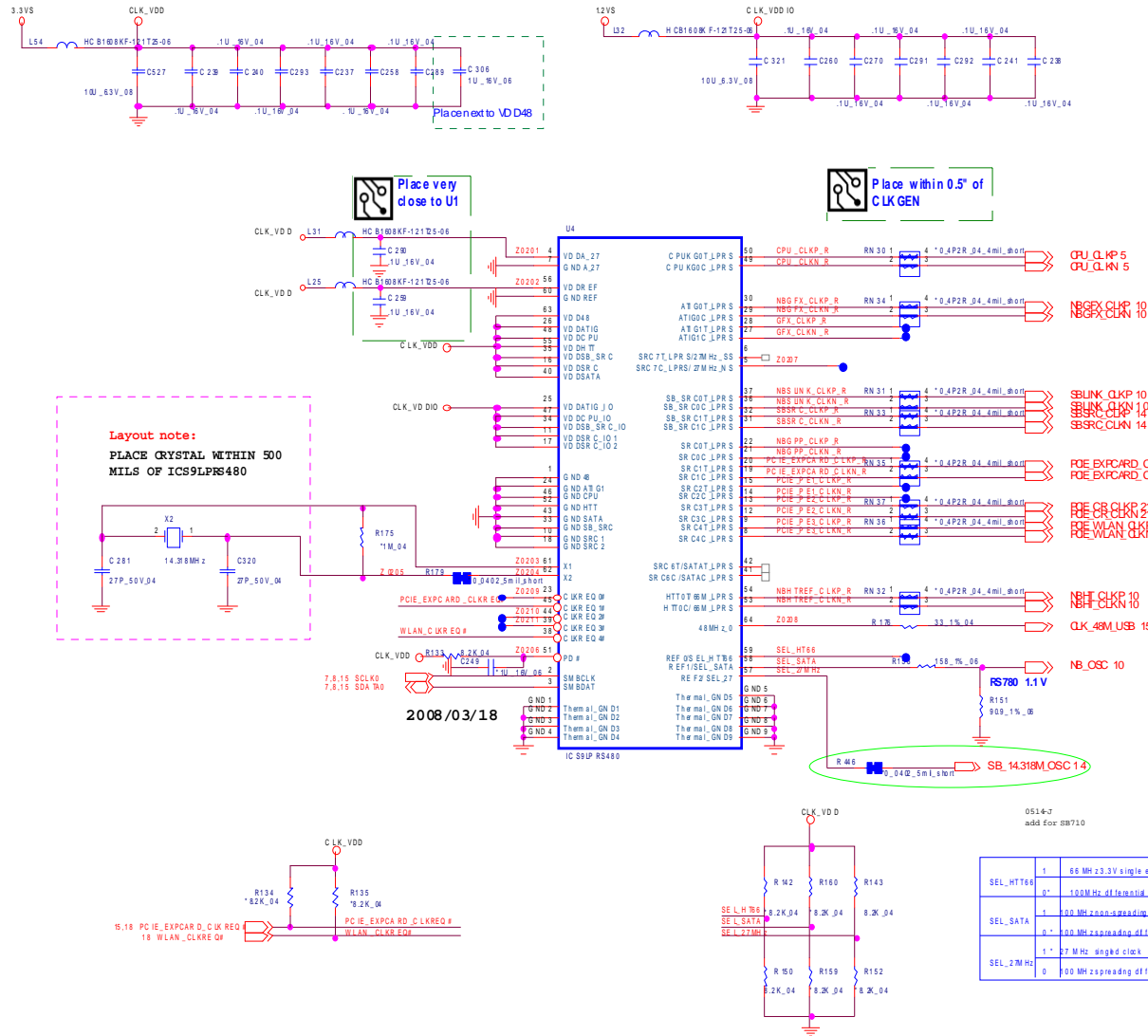
The schematic diagrams in this chapter are based upon version 6-7P-C5505-002. If your mainboard (or other boards) are a later version, please check with the Service Center for updated diagrams (if required).

System Block Diagram

Sheet 1 of 35
System Block
Diagram



Clock Generator



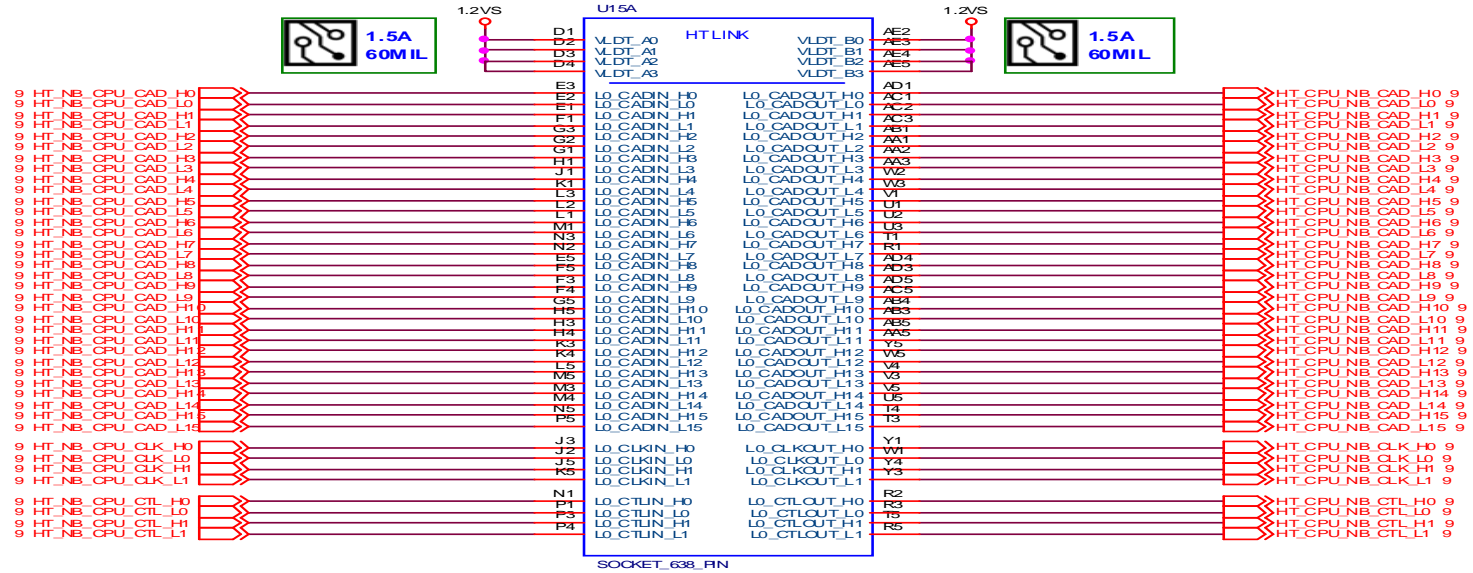
Sheet 2 of 35
Clock Generator

Clock chip has internal series terminations for differential pairs, external resistors are reserved for debug purpose.

B. Schematic Diagrams

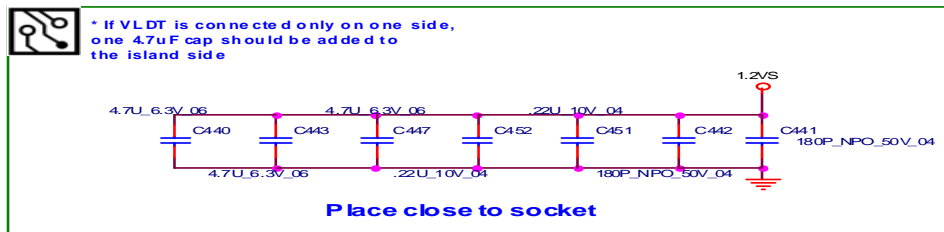
Schematic Diagrams

CPU-1

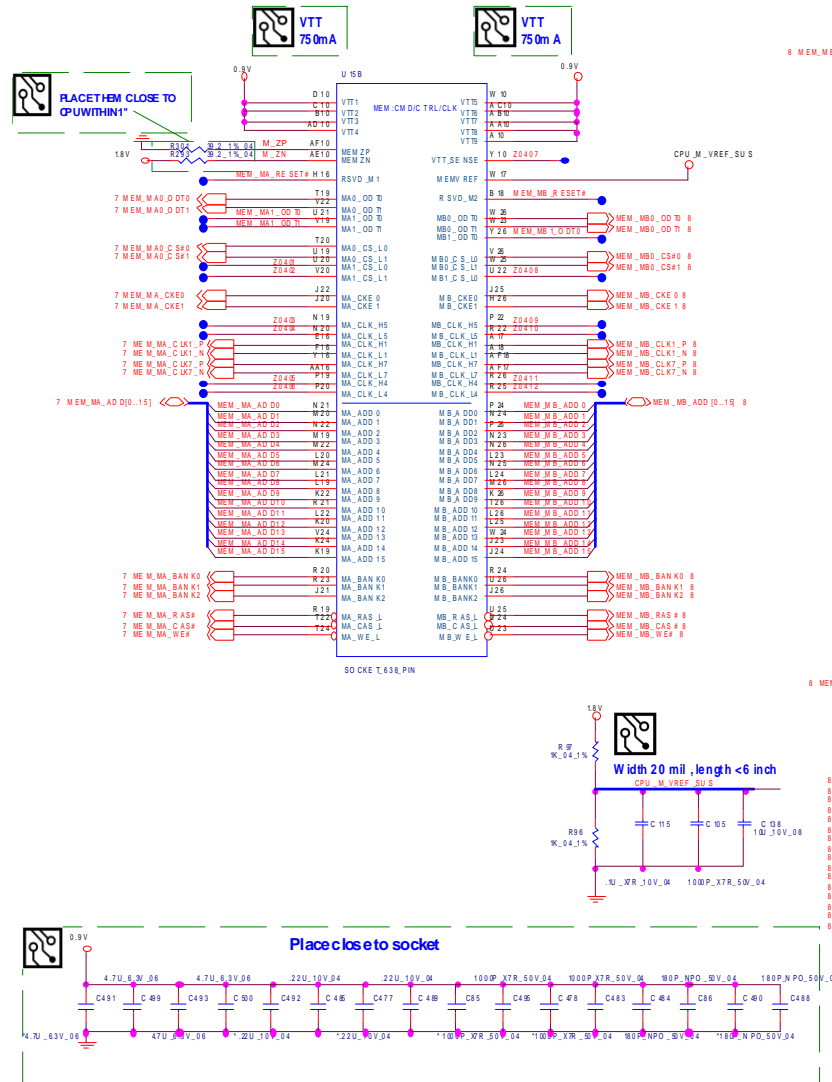


Sheet 3 of 35
CPU-1

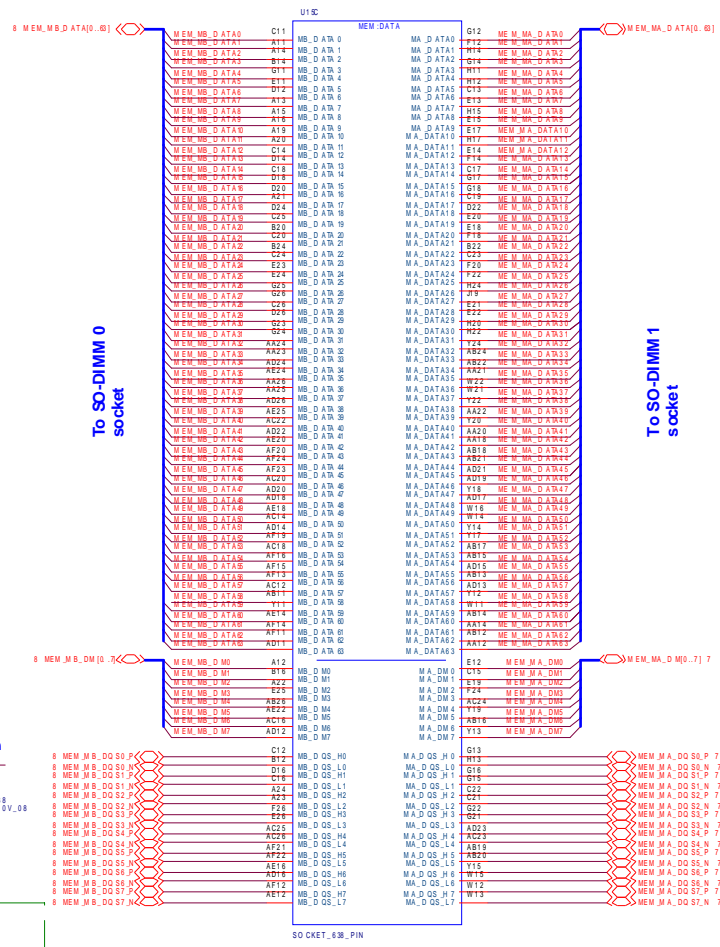
B.Schematic Diagrams



CPU-2



Processor Memory Interface

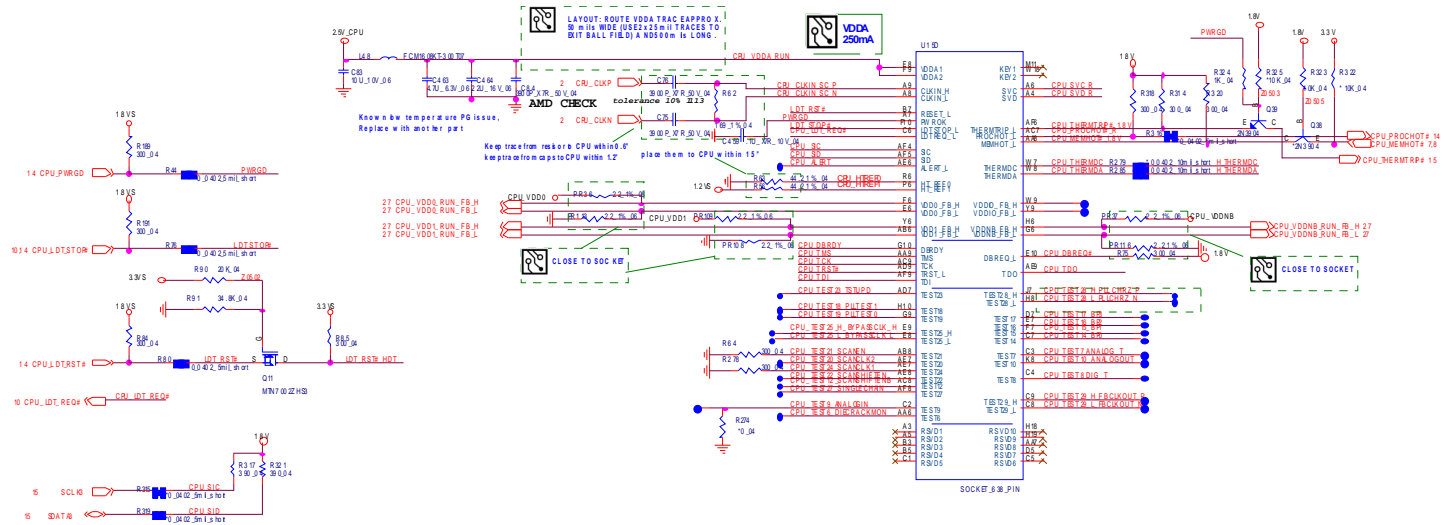


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CPU-2

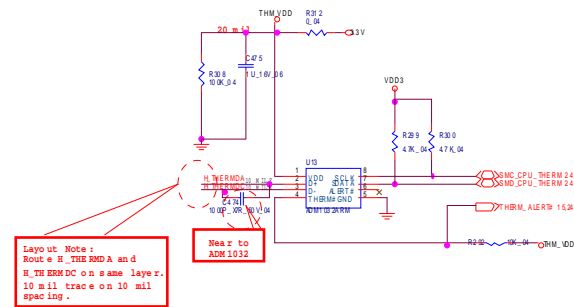
B.Schematic Diagrams

CPU-3

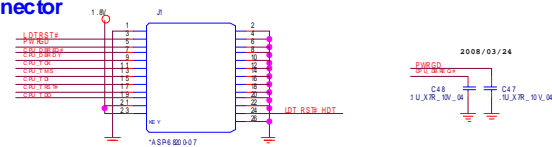
Sheet 5 of 35
CPU-3



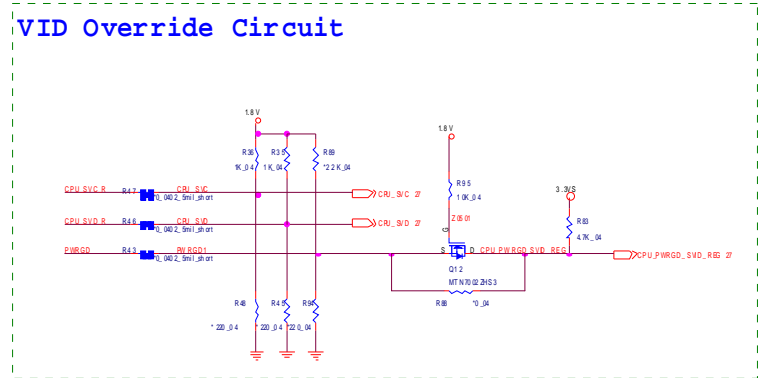
Thermal IC



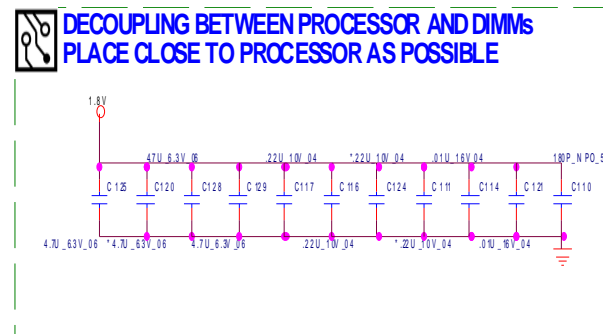
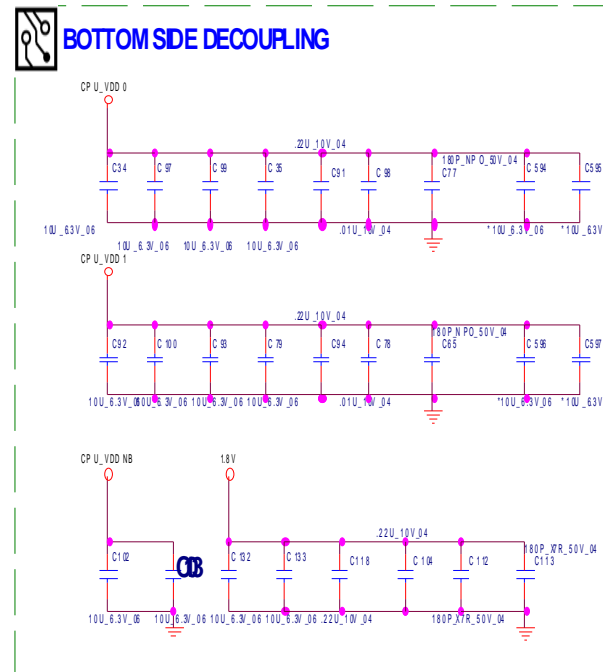
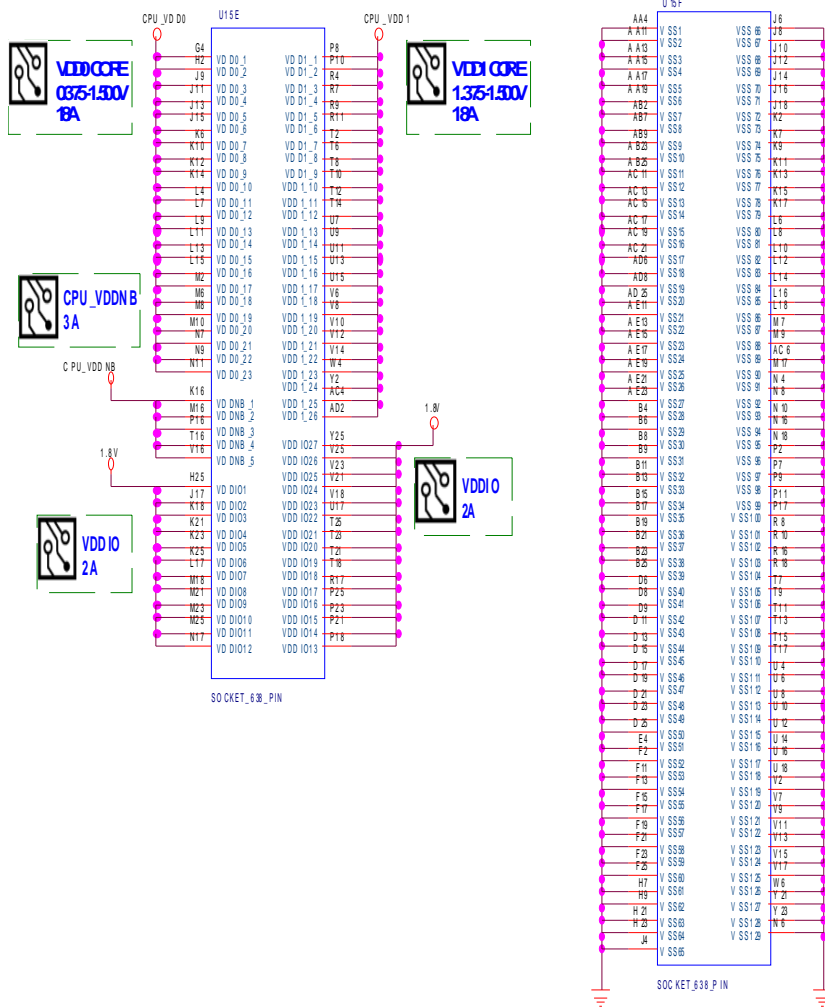
HDT Connector



VID Override Circuit



CPU-4

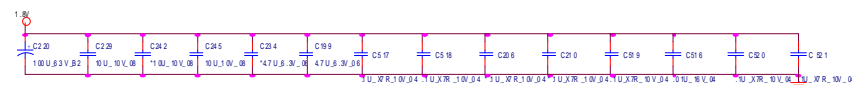
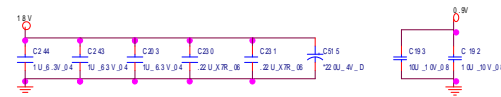
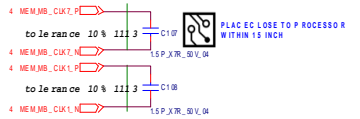
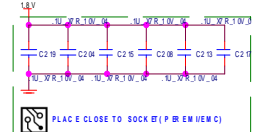
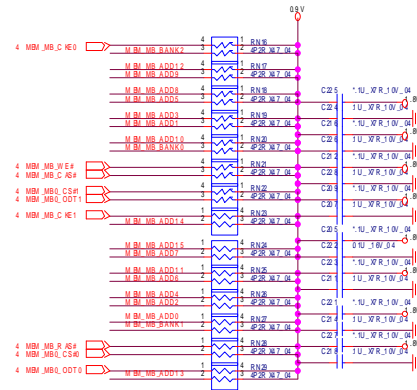
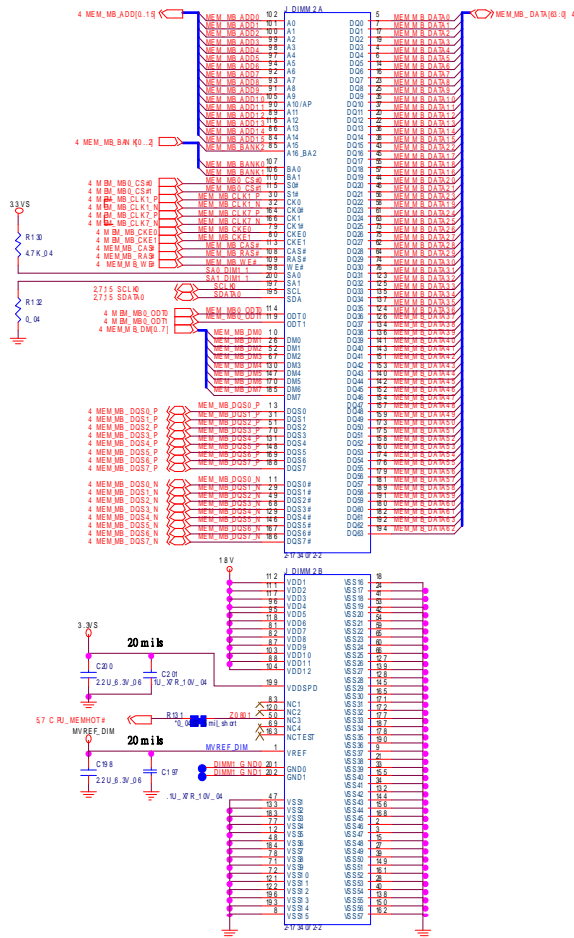


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CPU-4

B. Schematic Diagrams

DDRII SO-DIMM_1

SO-DIMM 1



B. Schematic Diagrams

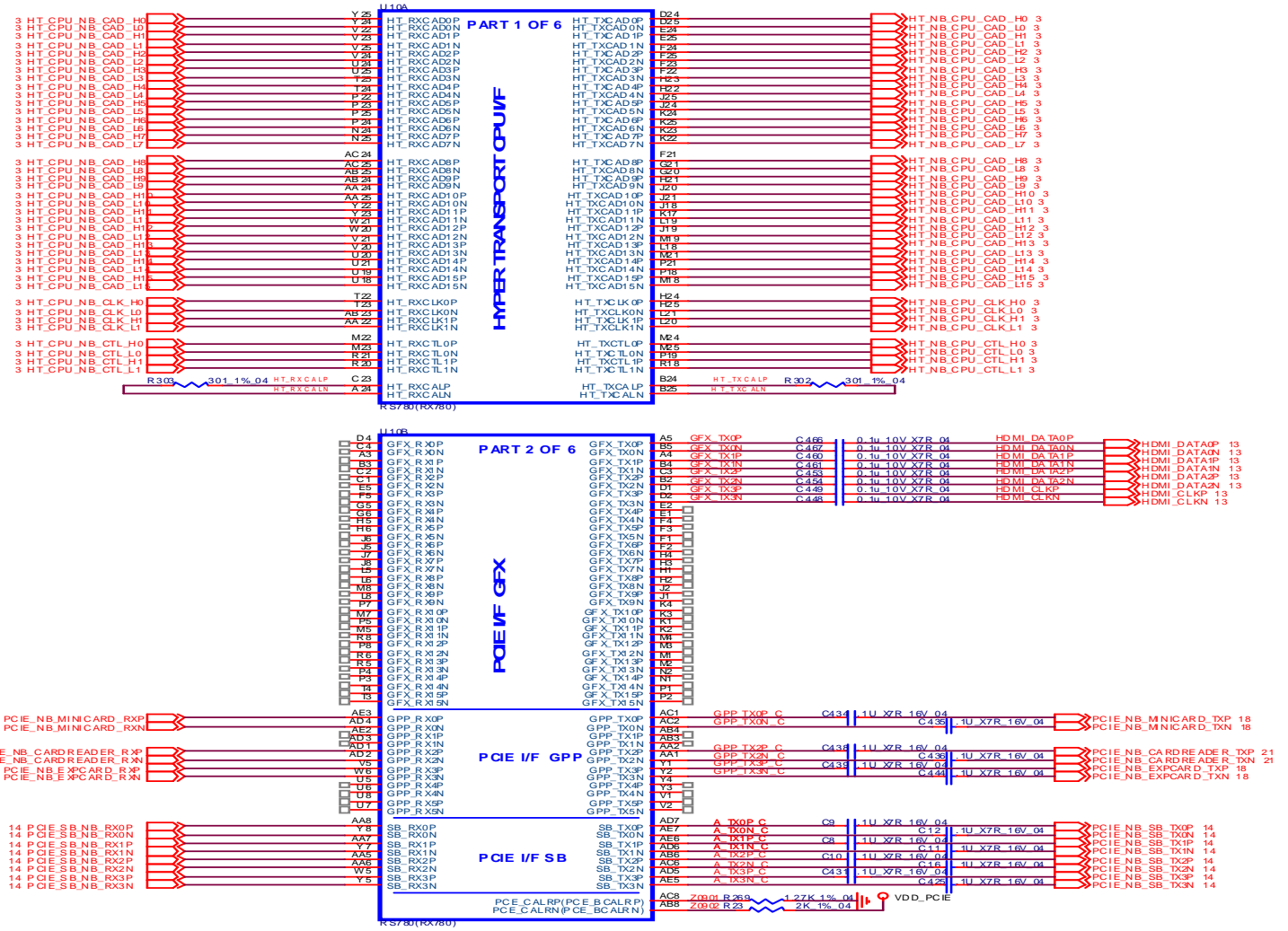
Sheet 8 of 35
DDRII SO-DIMM_1

Schematic Diagrams

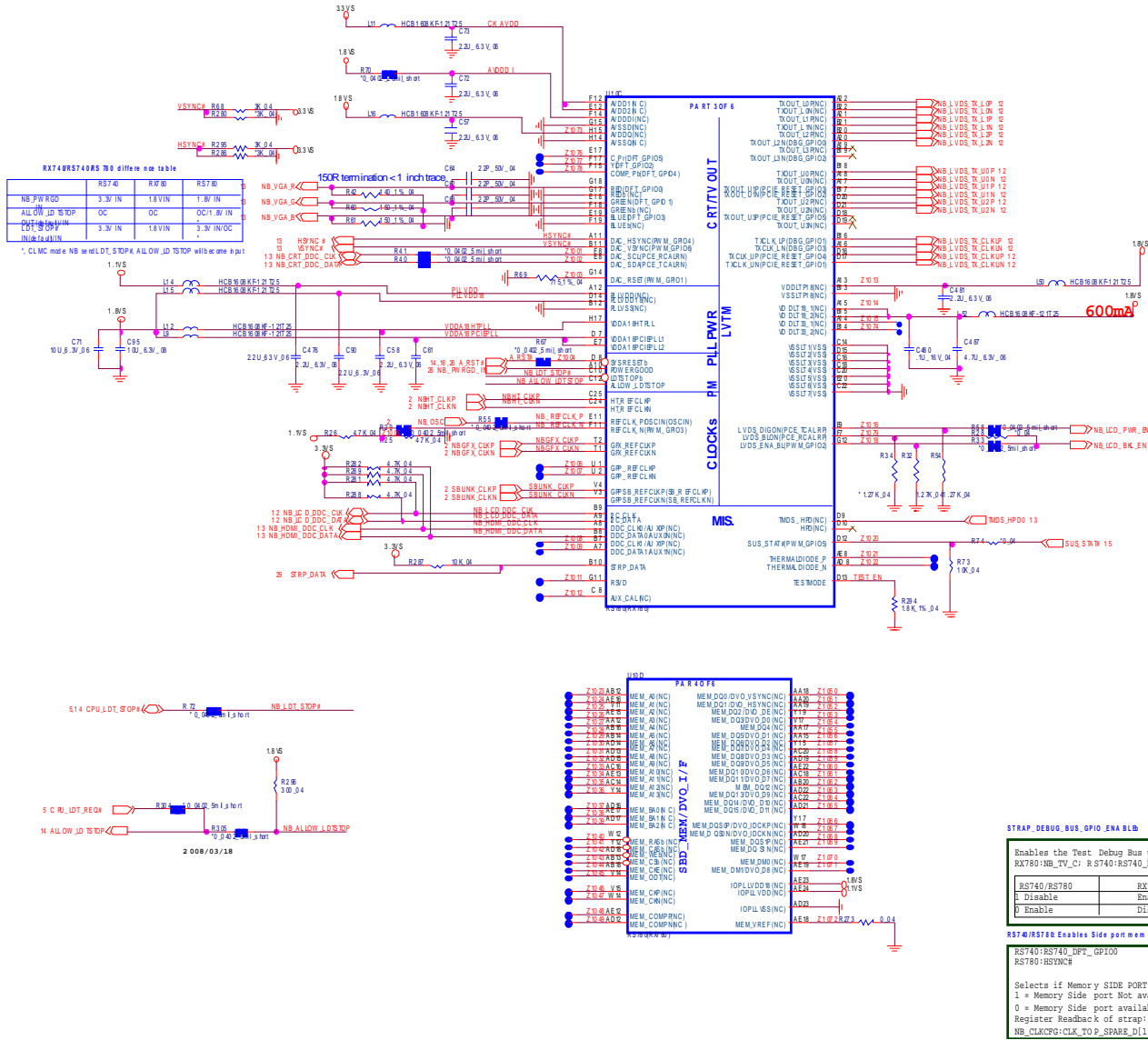
RS780M-1

B.Schematic Diagrams

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RS780M-1



RS780M-2



Sheet 10 of 35
RS780M-2

B.Schematic Diagrams

STRAP_DEBUG_BUS_GPIO_ENA_BLB

Enables the Test Debug Bus using GPIO.
 RX780:NB_TV_C; R S740:RS740_DFT_GPIOS; RS780:VSYNCH

RST40/RS780	RS780
E Disable	Enable
D Enable	Disable

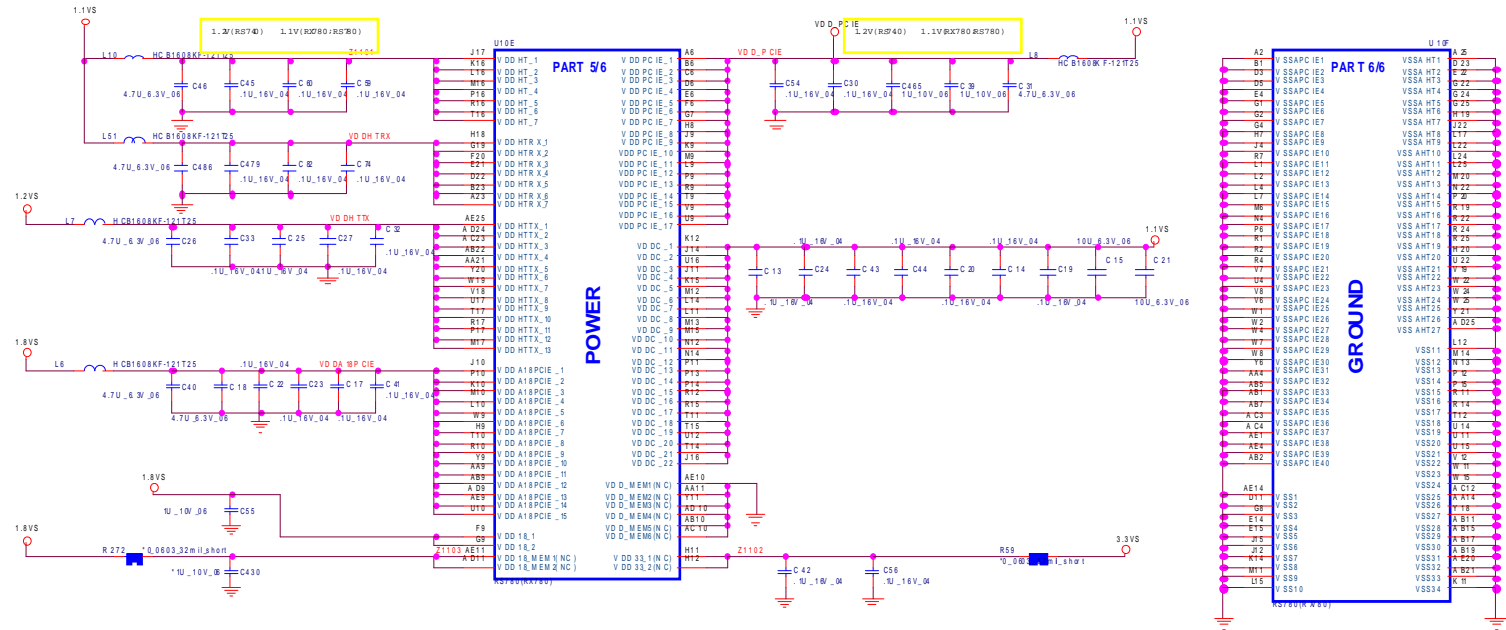
RS740/RS780 Enables Side port mem cry

RS740:RS740_DFT_GPIOS
 RS780:VSYNCH#

Selects if Memory SIDE PORT is available or not
 0 = Memory Side port Not available
 1 = Memory Side port available
 Register Readback of strap:
 NB_CLKCFG_CLK_TO_P_SPARE_B[1]

Schematic Diagrams

RS780M-3



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RS780M-3

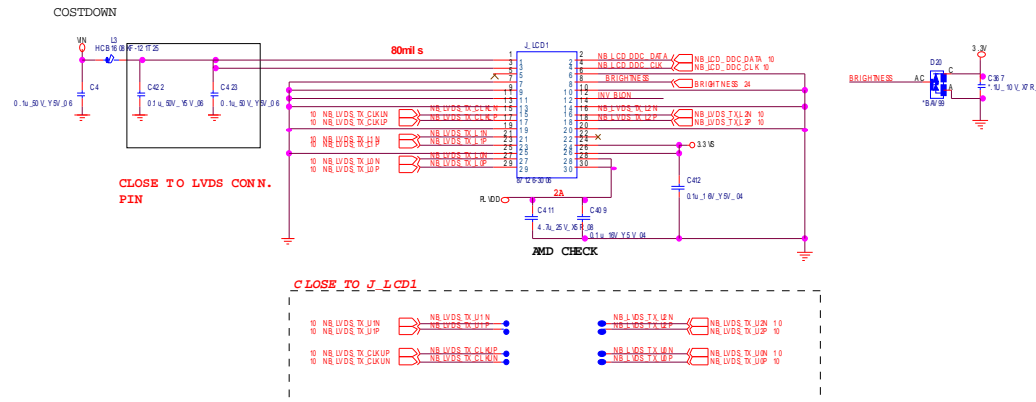
B.Schematic Diagrams

RS740/R X780/RS 780 POWER DIFFERENCE TABLE

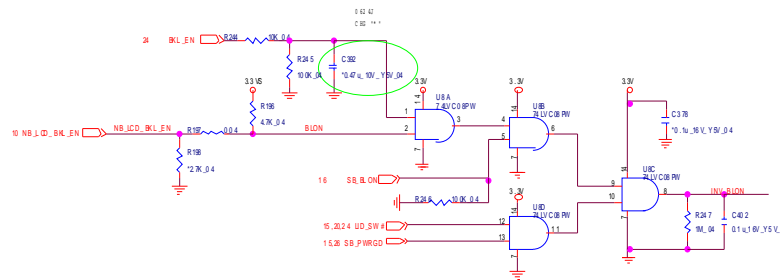
PIN NAME	RS740	RX780	RS780	PIN NAME	RS740	RX780	RS780
VDD HT	N/C	+1.1V	+1.1V	IOPLLVD	+1.2V	N/C	+1.1V
VDD HTRX	N/C	+1.1V	+1.1V	AVDD	+3.3V	N/C	+3.3V
VDD HTX	+1.2V	+1.2V	+1.2V	AVDD I	+1.8V	N/C	+1.8V
VDD A18PCIE	N/C	+1.8V	+1.8V	AVDD Q	+1.8V	N/C	+1.8V
VDD G18	+1.8V	+1.8V	+1.8V	PLLVD	+1.2V	N/C	+1.1V
VDD B_MEM	N/C	N/C	+1.8V	PLLVD T8	+1.8V	N/C	+1.8V
VDD PCIE	+1.2V	+1.1V	+1.1V	VDDA18PCIEPLL	+1.2V	+1.8V	+1.8V
VDD C	+1.2V	+1.1V	+1.1V	VDDA18TP L	+1.8V	+1.8V	+1.8V
VDD MEM	+1.8V/1.5V	N/C	+1.8V/1.5V	VDDLP18	+1.8V	N/C	+1.8V
VDD G33	+3.3V	N/C	+3.3V	VDDLP18	+1.8V	N/C	+1.8V
IOPLLVD T8	+1.8V	N/C	+1.8V	VDD T33	+3.3V	N/C	N/C

LVDS, Inverter

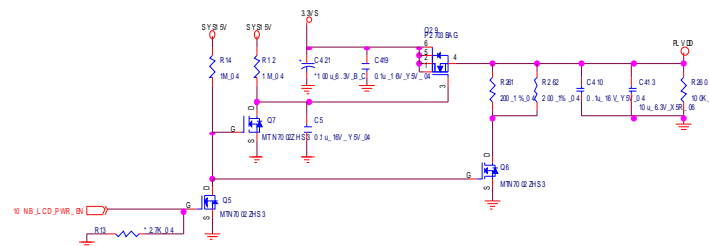
PANEL CONNECTOR



INVERTER CONNECTOR



PANEL POWER

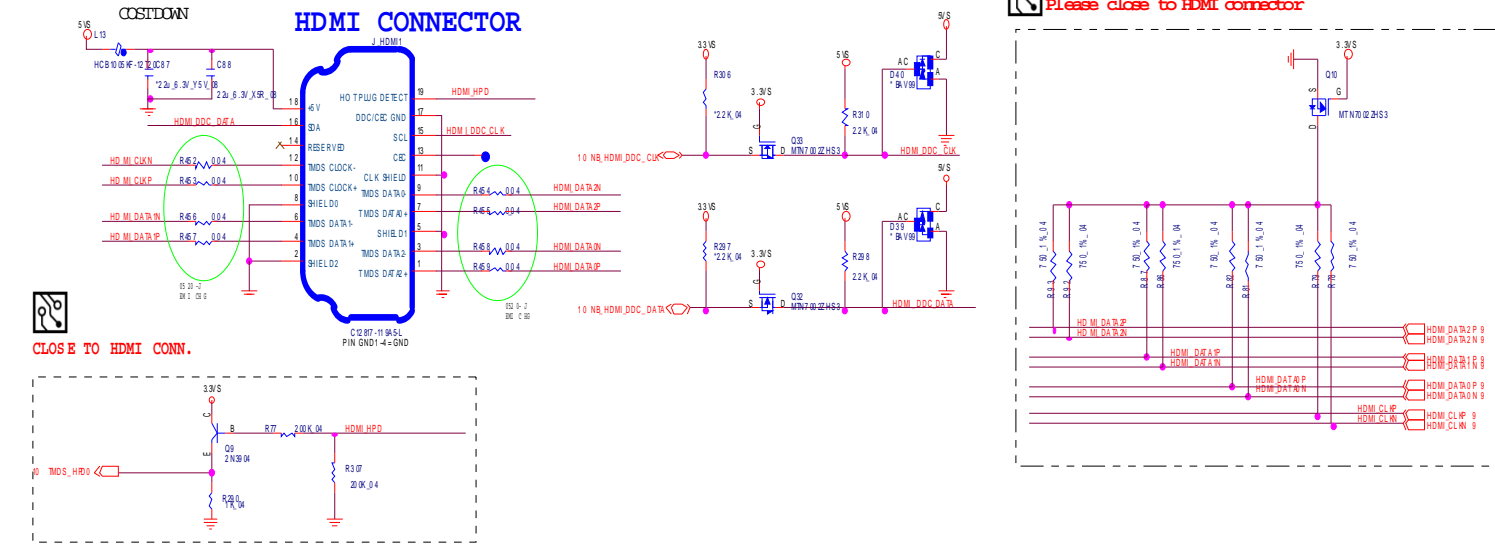


Sheet 12 of 35
LVDS, Inverter

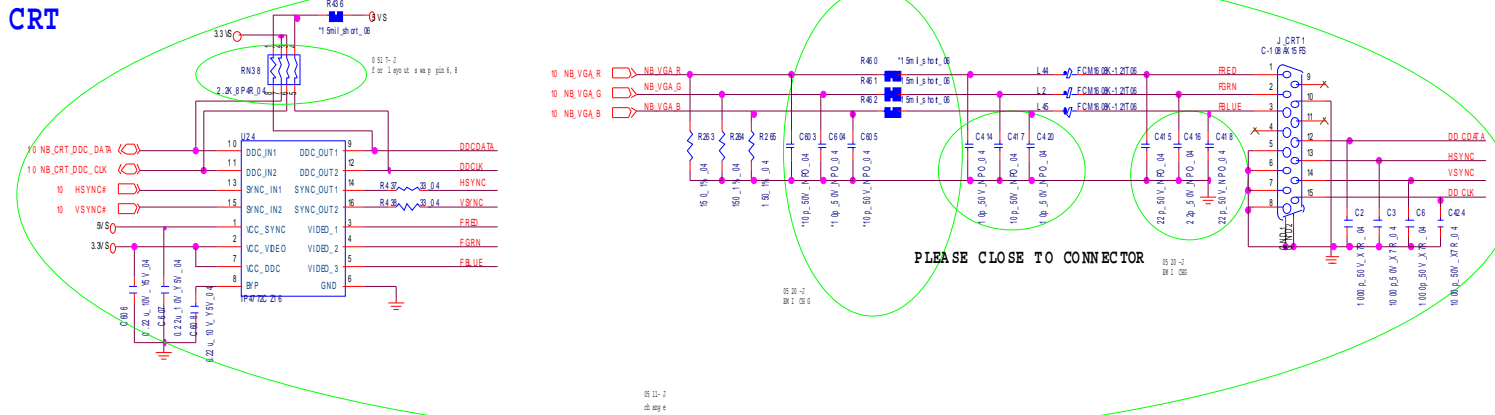
B. Schematic Diagrams

HDMI, CRT

HDMI

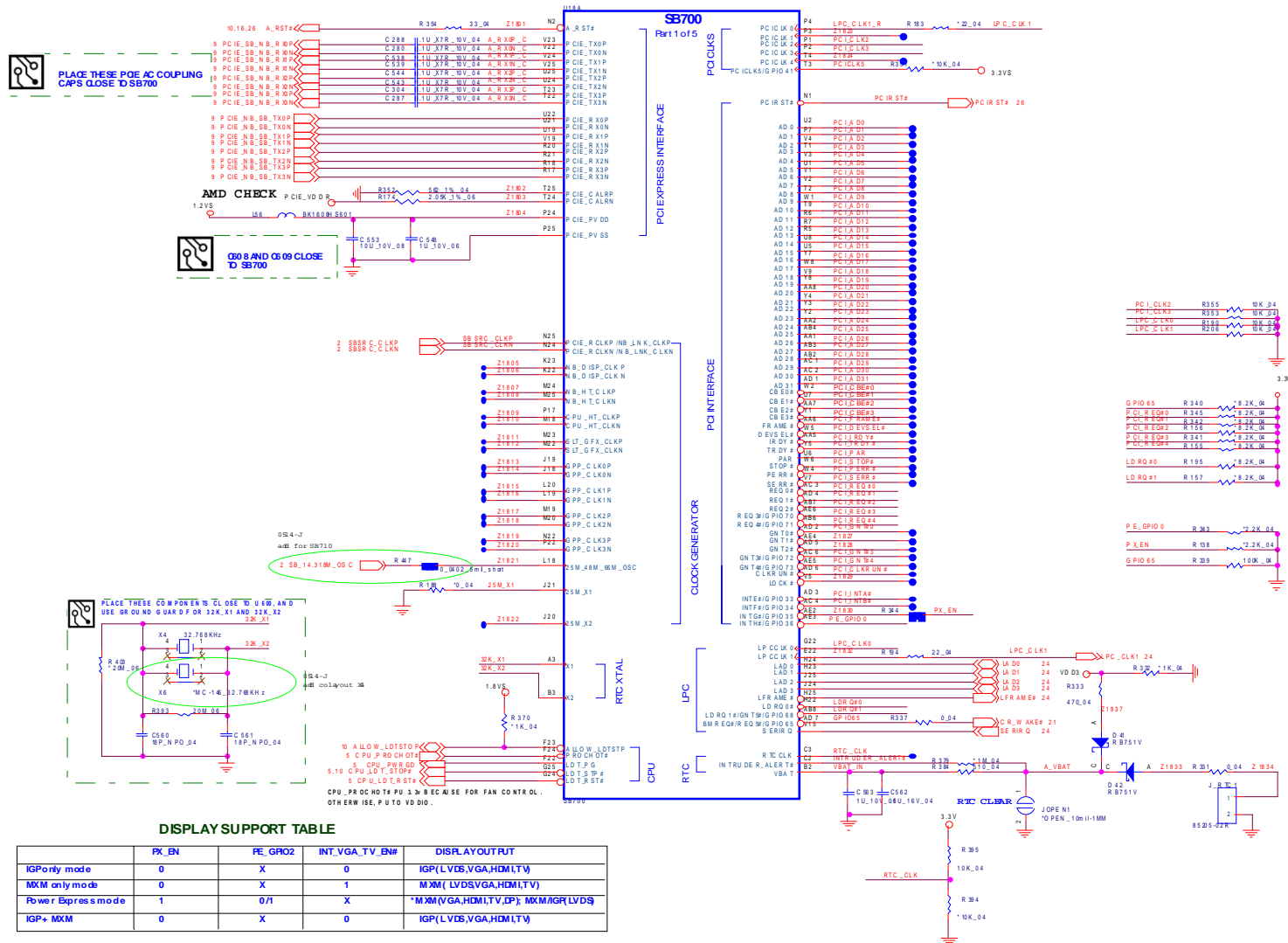


CRT



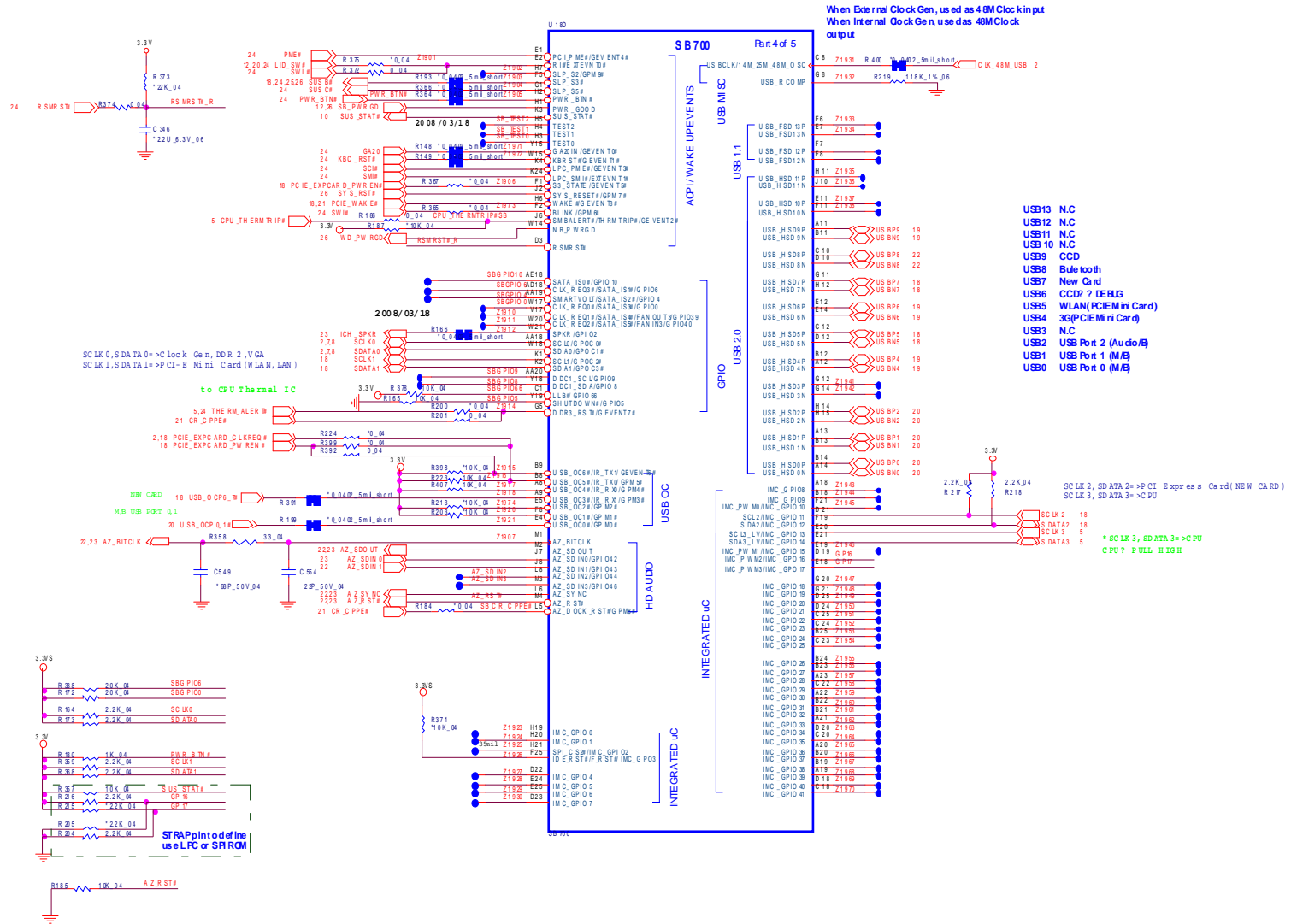
Sheet 13 of 35
HDMI, CRT

SB700-1



Sheet 14 of 35
SB700-1

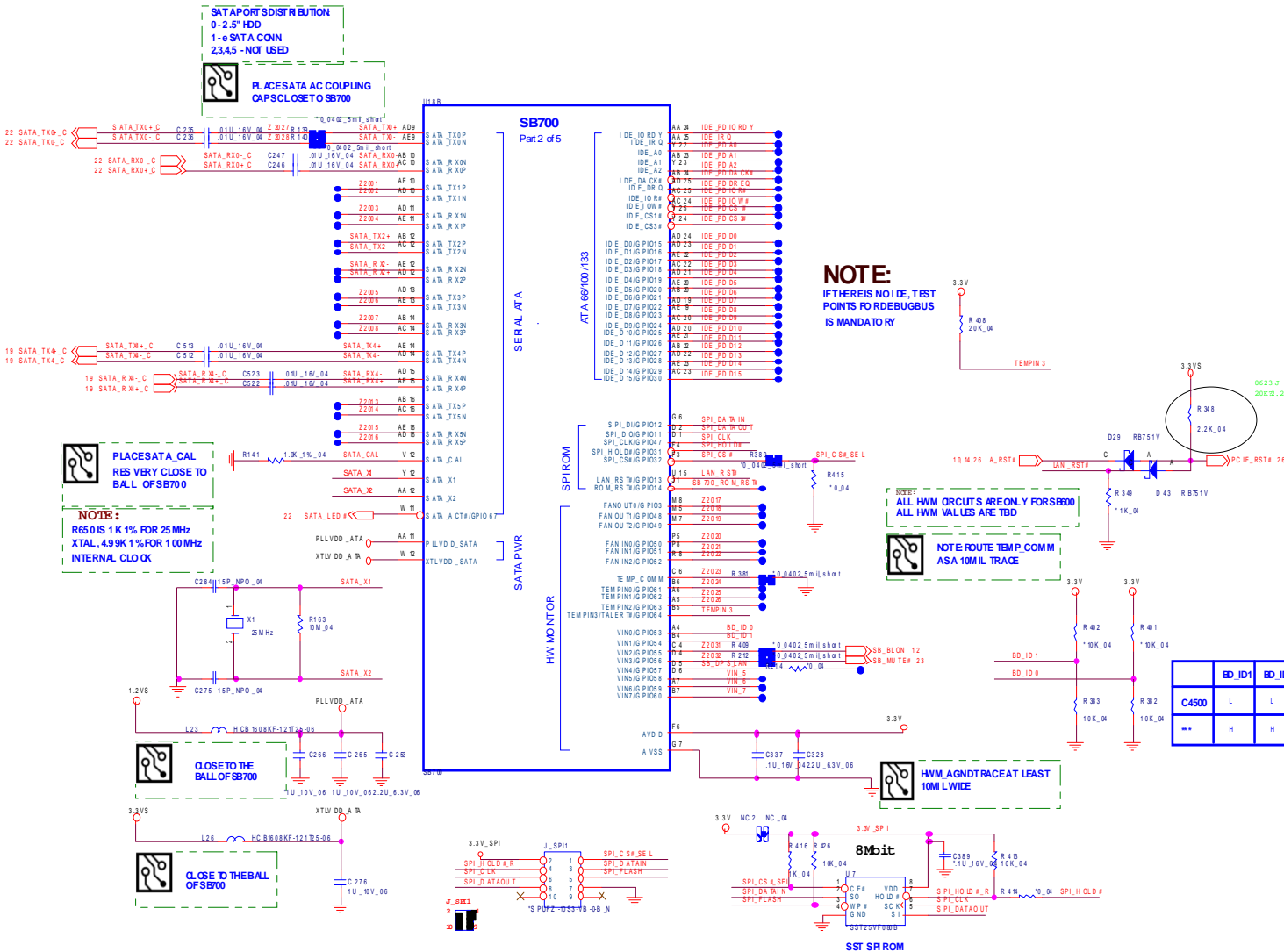
SB700-2



B.Schematic Diagrams

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SB700-2

SB700-3

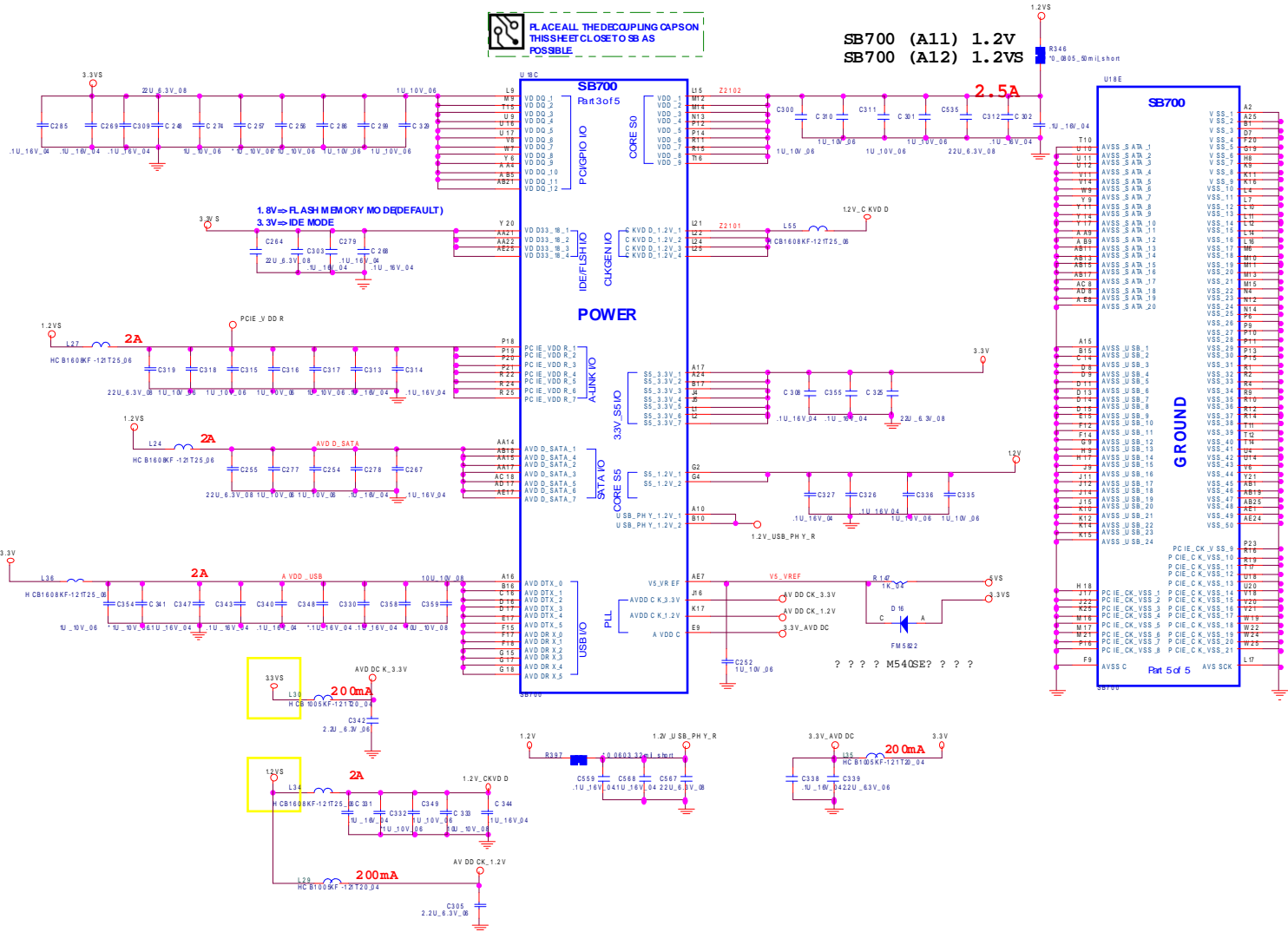


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SB700-3

B. Schematic Diagrams

SB700-4

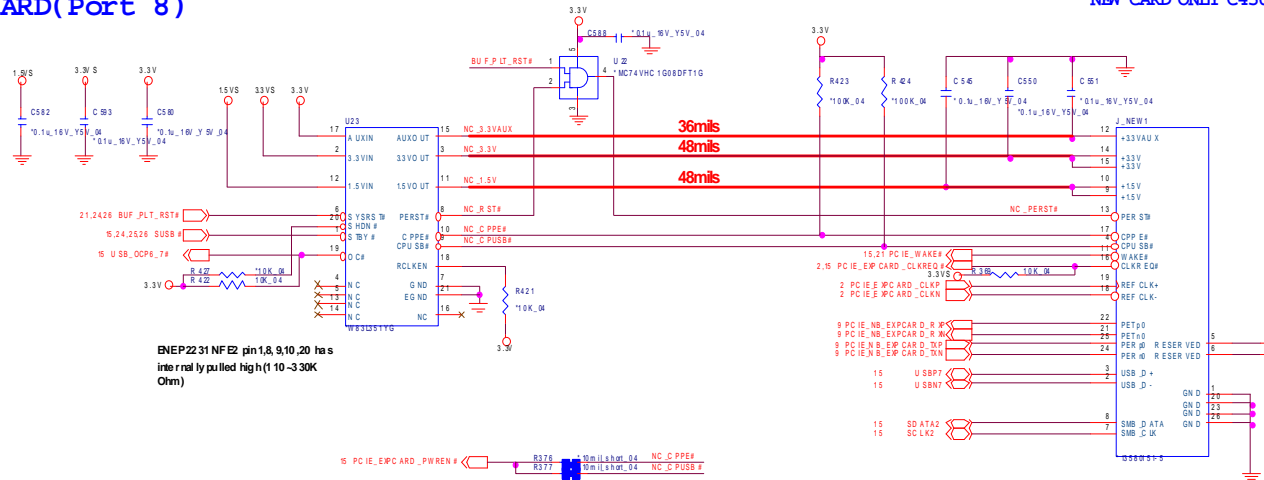
Sheet 17 of 35
SB700-4



New Card, Mini PCIE

NEW CARD (Port 8)

NEW CARD ONLY C4500 SUPPORT



ENE2231 NFE2 pin 1,8, 9,10,20 has internal pulled high(1 10-330K Ohm)

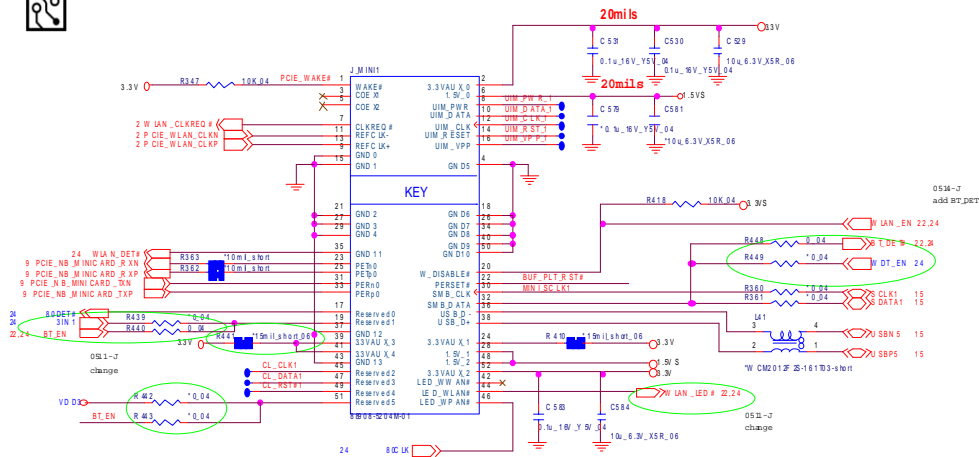
Sheet 18 of 35
New Card, Mini PCIE

B. Schematic Diagrams

MINI CARD (WLAN, Port 5)

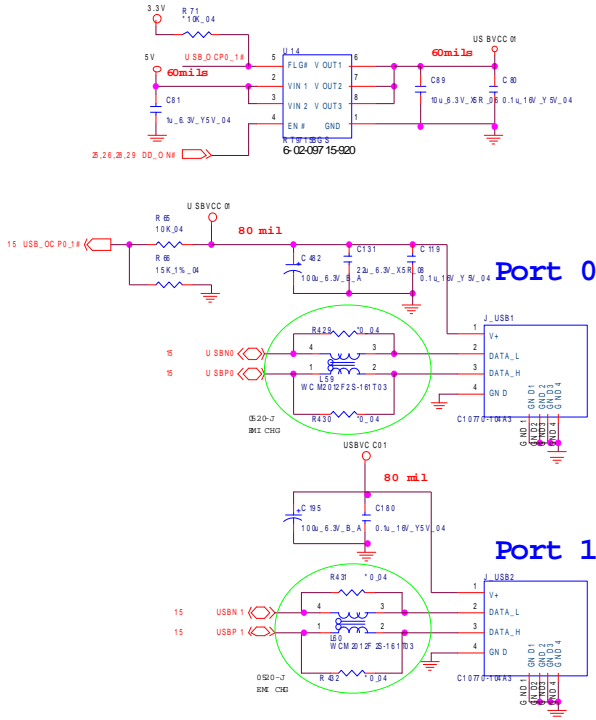


Layout Show 'WLAN(Mmax, 802.11N)' Nb1c

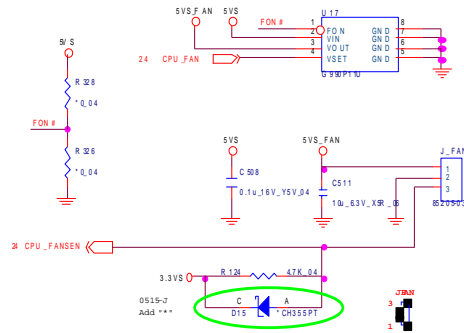


USB, Fan, TP, Multi-Con

USB PORT*2 (Port 0, Port1)

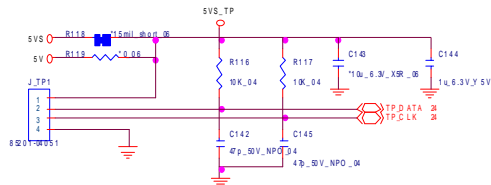


FAN CONTROL



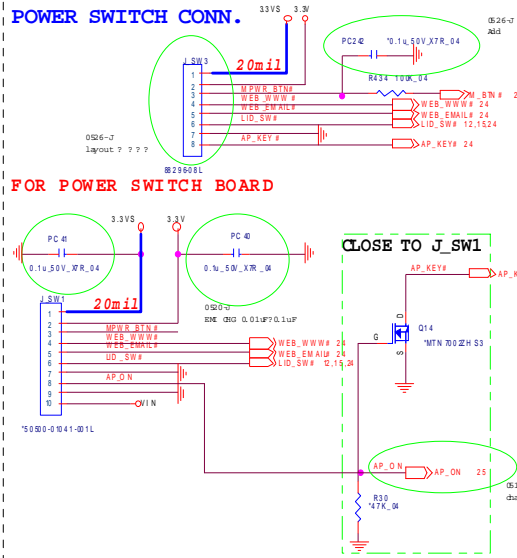
CLICK CONN

FOR CLICK BOARD

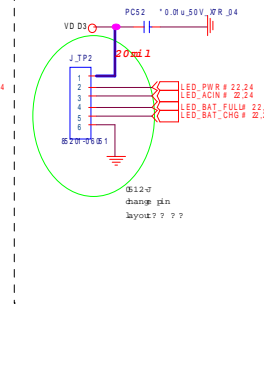


POWER SWITCH CONN.

FOR POWER SWITCH BOARD

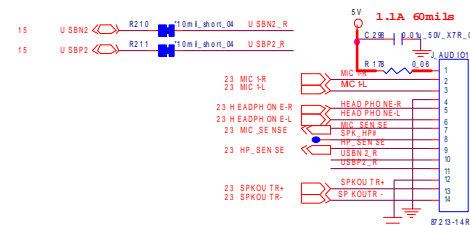


C550q Click/B LED Use



Audio/B CONN. (Port 2)

FOR AUDIO/B

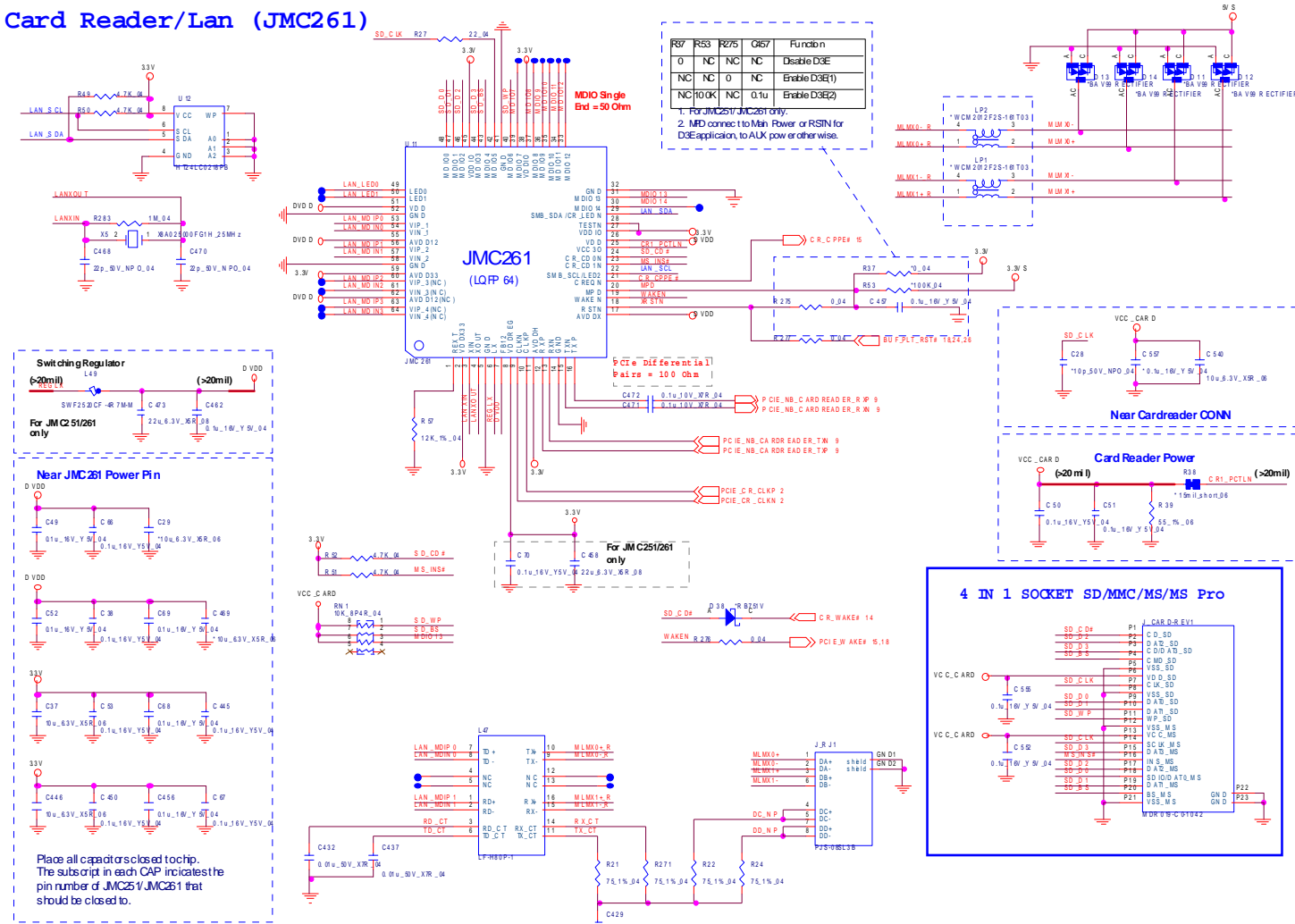


Sheet 20 of 35
USB, Fan, TP,
Multi-Con

B.Schematic Diagrams

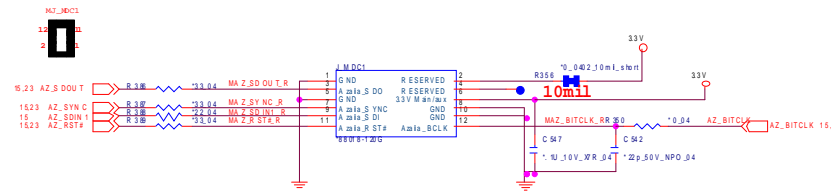
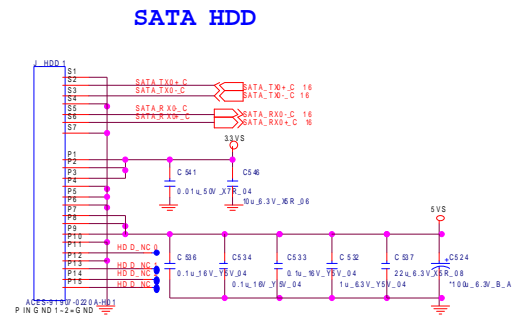
Card Reader/LAN JMC261

Card Reader/Lan (JMC261)

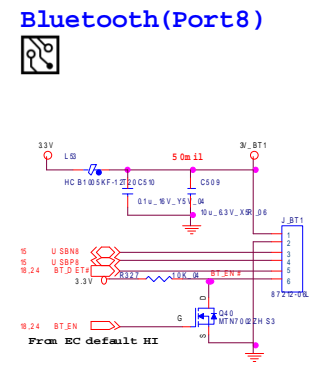
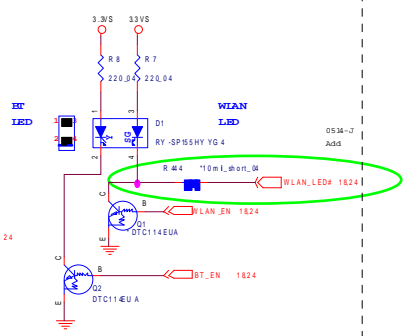
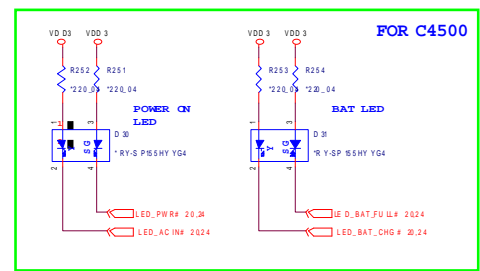
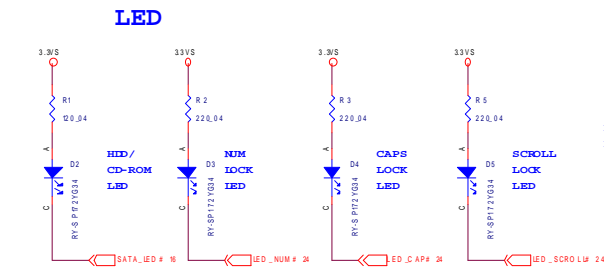


Sheet 21 of 35
Card Reader/LAN
JMC 261

SATA HDD, LED, MDC, BT



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SATA HDD, LED,
MDC, BT

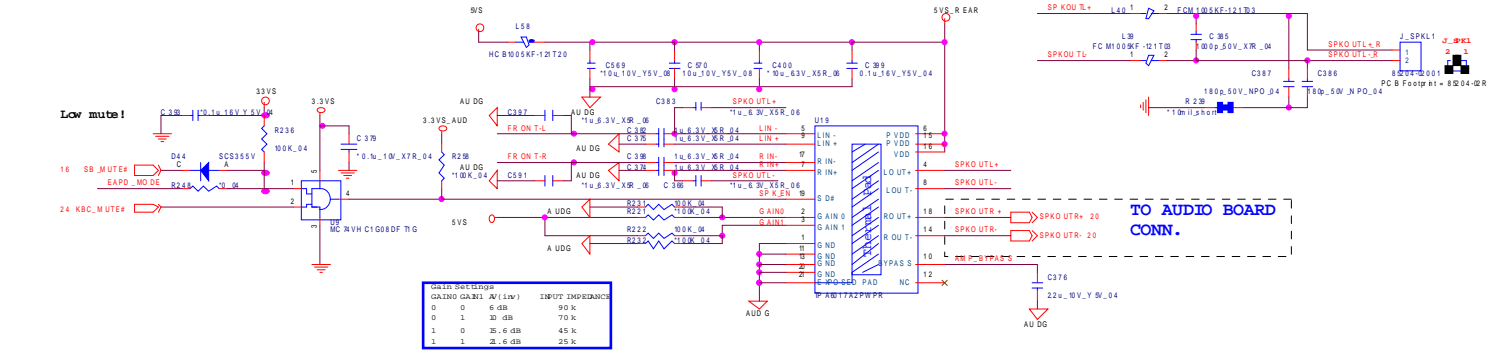
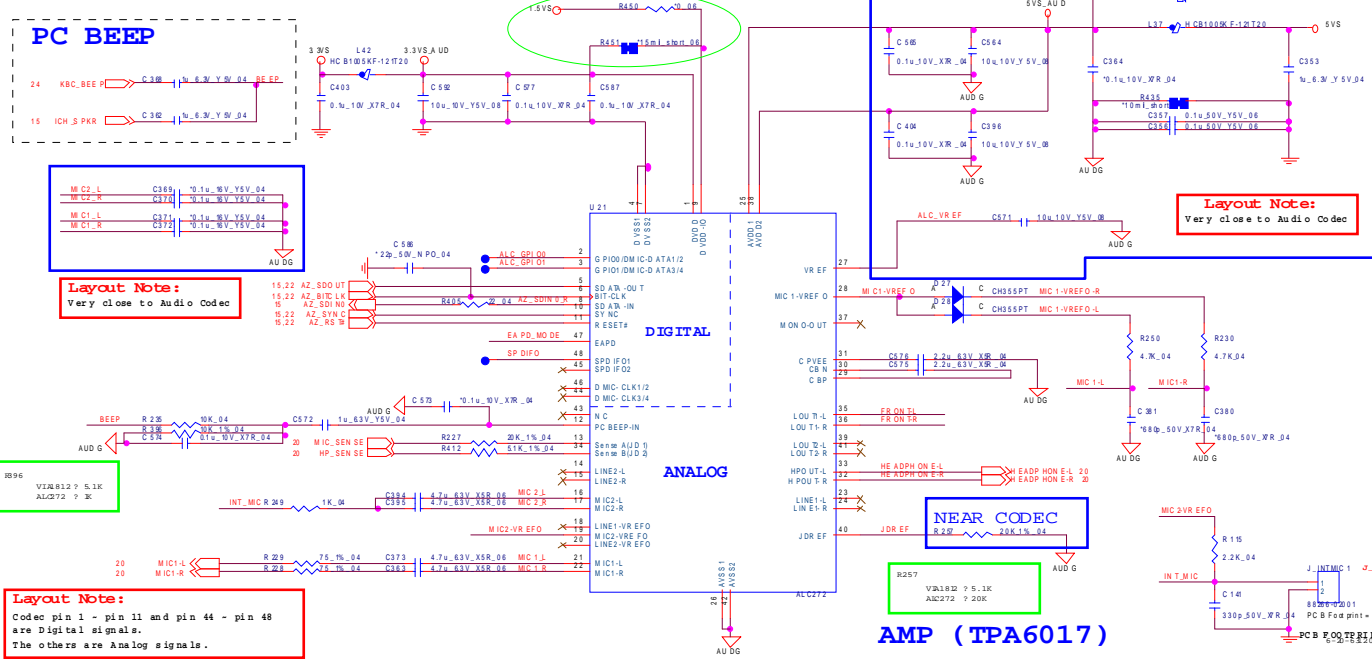


B. Schematic Diagrams

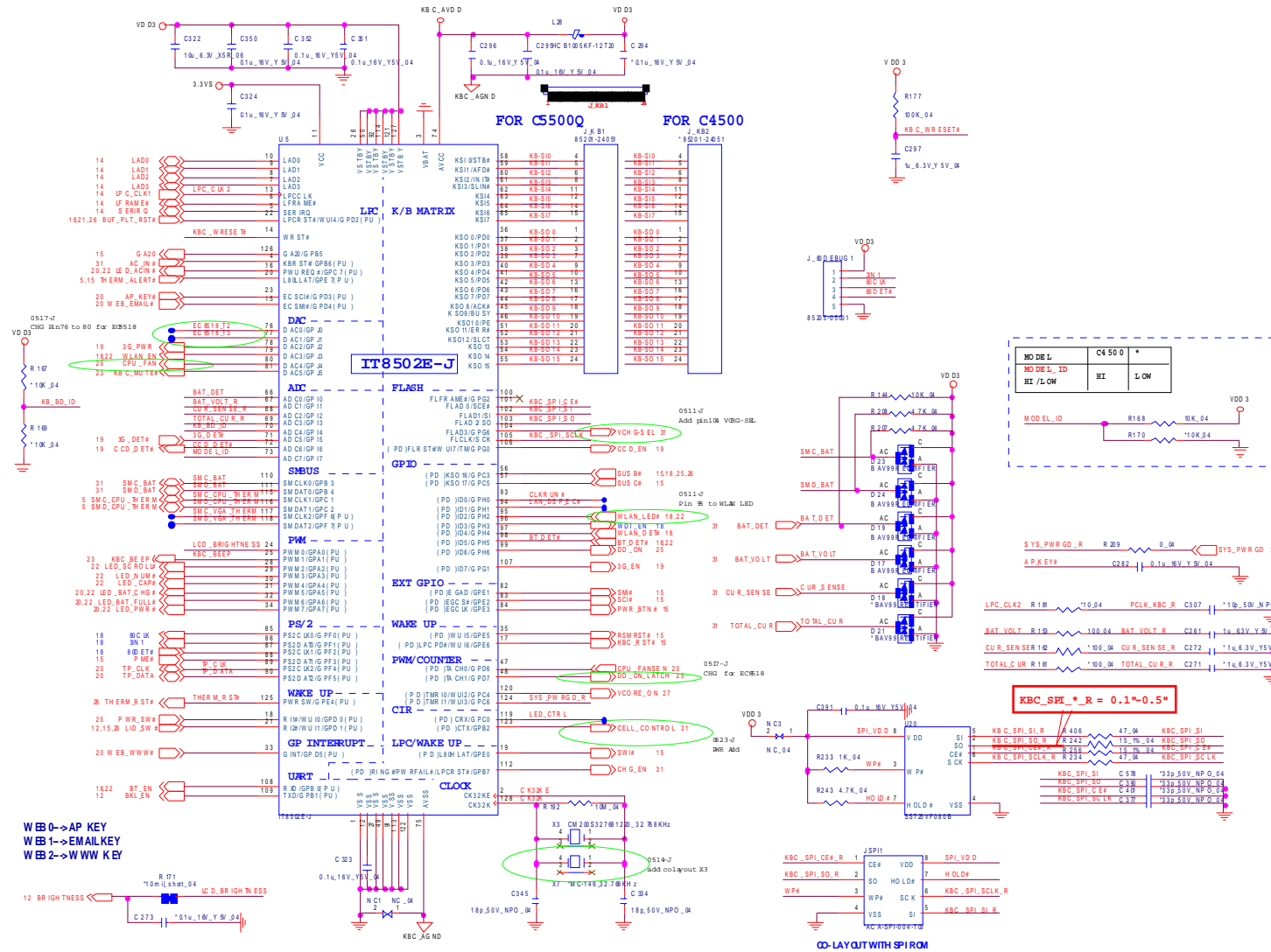
Audio Codec ALC272

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Audio Codec
ALC272

CODEC (ALC272-GR)

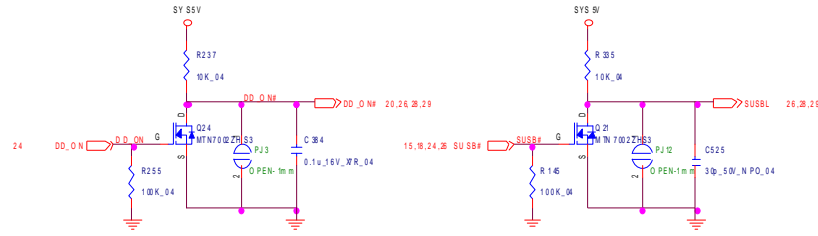


KBC ITE IT8502E



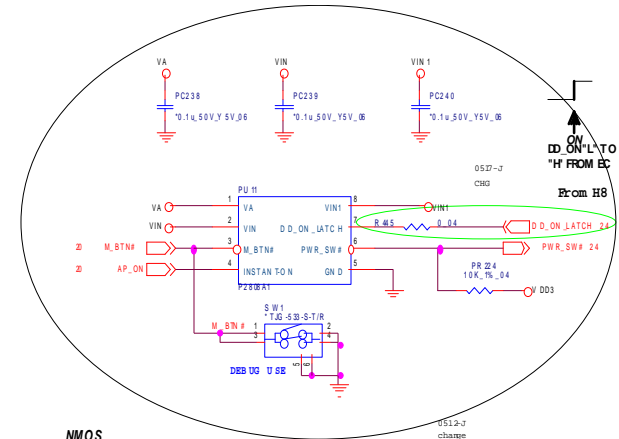
B.Schematic Diagrams

System PWR, 2.5V

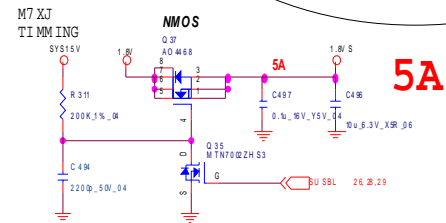
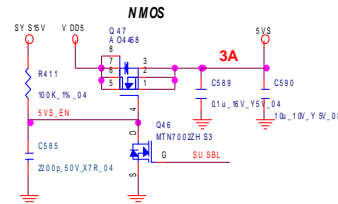
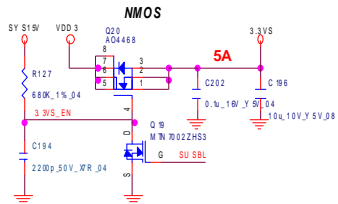


5A

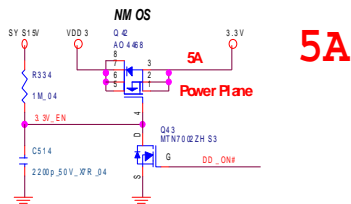
5A



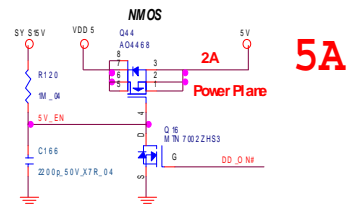
Sheet 25 of 35
System PWR, 2.5V



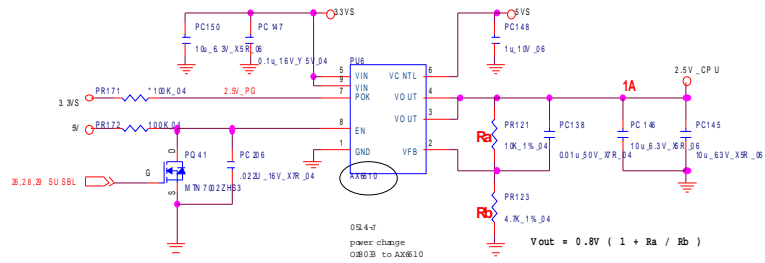
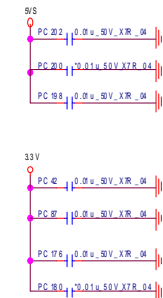
5A



5A



5A

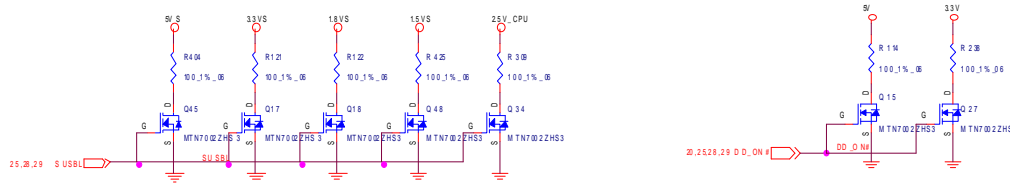


05L4-7
power change
08/03 to A3610

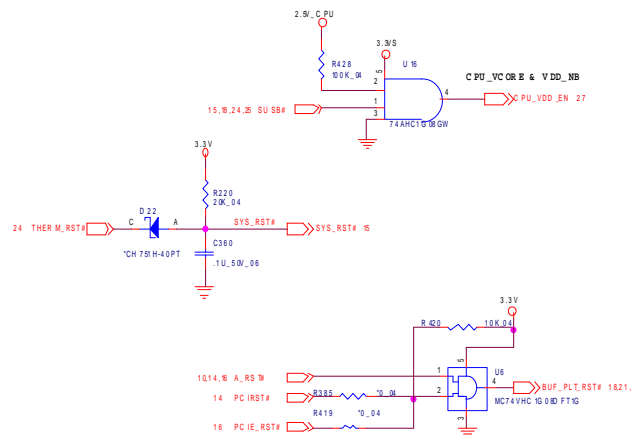
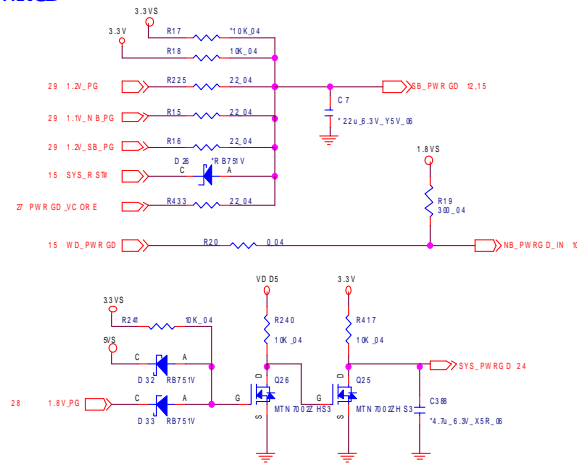
$$V_{out} = 0.8V (1 + R_a / R_b)$$

B.Schematic Diagrams

PWRGD, RST

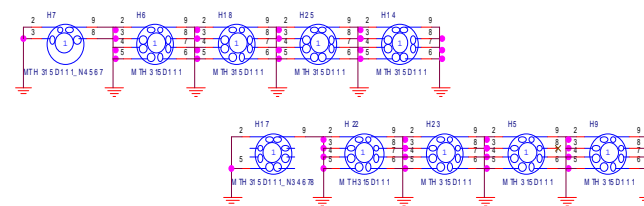
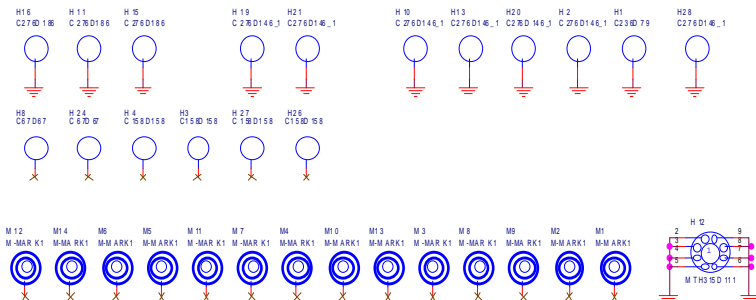


PWRGD



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PWRGD, RST

B. Schematic Diagrams



VCORE_Core

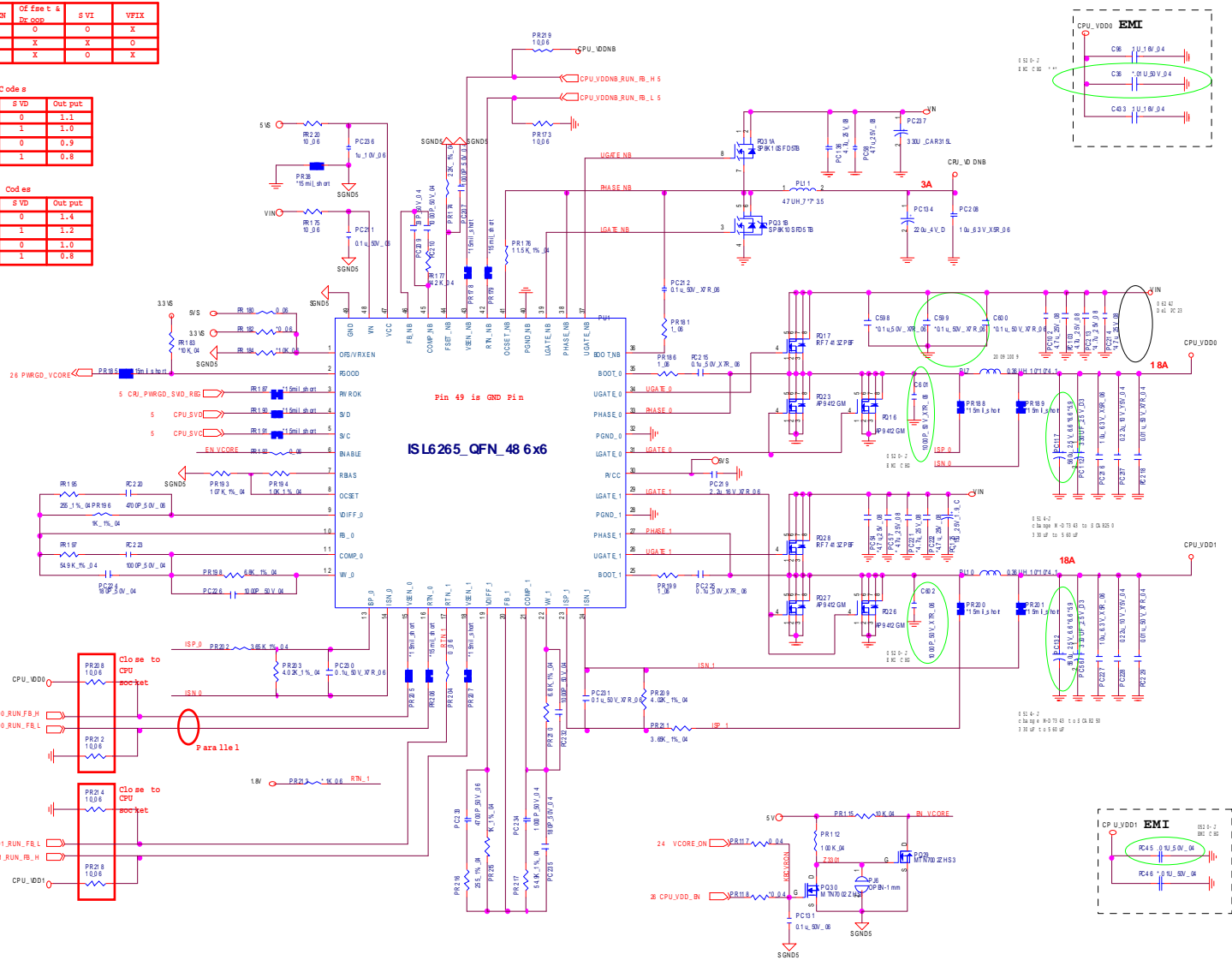
OPS/VFIX EN	OE Escrt & Dr. con.	S VI	VFIX
GN D	0	0	X
+3.3V	X	X	0
+5V	X	0	X

Meta 1 VID Codes		
SV C	S VD	Out put
0	0	1.1
0	1	1.0
1	0	0.9
1	1	0.8

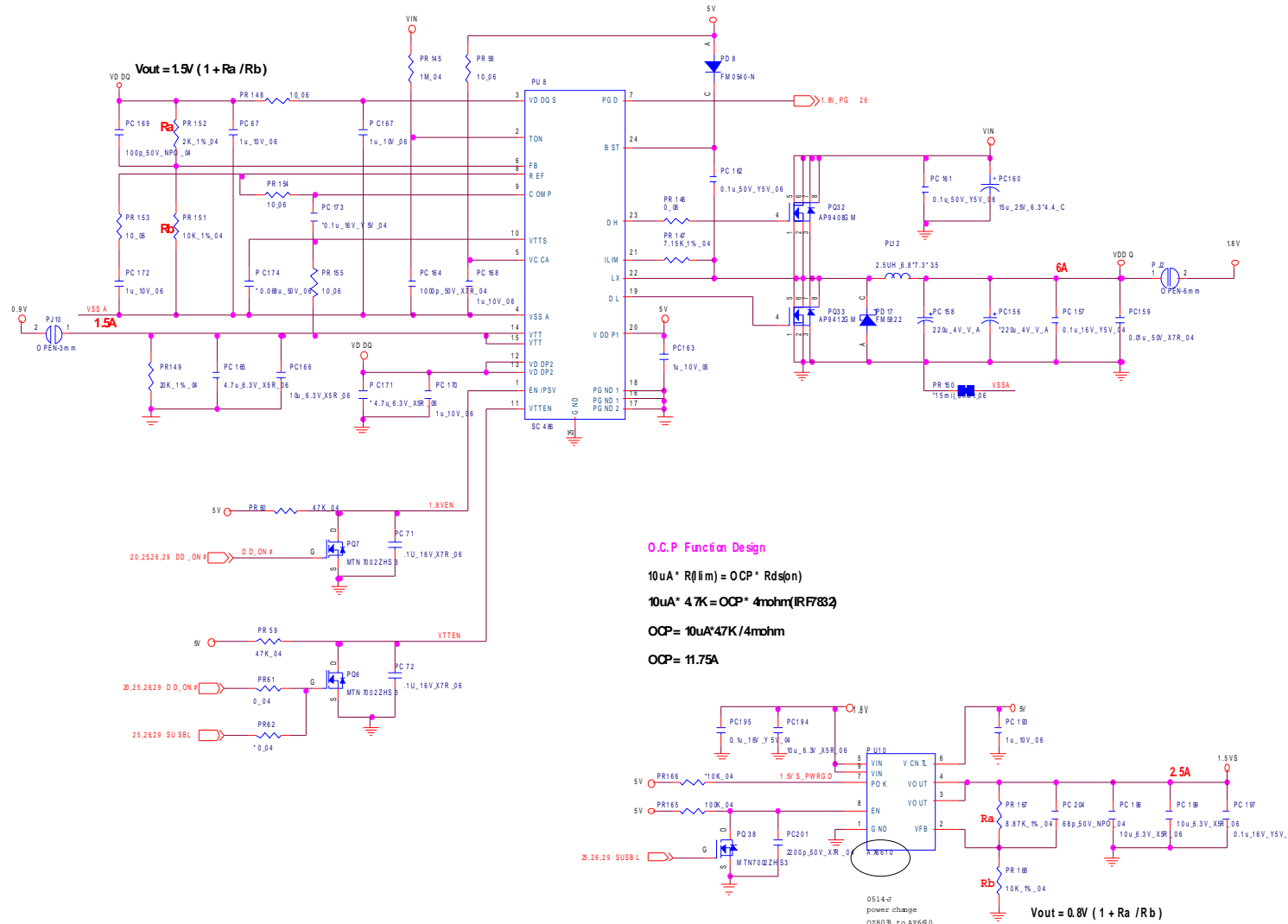
V FIX EN VID Codes		
SV C	S VD	Out put
0	0	1.4
0	1	1.2
1	0	1.0
1	1	0.8

Sheet 27 of 35
VCORE_Core

B.Schematic Diagrams



0.9V, 1.8V, 1.8VS, 1.5VS



Sheet 28 of 35
0.9V, 1.8V, 1.8VS,
1.5VS

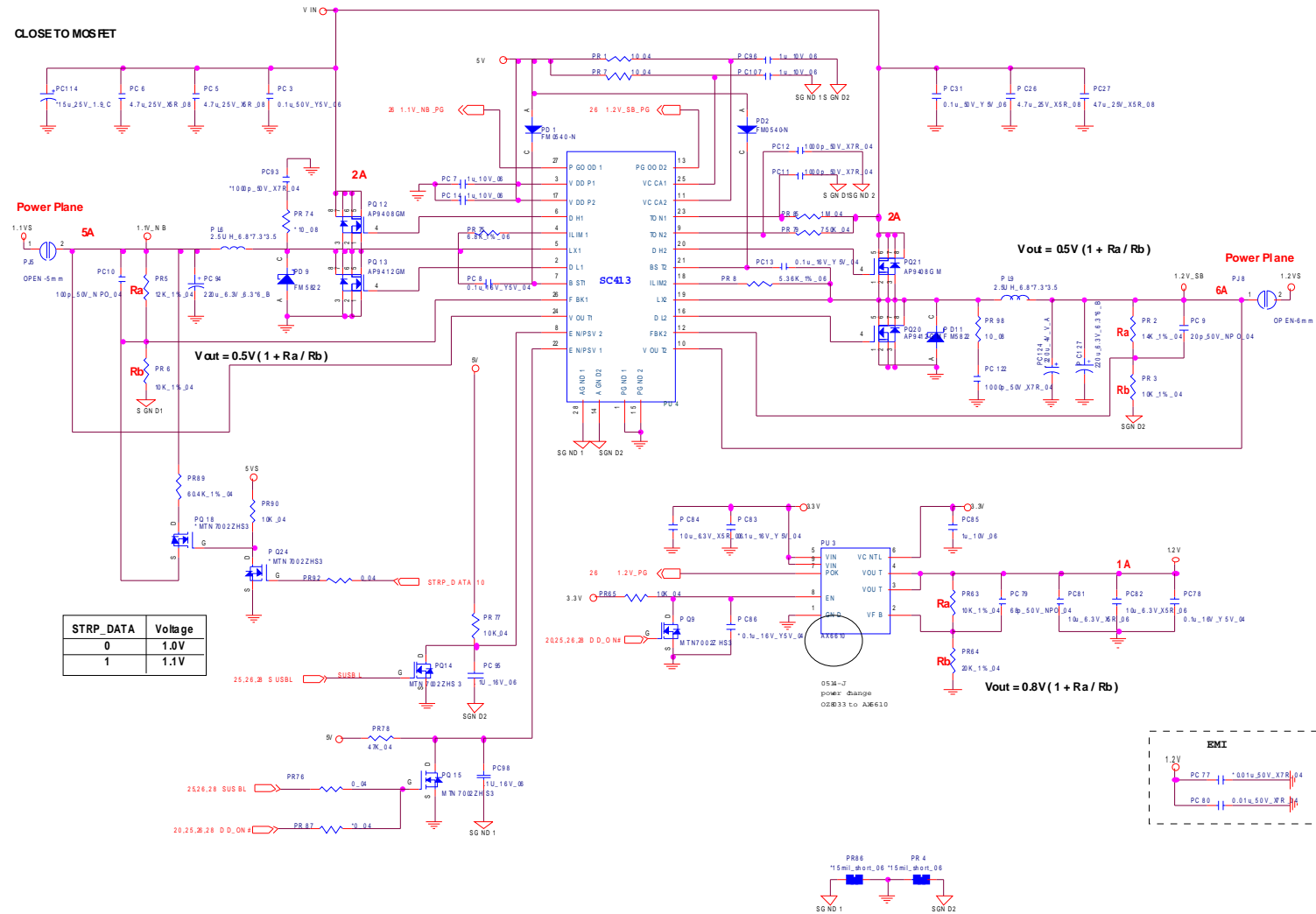
B. Schematic Diagrams

Schematic Diagrams

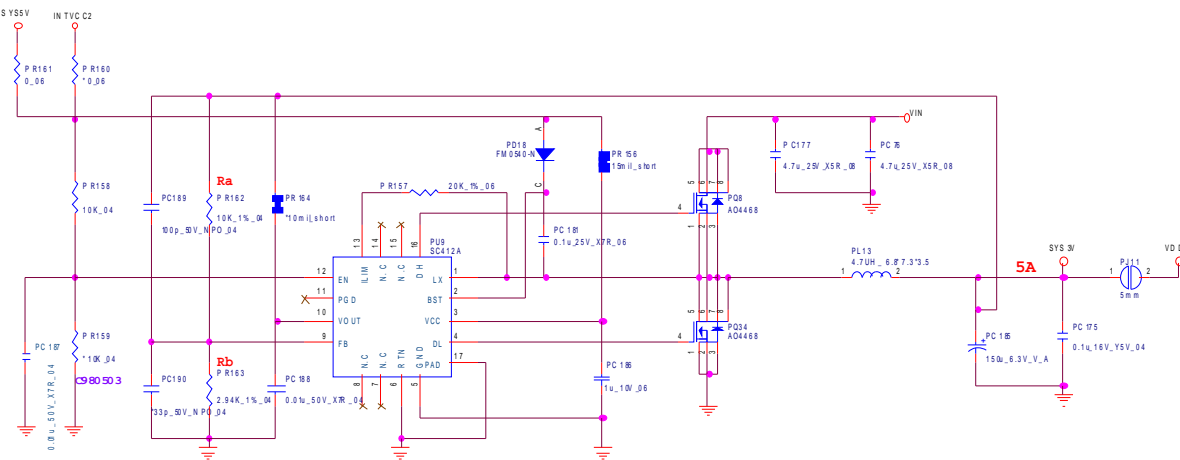
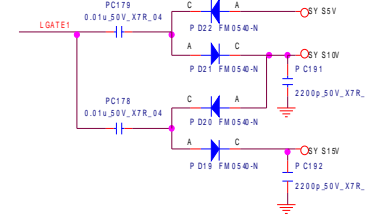
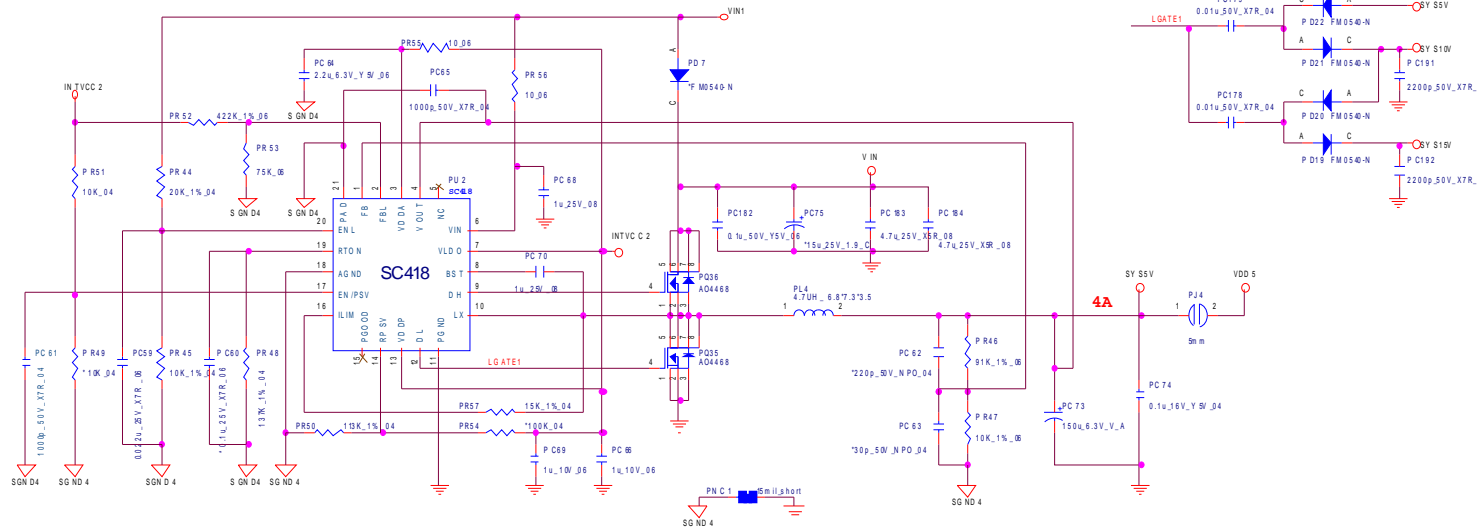
1.1VS, 1.2V, 1.2VS

B.Schematic Diagrams

Sheet 29 of 35
1.1VS, 1.2V, 1.2VS



VDD3, VDD5

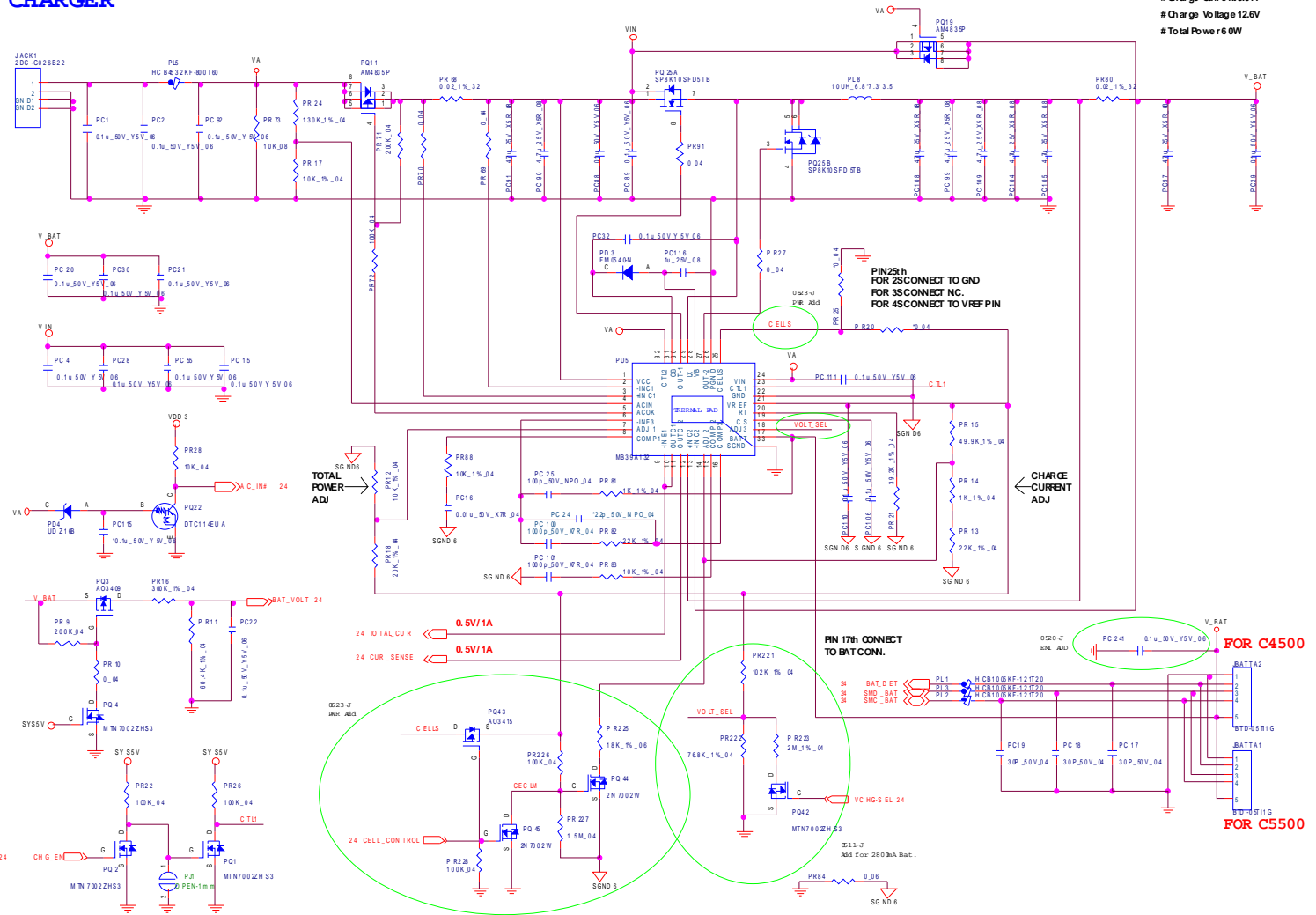


Sheet 30 of 35
VDD3, VDD5

Charger, DC In

CHARGER

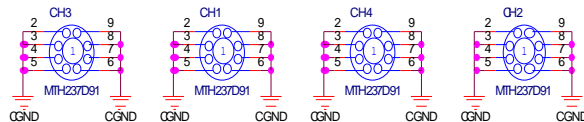
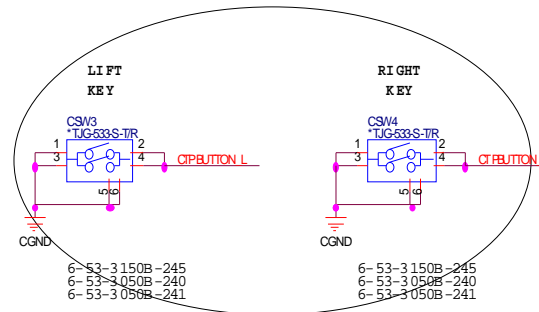
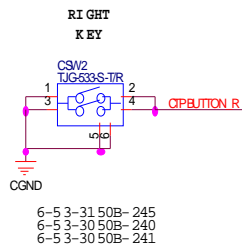
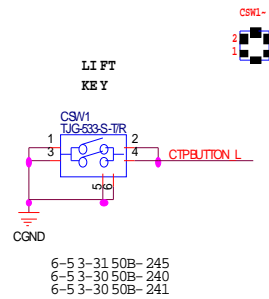
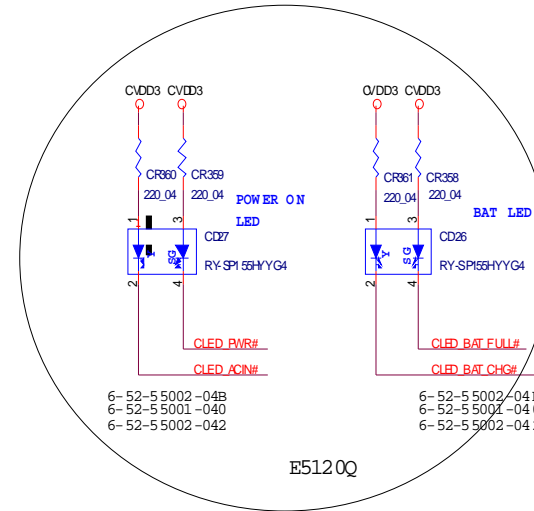
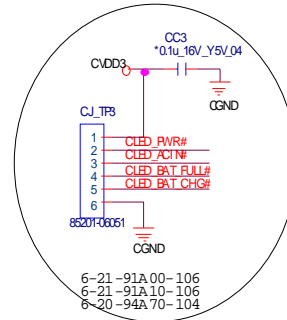
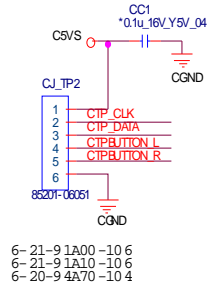
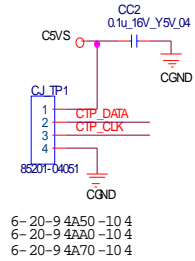
Charge Current 3.0 A
 # Charge Voltage 12.6V
 # Total Power 60W



Sheet 31 of 35
 Charger, DC In

Click Board

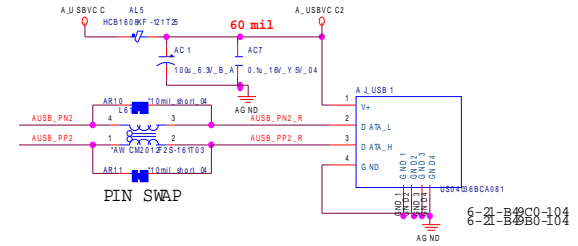
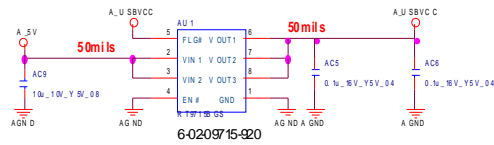
CLICK BOARD



Sheet 32 of 35
Click Board

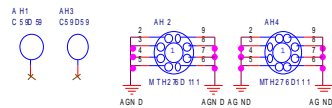
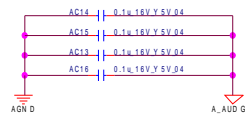
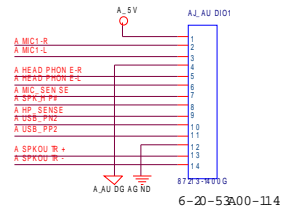
Audio Board/USB

USB PORT

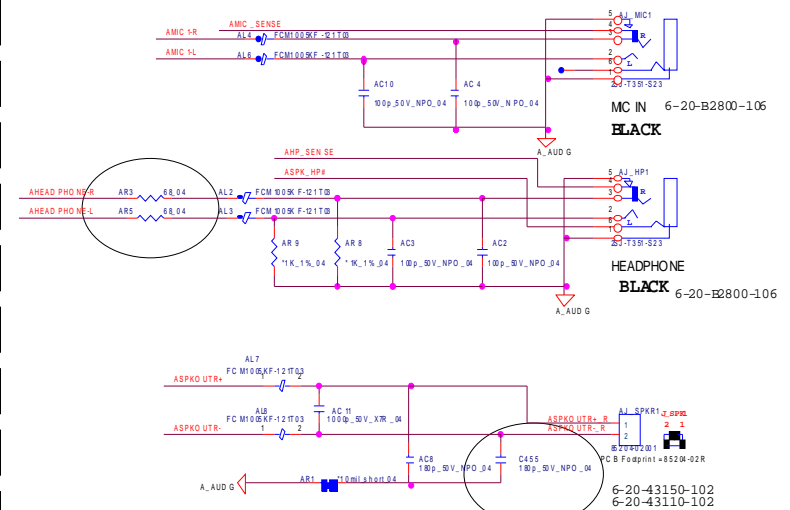


TO M/B

Sheet 33 of 35
Audio Board/USB

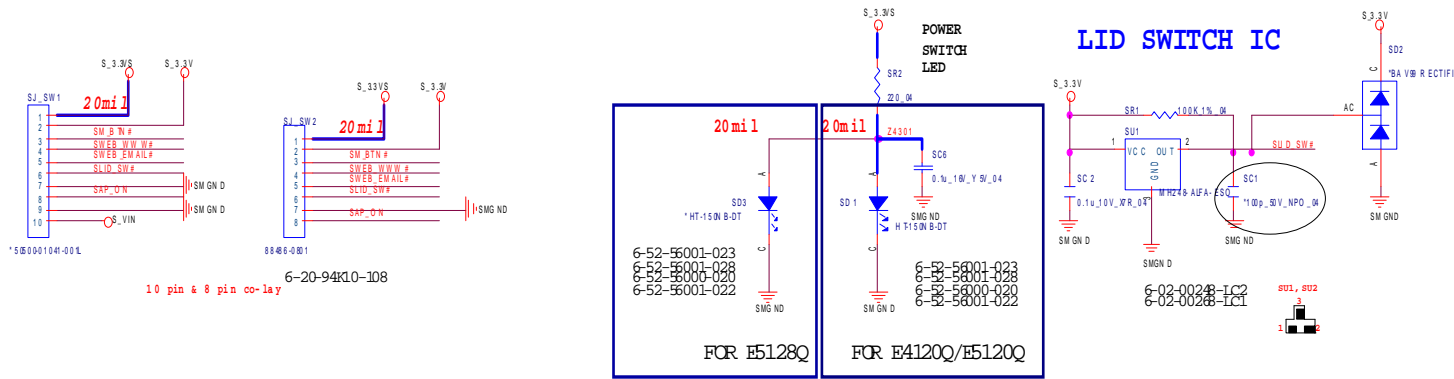


AUDIO JACK



Power Switch Board

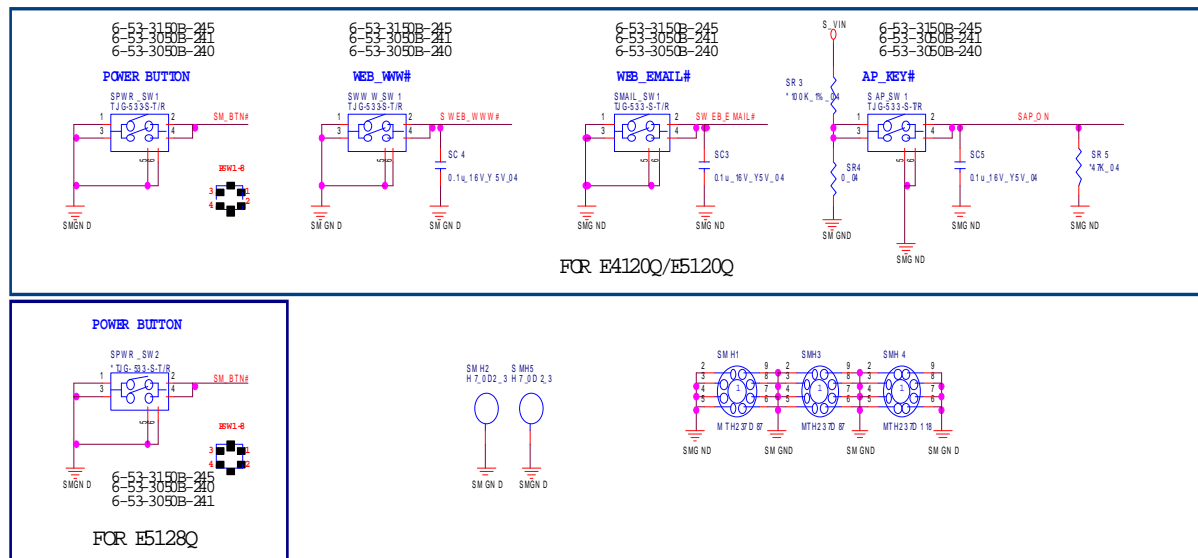
POWER SW & LED & HOT KEY



Sheet 34 of 35
Power Switch Board

B. Schematic Diagrams

HOT KEY



External ODD Board

ODD BOARD FOR E5120Q

Sheet 35 of 35
External ODD
Board

