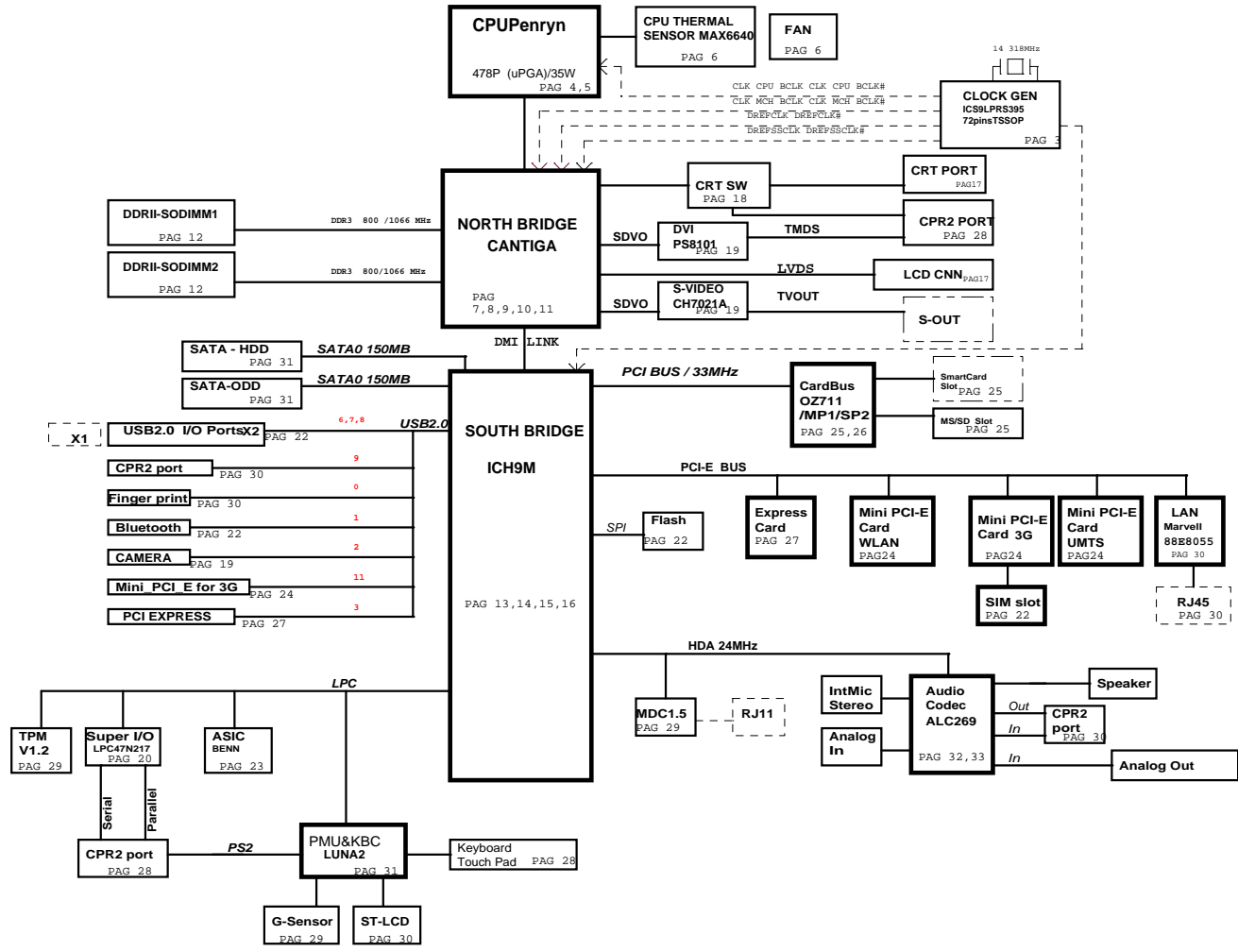


FJ3 BLOCK DIAGRAM

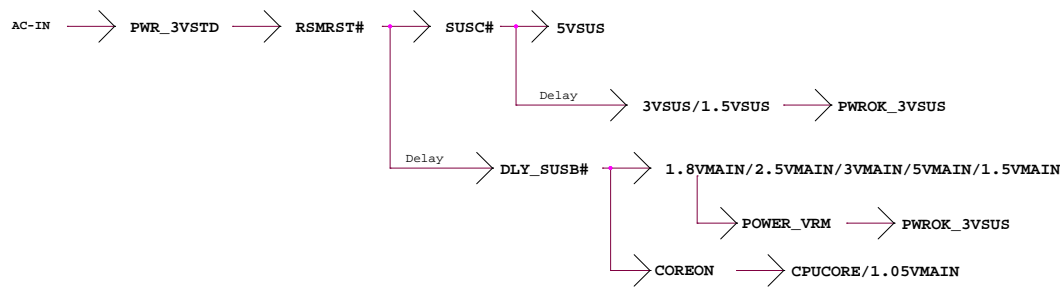



FJ3 BLOCK DIAGRAM		
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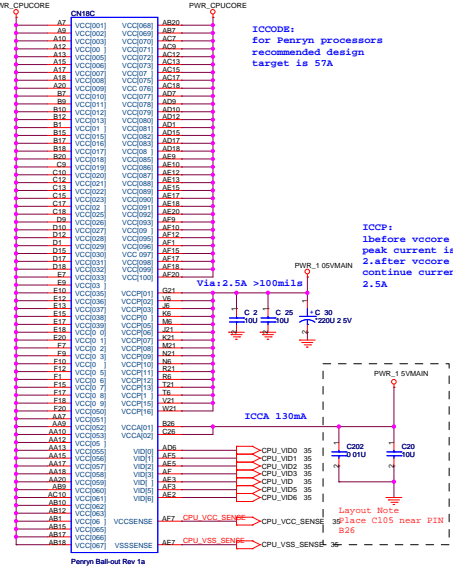
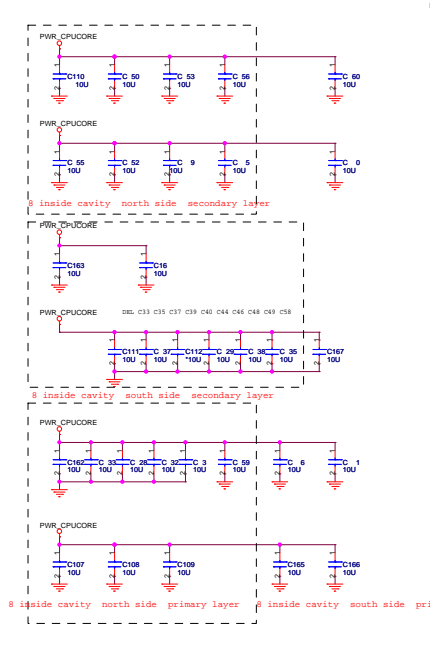
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01--System Block Diagram	25--OZ711 (1/2)/3 IN1 / SMART CARD CONN
02--SCHEMATIC PAGE LIST	26--OZ711 (2/2) NC
03--CLOCK G_IC3395	27--NEW CARD
04--Penryn CPU (POWER/NC)	28--DOCKING CN/ KB /LCM/SW
05--Penryn Processor(Power/NC)	29--SATA/MDC/TPM/GSENSOR/ ODD
06--Thermal Sensor and FAN	30--LAN_MARVELL 88E8055
07--CANTIGA_A(HOST)	31--EC Controller
08--CANTIGA_B(VGA,DMI)	32--AUDIO AL269 1/2
09--CANTIGA_C(DDR)	33--AUDIO AL269 2/2
10--CANTIGA_D(VCC,NCTF)	34--RESET
11--CANTIGA_E(Power)	35--CPU CORE POWER
12--DDR3 SO-DIMM(204P)	36--LDO/SWITCH
13--ICH9-M HOST(1/4)	37--BATTERY
14--ICH9-M PCIE(2/4)	38--CHARGER
15--ICH9-M GPIO(3/4)	39--5VPMU/5VSTD/3VPMU/3VSTD
16--ICH9-M Power(4/4)	40--1.5V_DIMM
17--LVDS/CRT	41--BATTERY CN
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21--COM/PARALLEL PORT	
22--SPI BIOS/USB port/TV CONN/BT/SIM	
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24--MINI PCIE	

POWER_Control_Tree

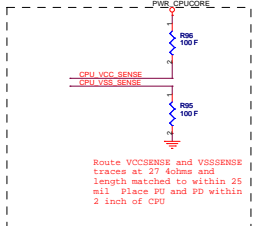


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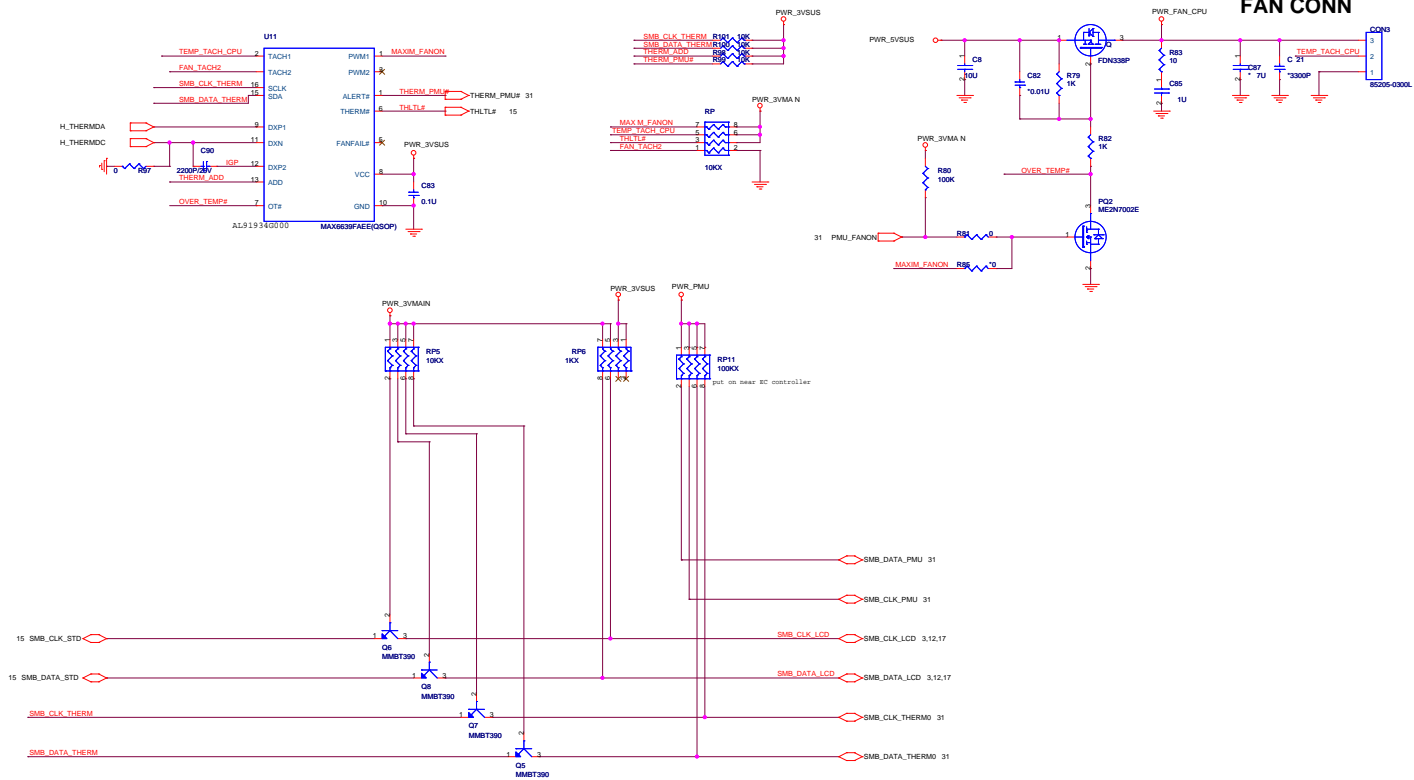
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AE	VSS011	VSS012	E6
AF	VSS013	VSS014	E7
AG	VSS015	VSS016	E8
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AI	VSS019	VSS020	E10
AJ	VSS021	VSS022	E11
AK	VSS023	VSS024	E12
AL	VSS025	VSS026	E13
AM	VSS027	VSS028	E14
AN	VSS029	VSS030	E15
AO	VSS031	VSS032	E16
AP	VSS033	VSS034	E17
AQ	VSS035	VSS036	E18
AR	VSS037	VSS038	E19
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AU	VSS043	VSS044	E22
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AW	VSS047	VSS048	E24
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BE	VSS063	VSS064	E32
BF	VSS065	VSS066	E33
BG	VSS067	VSS068	E34
BH	VSS069	VSS070	E35
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BJ	VSS073	VSS074	E37
BK	VSS075	VSS076	E38
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BM	VSS079	VSS080	E40
BN	VSS081	VSS082	E41
BO	VSS083	VSS084	E42
BP	VSS085	VSS086	E43
BQ	VSS087	VSS088	E44
BR	VSS089	VSS090	E45
BS	VSS091	VSS092	E46
BT	VSS093	VSS094	E47
BV	VSS095	VSS096	E48
BW	VSS097	VSS098	E49
BX	VSS099	VSS100	E50
BY	VSS101	VSS102	E51
BZ	VSS103	VSS104	E52
CA	VSS105	VSS106	E53
CB	VSS107	VSS108	E54
CC	VSS109	VSS110	E55
CD	VSS111	VSS112	E56
CE	VSS113	VSS114	E57
CF	VSS115	VSS116	E58
CG	VSS117	VSS118	E59
CH	VSS119	VSS120	E60
CI	VSS121	VSS122	E61
CJ	VSS123	VSS124	E62
CK	VSS125	VSS126	E63
CL	VSS127	VSS128	E64
CM	VSS129	VSS130	E65
CN	VSS131	VSS132	E66
CO	VSS133	VSS134	E67
CP	VSS135	VSS136	E68
CQ	VSS137	VSS138	E69
CR	VSS139	VSS140	E70
CS	VSS141	VSS142	E71
CT	VSS143	VSS144	E72
CU	VSS145	VSS146	E73
CV	VSS147	VSS148	E74
CW	VSS149	VSS150	E75
CX	VSS151	VSS152	E76
CY	VSS153	VSS154	E77
CZ	VSS155	VSS156	E78
DA	VSS157	VSS158	E79
DB	VSS159	VSS160	E80
DC	VSS161	VSS162	E81
DD	VSS163	VSS164	E82
DE	VSS165	VSS166	E83
DF	VSS167	VSS168	E84
DG	VSS169	VSS170	E85
DH	VSS171	VSS172	E86
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DJ	VSS175	VSS176	E88
DK	VSS177	VSS178	E89
DL	VSS179	VSS180	E90
DM	VSS181	VSS182	E91
DN	VSS183	VSS184	E92
DO	VSS185	VSS186	E93
DP	VSS187	VSS188	E94
DQ	VSS189	VSS190	E95
DR	VSS191	VSS192	E96
DS	VSS193	VSS194	E97
DT	VSS195	VSS196	E98
DV	VSS197	VSS198	E99
DW	VSS199	VSS200	E100



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PWR_15VMAIN 11.18 2.27 55. 0
PWR_105VMAIN 7.8,10.11,13,16 35. 2

Thermal Sensor



QUANTA COMPUTER

Thermal Sensor and Fan

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0000	0000000000	1A
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0000	0000	0000

033 MAIN BOARD

M.A.DQ83[0] 12

U280

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M.A.D63	AJ.2	SA.DD.0

DDR SYSTEM MEMORY A

12 M.B.DQ83[0] 12

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M.B.D15	BC.7	SB.DD.0
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M.B.D17	BC.3	SB.DD.0
M.B.D18	BC.3	SB.DD.0
M.B.D19	BE.3	SB.DD.0
M.B.D20	BE.5	SB.DD.0
M.B.D21	BE.1	SB.DD.0
M.B.D22	BE.0	SB.DD.0
M.B.D23	BE.1	SB.DD.0
M.B.D24	BE.2	SB.DD.0
M.B.D25	BE.3	SB.DD.0
M.B.D26	BE.4	SB.DD.0
M.B.D27	BE.5	SB.DD.0
M.B.D28	BE.6	SB.DD.0
M.B.D29	BE.7	SB.DD.0
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M.B.D31	BE.9	SB.DD.0
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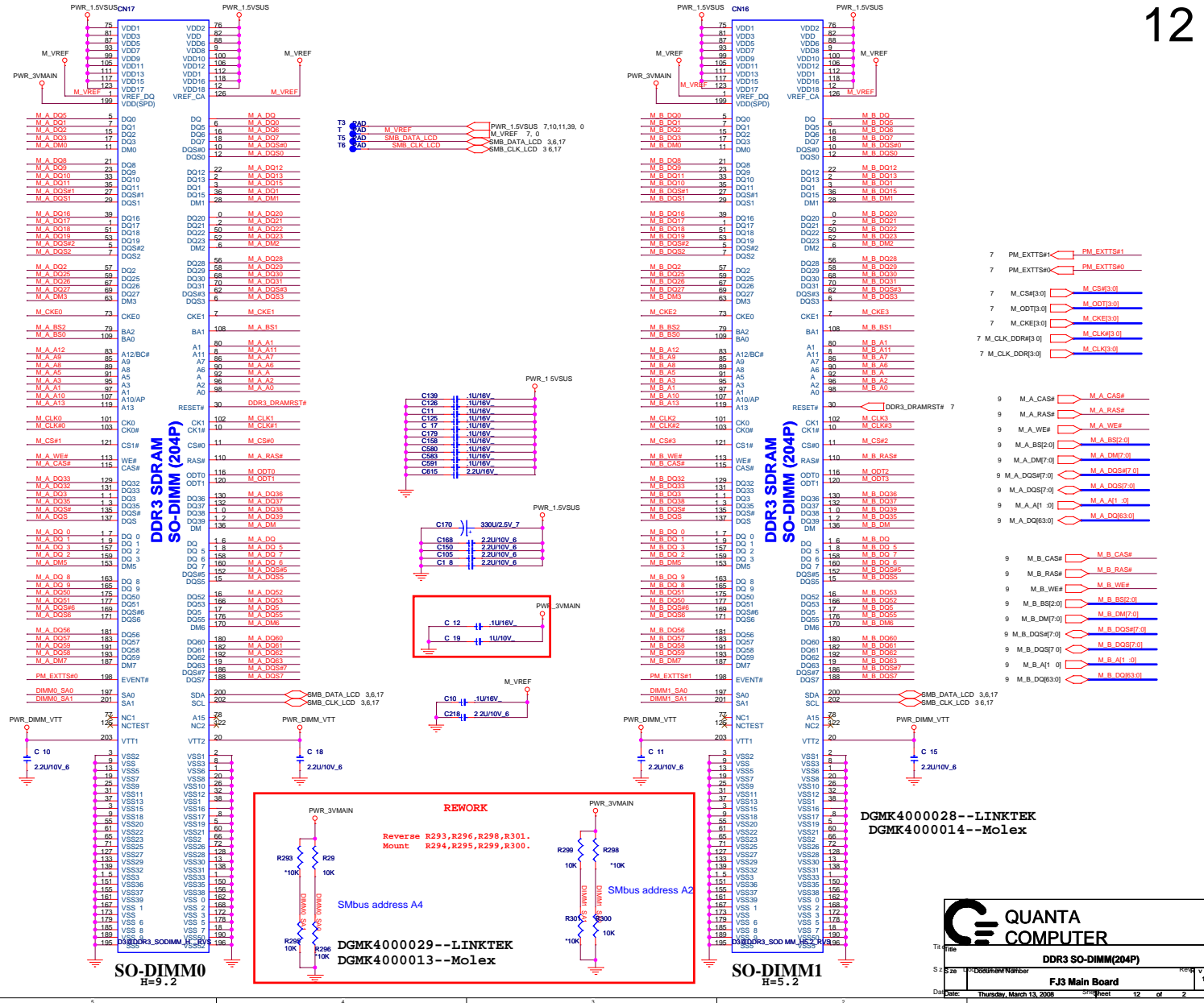
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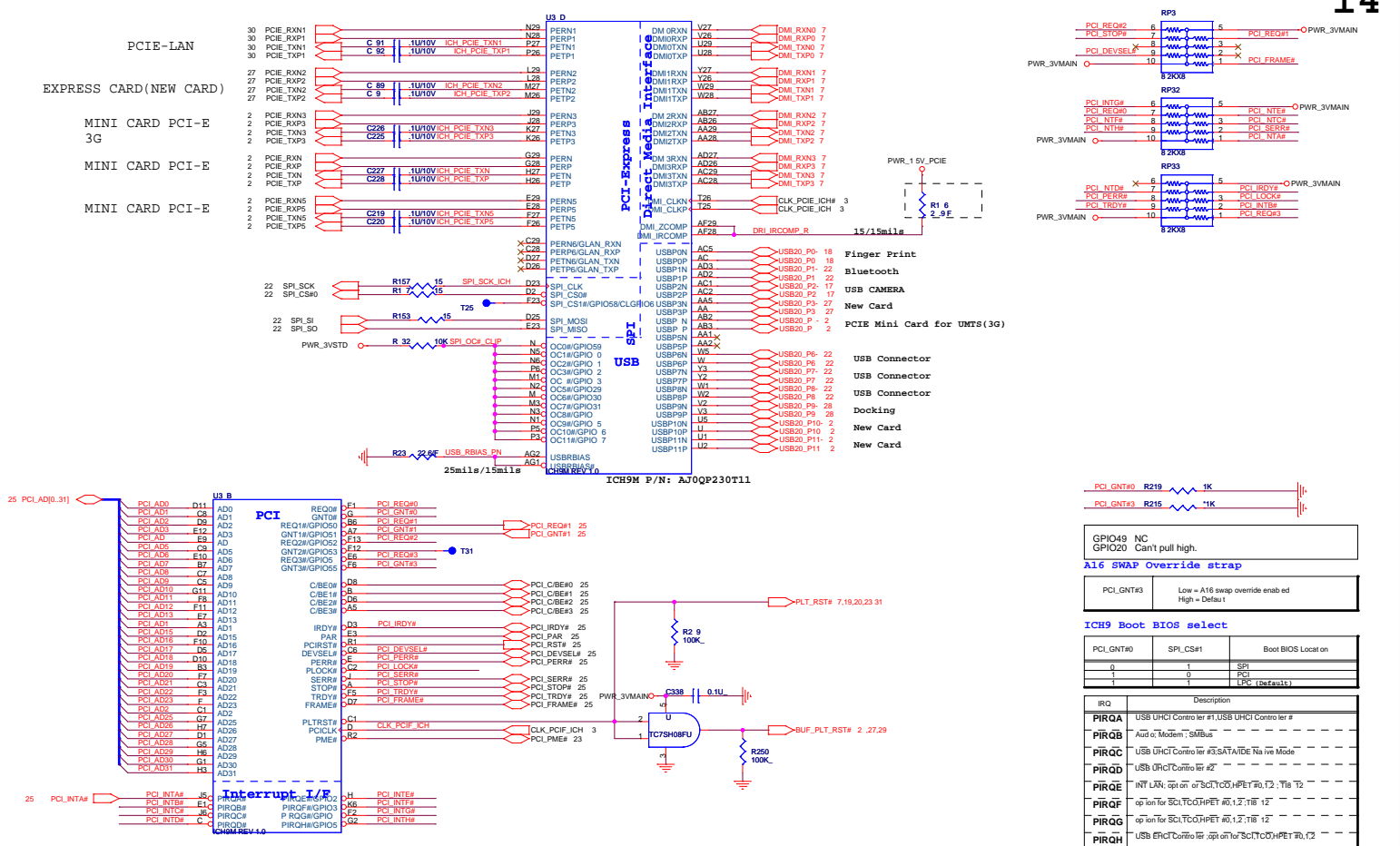
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SB.DD.4	AI.7	SB.DD.0
SB.DD.5	AI.6	SB.DD.0
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SB.DD.9	AI.8	SB.DD.0
SB.DD.10	BA.8	SB.DD.0
SB.DD.11	AY.8	SB.DD.0
SB.DD.12	AI.7	SB.DD.0
SB.DD.13	AR.7	SB.DD.0
SB.DD.14	BA.7	SB.DD.0
SB.DD.15	BC.7	SB.DD.0
SB.DD.16	BC.5	SB.DD.0
SB.DD.17	BC.3	SB.DD.0
SB.DD.18	BC.3	SB.DD.0
SB.DD.19	BE.3	SB.DD.0
SB.DD.20	BE.5	SB.DD.0
SB.DD.21	BE.1	SB.DD.0
SB.DD.22	BE.0	SB.DD.0
SB.DD.23	BE.1	SB.DD.0
SB.DD.24	BE.2	SB.DD.0
SB.DD.25	BE.3	SB.DD.0
SB.DD.26	BE.4	SB.DD.0
SB.DD.27	BE.5	SB.DD.0
SB.DD.28	BE.6	SB.DD.0
SB.DD.29	BE.7	SB.DD.0
SB.DD.30	BE.8	SB.DD.0
SB.DD.31	BE.9	SB.DD.0
SB.DD.32	BE.10	SB.DD.0
SB.DD.33	BE.11	SB.DD.0
SB.DD.34	BE.12	SB.DD.0
SB.DD.35	BE.13	SB.DD.0
SB.DD.36	BE.14	SB.DD.0
SB.DD.37	BE.15	SB.DD.0
SB.DD.38	BE.16	SB.DD.0
SB.DD.39	BE.17	SB.DD.0
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SB.DD.43	BE.21	SB.DD.0
SB.DD.44	BE.22	SB.DD.0
SB.DD.45	BE.23	SB.DD.0
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SB.DD.48	BE.26	SB.DD.0
SB.DD.49	BE.27	SB.DD.0
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SB.DD.60	BE.38	SB.DD.0
SB.DD.61	BE.39	SB.DD.0
SB.DD.62	BE.40	SB.DD.0
SB.DD.63	BE.41	SB.DD.0

DDR SYSTEM MEMORY B

U286

SB.BS.0	M.B.BS0	M.B.BS0 12
SB.BS.1	M.B.BS1	M.B.BS1 12
SB.BS.2	M.B.BS2	M.B.BS2 12
SB.RAS#	M.B.RAS#	M.B.RAS# 12
SB.CAS#	M.B.CAS#	M.B.CAS# 12
SB.WE#	M.B.WE#	M.B.WE# 12
AM.7	M.B.DM0	M.B.DM[7..0] 12
AY.7	M.B.DM1	M.B.DM[7..0] 12
BB.0	M.B.DM2	M.B.DM[7..0] 12
BB.1	M.B.DM3	M.B.DM[7..0] 12
BB.2	M.B.DM4	M.B.DM[7..0] 12
BB.3	M.B.DM5	M.B.DM[7..0] 12
BB.4	M.B.DM6	M.B.DM[7..0] 12
BB.5	M.B.DM7	M.B.DM[7..0] 12
AL.7	M.B.DQS0	M.B.DQS[7..0] 12
AV.8	M.B.DQS1	M.B.DQS[7..0] 12
BB.1	M.B.DQS2	M.B.DQS[7..0] 12
BB.2	M.B.DQS3	M.B.DQS[7..0] 12
BB.3	M.B.DQS4	M.B.DQS[7..0] 12
BB.4	M.B.DQS5	M.B.DQS[7..0] 12
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BB.8	M.B.DQS9	M.B.DQS[7..0] 12
BB.9	M.B.DQS10	M.B.DQS[7..0] 12
BB.10	M.B.DQS11	M.B.DQS[7..0] 12
BB.11	M.B.DQS12	M.B.DQS[7..0] 12
BB.12	M.B.DQS13	M.B.DQS[7..0] 12
BB.13	M.B.DQS14	M.B.DQS[7..0] 12
BB.14	M.B.DQS15	M.B.DQS[7..0] 12
BB.15	M.B.DQS16	





GPIO49 NC
GPIO20 Can't pull high.

A16 SWAP Override strap

PCI_GNT#3	Low = A16 swap override enab ed
	High = Default

ICH9 Boot BIOS select

PCI_GNT#0	SPL_CS#1	Boot BIOS Locat on
0	1	SPI
1	0	PCI
1	1	EEP (Default)

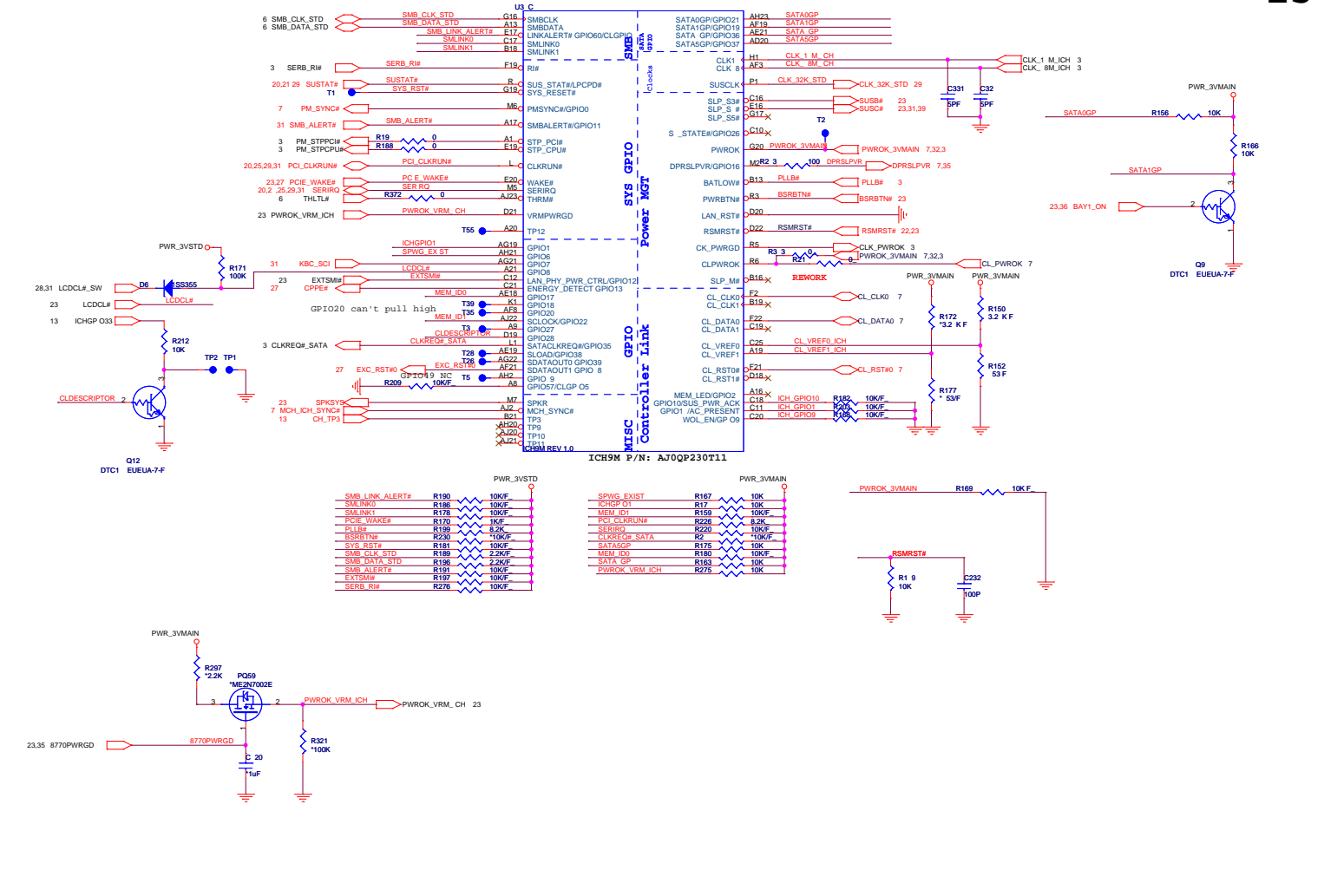
IRQ	Description
PIRQA	USB UHCI Control ler #1 USB UHCI Control ler #
PIRQB	Auto or Modern; SMBus
PIRCB	USB UHCI Control ler #3 SATA/IDE Na ive Mode
PIRCD	USB UHCI Control ler #2
PIRQE	INT LAN; opt on or SCLTCO#PET #0; T2; T1b; T2
PIRQF	opt on for SCLTCO#PET #0; 1; 2; T1b; 12
PIRQG	opt on for SCLTCO#PET #0; 1; 2; T1b; 12
PIRQH	USB UHCI Control ler; opt on for SCLTCO#PET #0; T2

QUANTA COMPUTER

ICH9-M PCIe 2 of 4

Document Number: FJ3 Main Board

Date: Thursday, March 13, 2008



SMB_LINK_ALERT#	R190	10K
SMB_WAKE#	R196	10K
SMB_LINK#	R178	10K
PCIE_WAKE#	R170	1K
PLL#	R159	8.2K
BSRBTN#	R230	10K
SYS_RST#	R181	10K
SMB_CLK_STD	R189	2.2K
SMB_DATA_STD	R198	2.2K
SMB_ALERT#	R191	10K
EXTSM#	R197	10K
SERB_R#	R276	10K

SPWS_EXIST	R167	10K
ICH9P01	R17	10K
MEM_ID1	R159	10K
PCI_CLKRUN#	R226	8.2K
SERIRQ	R200	10K
CLKREQ# SATA	R2	10K
MEM_ID0	R175	10K
SATA_GP	R180	10K
SATA_GP	R163	10K
PWROK_VRM_ICH	R279	10K

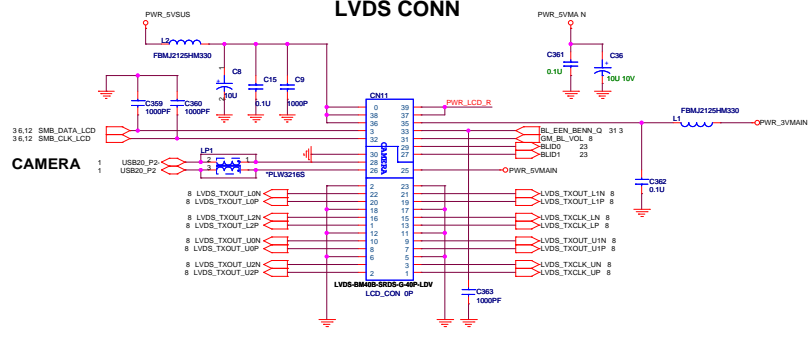
QUANTA COMPUTER

Document Number: **ICH9-M GPIO 3 of 4** Rev: _____

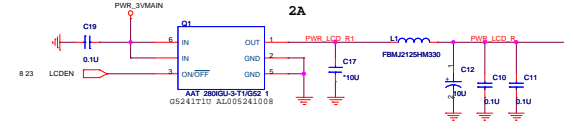
Document Number: **FJ3 Main Board** Rev: **1A**

Thursday, March 13, 2008 15 of 2

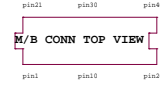
LVDS CONN



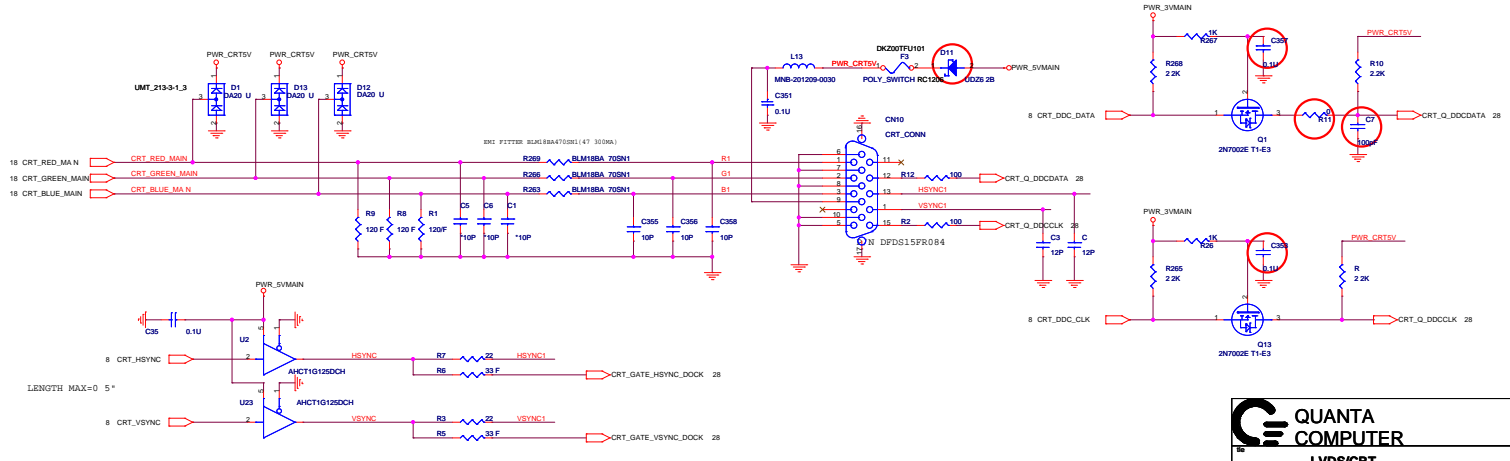
PANEL VCC CONTROL



After B stage CN5 pin define



CN5	CN5 footprint
pin1	pin1
pin2	pin2
pin3	pin3
pin20	pin20



QUANTA COMPUTER

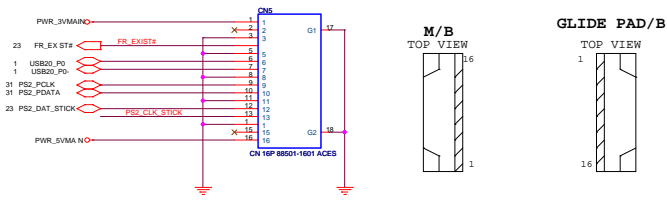
LVDS/CRT

Document Number: **FJ3 MAIN BOARD**

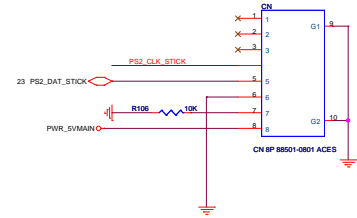
Rev: 1A

Date: Thursday, March 11, 2005

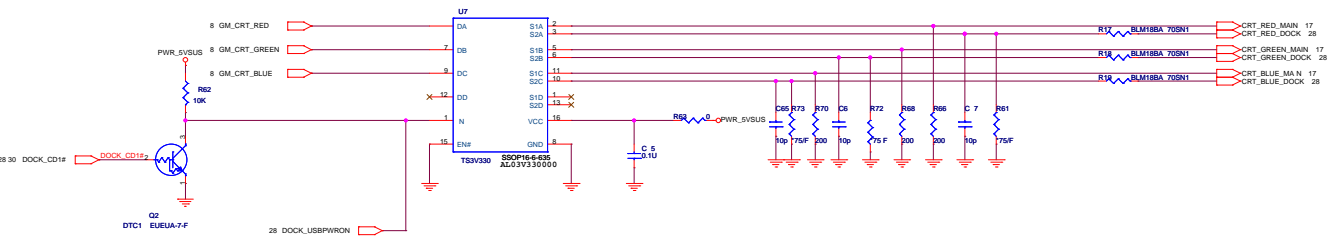
GLIDE PAD CONN



Stick Point CONN



CRT SW CIRCUIT



QUANTA COMPUTER

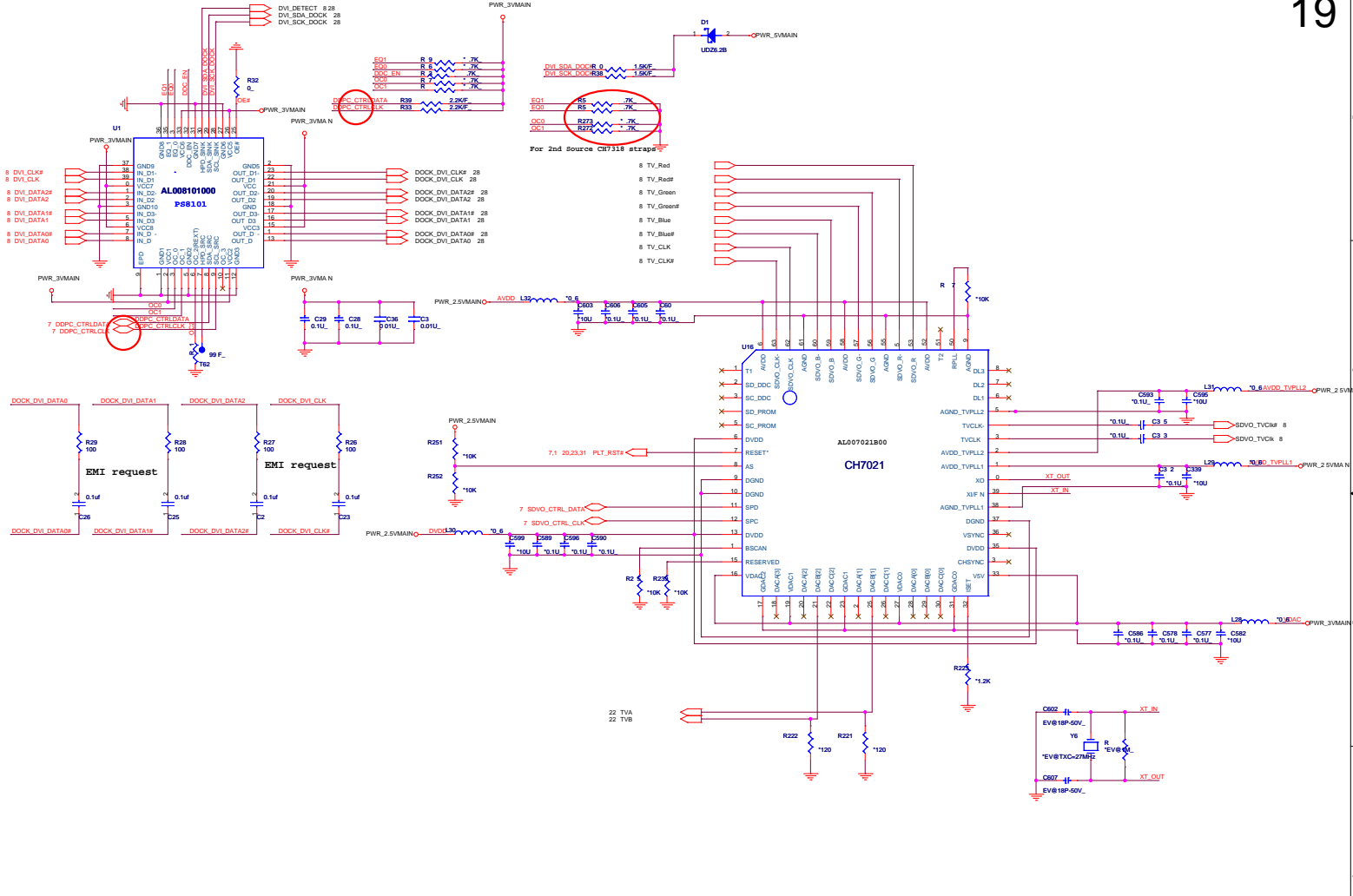
CRT SW/GLIDE PAD/StickPoint

Document Number: FJ3 MAIN BOARD

Rev: 1A

Date: 11/11/2005

1 of 2

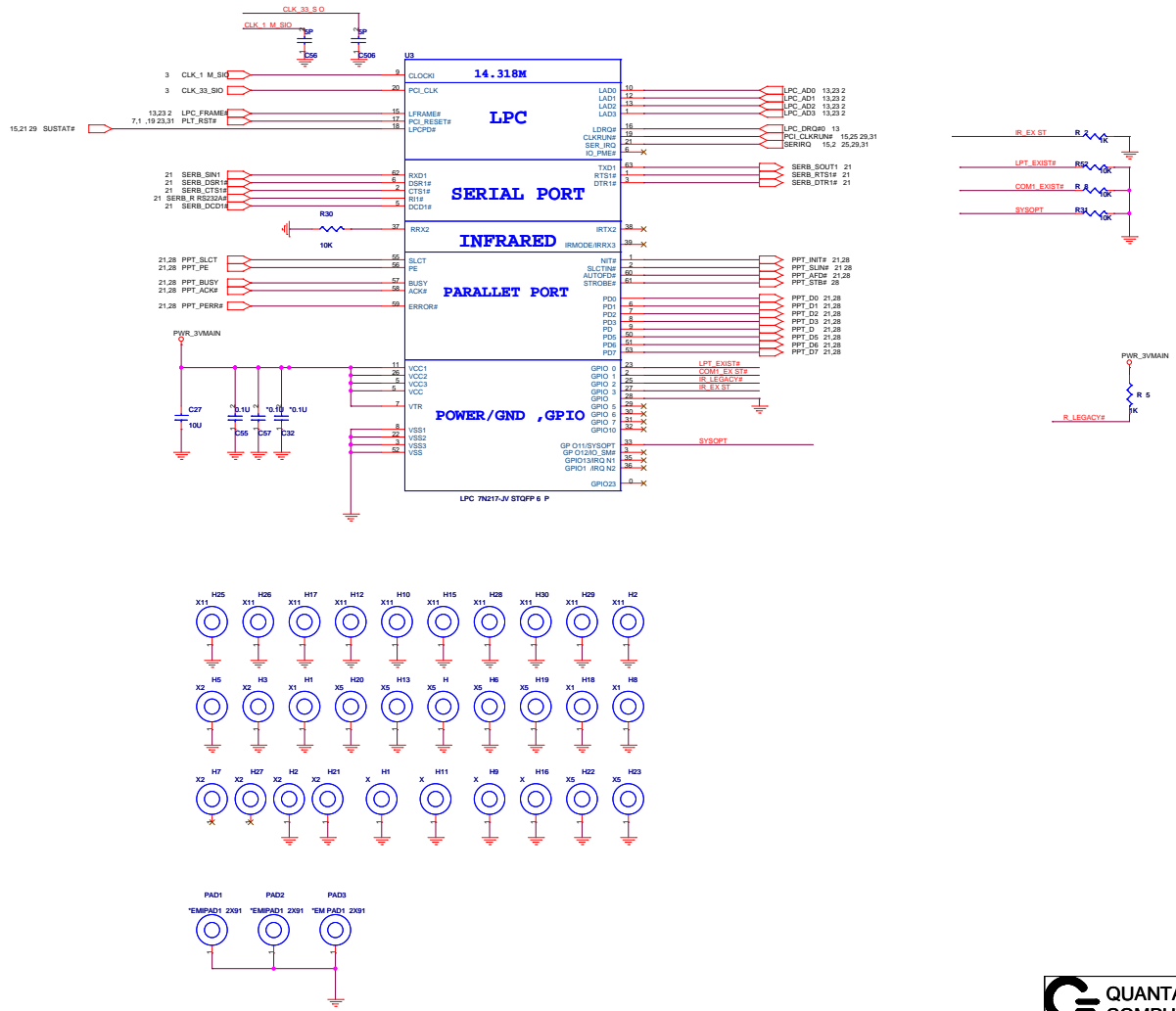


QUANTA COMPUTER

DVI Level Shift & S-Video Encode

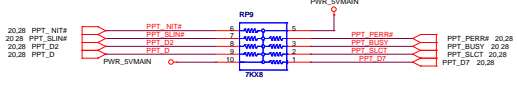
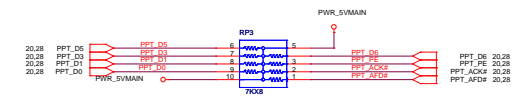
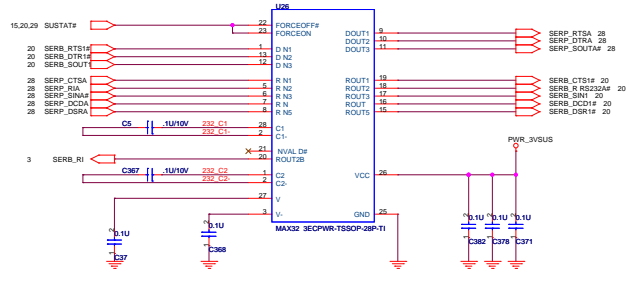
Rev	Doc No	Doc Name	Doc No	Doc Rev	Doc Date
1A	122	F-J3 MAIN BOARD	122	1.0	2005.03.11

Doc No: 122, Rev: 1A, Date: 2005.03.11



QUANTA COMPUTER
SUPER I/O
FJ3 MAIN BOARD

Doc No	Doc Date	Doc Rev

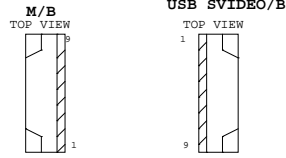
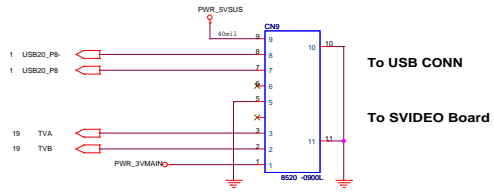
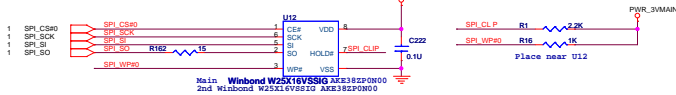


QUANTA COMPUTER

COM /PARALLEL PORT

Doc No:	Document Number:	Doc No:	Rev:
Case No:	Doc No:	FJ3 MAIN BOARD	1A
Date:	Issued:	1 Sheet	2 of 2
Rev:	Issued:	1 Sheet	2 of 2

16Mbit (2M Byte), SPI

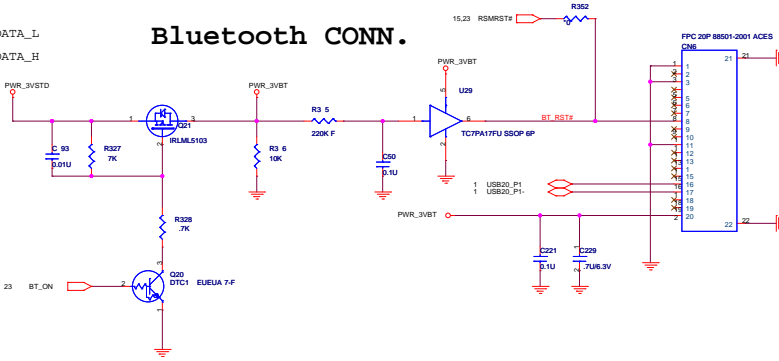


REVERSE TYPE SWAP THE PIN

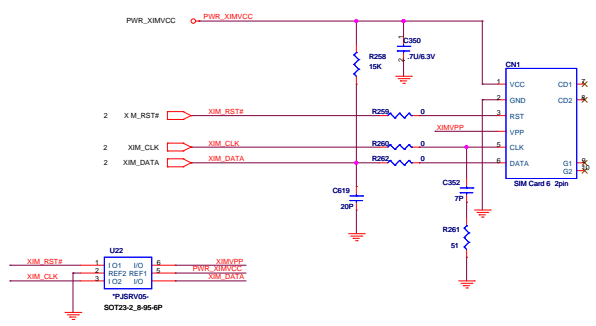
REVERSE TYPE SWAP THE PIN

V+ ==>GND
 DATA_H ==>DATA_L
 DATA_L ==>DATA_H
 GND ==> V+

Bluetooth CONN.



SIM CARD

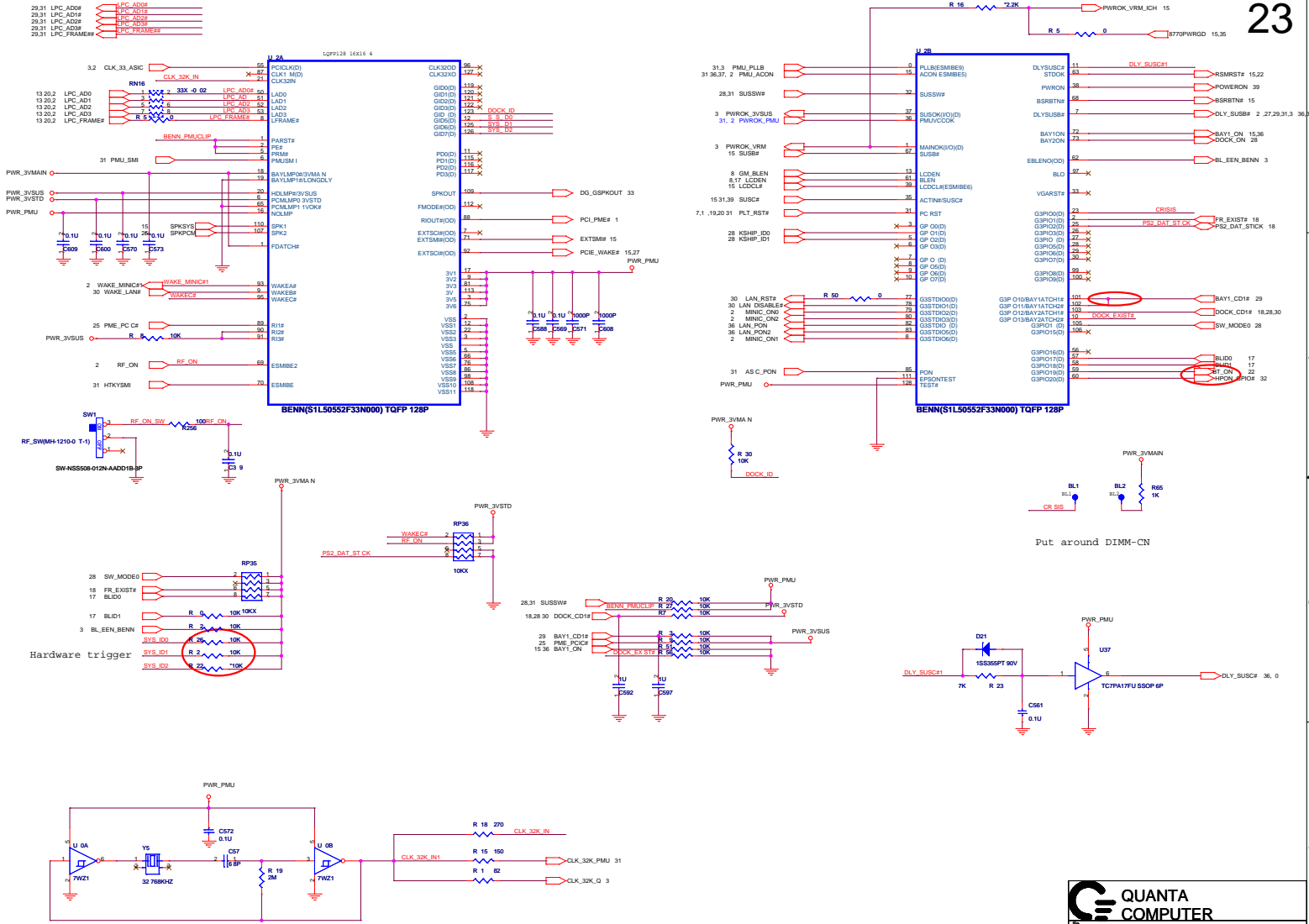


QUANTA COMPUTER

SPI BIOS/USB port/TV CONN

FJ3 MAIN BOARD


Doc No: FJ3-001, March 11, 2005

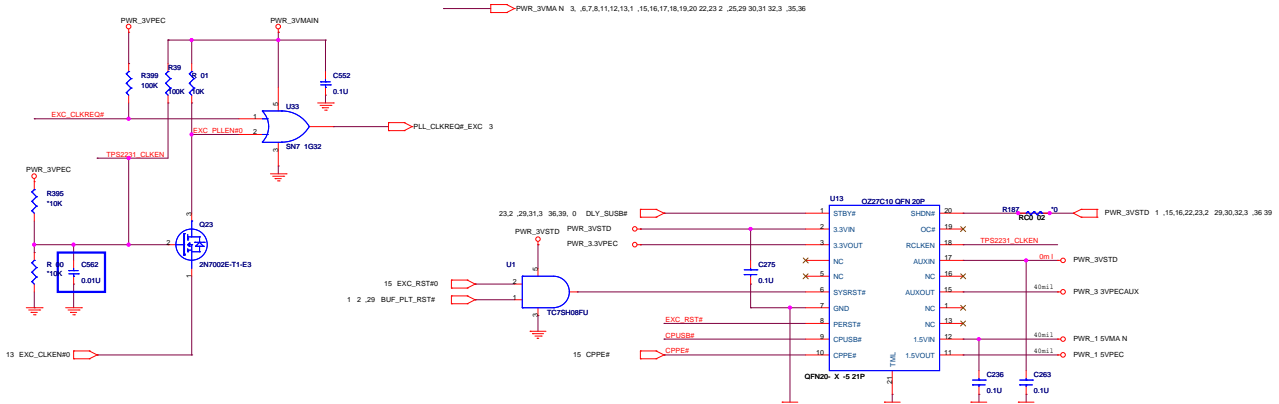


QUANTA COMPUTER
BENN
 FJ3 MAIN BOARD
 Document Number: FJ3 MAIN BOARD
 Date: Thursday, March 11, 2010 1:58:22 PM
 Rev: 1A

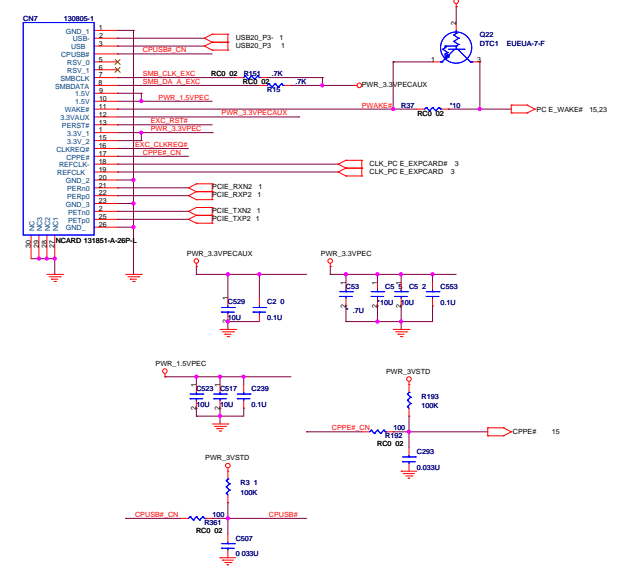
U158		
X-E10	REG# CCB3F	A0ICAD26 D8
		A1ICAD25 G10
		A2ICAD2
		A3ICAD3 D11
X-E7	D9 CAD27	A CAD22 D12
X-E6	D1 CAD9	A3ICAD1
X-E5	D2RFU	A3ICAD0 E13
X-E4	D3CAD0	A3ICAD0 E13
X-E3	D CAD1	A3ICAD0 E13
X-E2	D5CAD3	A3ICAD0 E13
X-E1	D6CAD5	A3ICAD0 E13
X-D9	D7CAD7	A3ICAD0 E13
X-D8	D8 CAD8	A3ICAD0 E13
X-D7	D9 CAD9	A3ICAD0 E13
X-D6	D10CAD11	A3ICAD0 E13
X-D5	D11CAD2	A3ICAD0 E13
X-D4	D12CAD	A3ICAD0 E13
X-D3	D13CAD	A3ICAD0 E13
X-D2	D14CAD	A3ICAD0 E13
X-D1	D15CAD	A3ICAD0 E13
X-D0	D16CAD	A3ICAD0 E13
X-C13	OE# CAD11	A20 CSTOPF E16
X-C12	WE# CONT#	A21 CTR#F E16
X-C11	WE# CONT#	A22 CTR#F E16
X-C10	WE# CONT#	A23 CTR#F E16
X-C9	WE# CONT#	A2 CAD17 G14
X-C8	WE# CONT#	A2 CAD17 G14
X-C7	WE# CONT#	A2 CAD17 G14
X-C6	WE# CONT#	A2 CAD17 G14
X-C5	WE# CONT#	A2 CAD17 G14
X-C4	WE# CONT#	A2 CAD17 G14
X-C3	WE# CONT#	A2 CAD17 G14
X-C2	WE# CONT#	A2 CAD17 G14
X-C1	WE# CONT#	A2 CAD17 G14
X-B1	W# J0518#CCLKRUN#	
X-E11	W# J0518#CCLKRUN#	
X-D8	RD#1 REG#CONT#	NC J1
X-D7	W# J0518#CCLKRUN#	NC J13
X-D6	W# J0518#CCLKRUN#	NC J13
X-D5	W# J0518#CCLKRUN#	NC J13
X-D4	W# J0518#CCLKRUN#	NC J13
X-D3	W# J0518#CCLKRUN#	NC J13
X-D2	W# J0518#CCLKRUN#	NC J13
X-D1	W# J0518#CCLKRUN#	NC J13
X-D0	W# J0518#CCLKRUN#	NC J13
X-C13	CE# CAD10	
X-C12	CE# CAD10	
X-C11	CE# CAD10	
X-C10	CE# CAD10	
X-C9	CE# CAD10	
X-C8	CE# CAD10	
X-C7	CE# CAD10	
X-C6	CE# CAD10	
X-C5	CE# CAD10	
X-C4	CE# CAD10	
X-C3	CE# CAD10	
X-C2	CE# CAD10	
X-C1	CE# CAD10	
X-B1	VS# CVS2	
X-B0	VS# CVS1	

QZ711SP2-0N MBGA 196P

 QUANTA COMPUTER		
QZ711 (NC)		
Part QZ711	Document Number FJ3 MAIN BOARD	Rev 1A
Date: 11/13/2008		



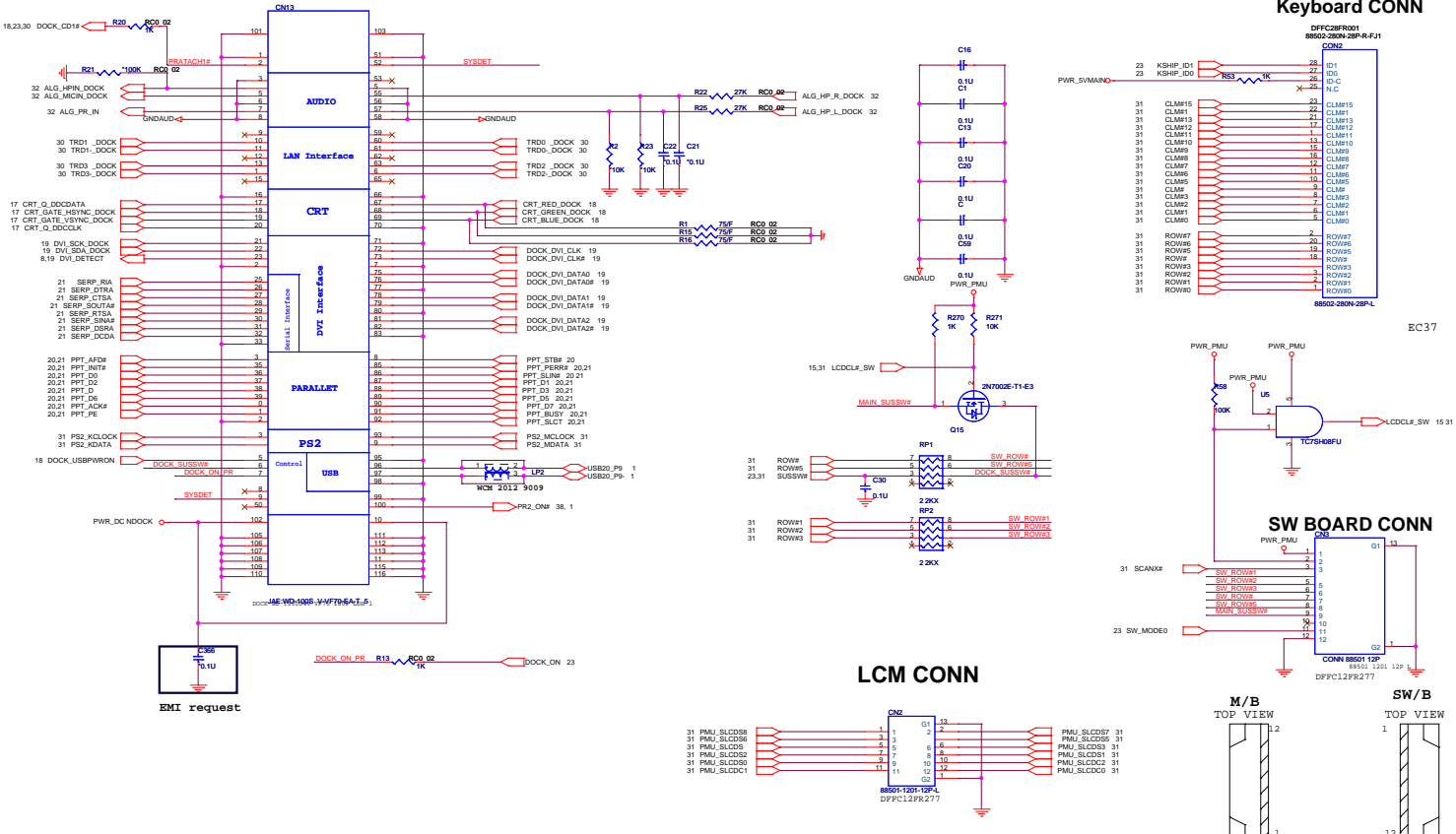
NEW CARD EXPRESS CARD SLOT



QUANTA COMPUTER

NEW CARD

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Doc Ver	FJ3 MAIN BOARD	1A
Doc Date	Tuesday, March 15, 2005	27 of 27



EMI request

QUANTA COMPUTER

DOCKING CN/KB/LCM/SW

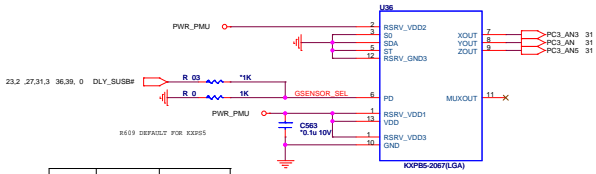
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Doc Date: 11/2005

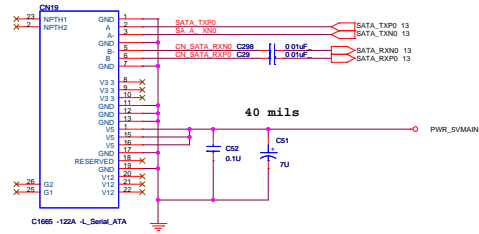
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Doc Date: 11/2005

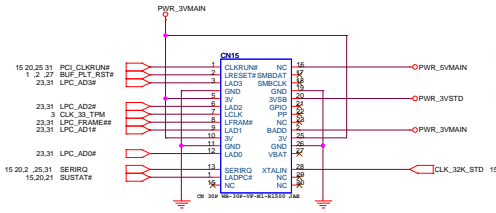
G-SENSOR



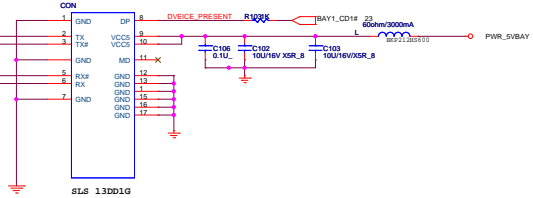
SATA CONN.



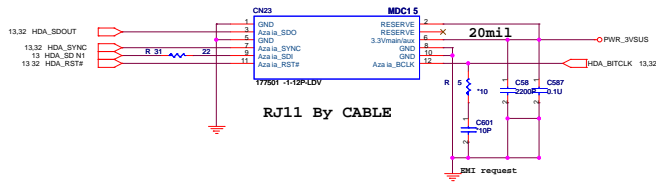
TPM CONN.



ODD CONNECTOR

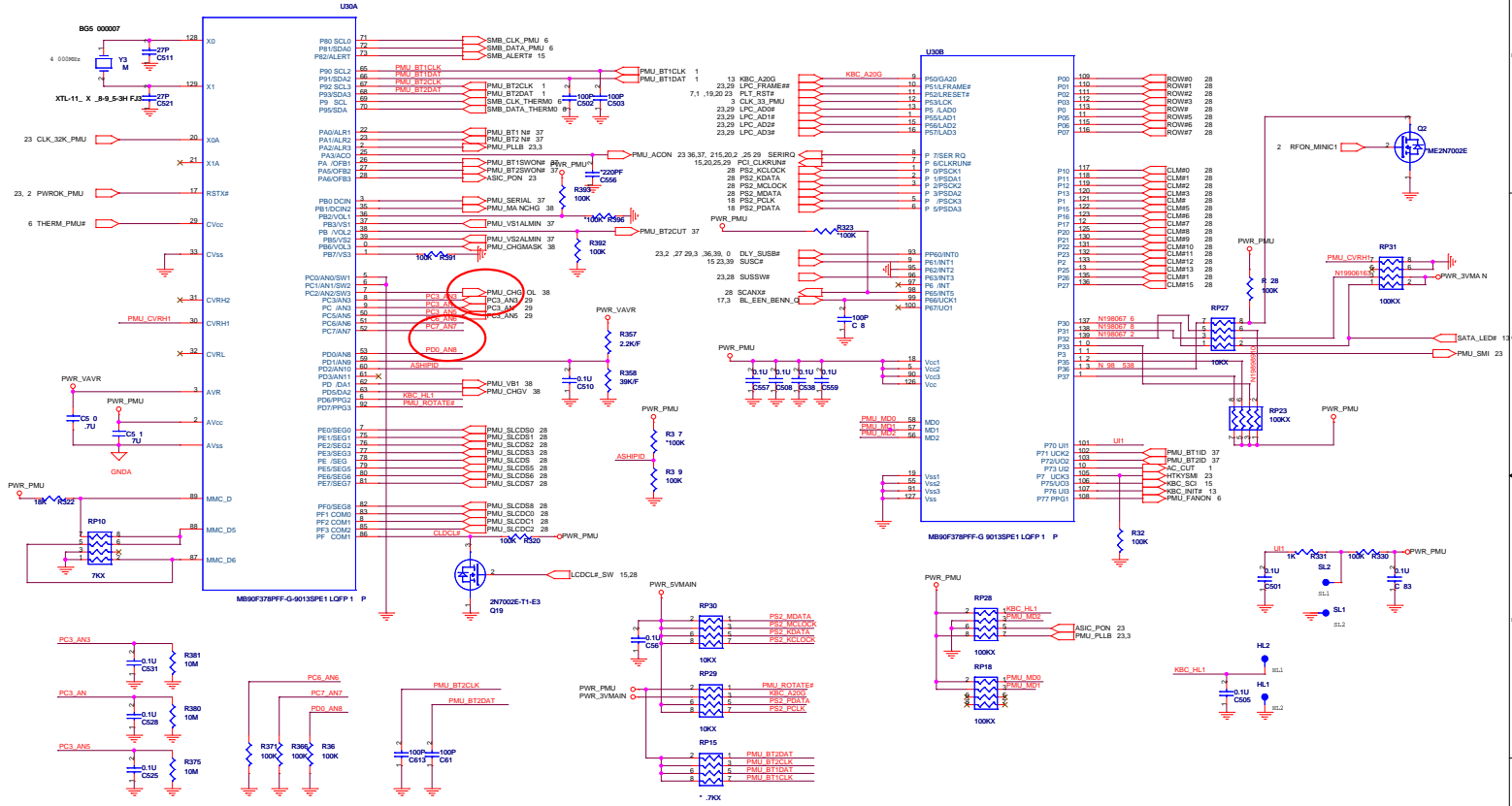


MDC CONN



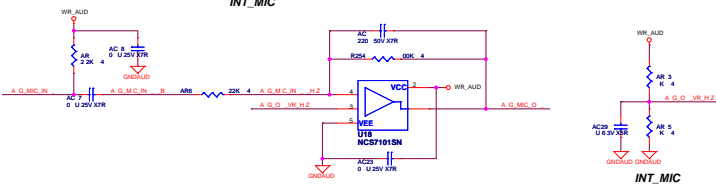
QUANTA COMPUTER
SATA/MDC/TPM/GSENSOR

Doc No	Doc Name	Doc Rev
F32	F33 MAIN BOARD	1A
Doc Date	Doc Rev	Doc Rev
11/2005	2	2

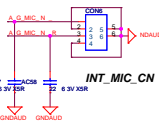


QUANTA COMPUTER
EC Controller
 FJ3 MAIN BOARD
 Document Number: FJ3 MAIN BOARD
 Date: Tuesday, March 15, 2005
 Rev: 1A

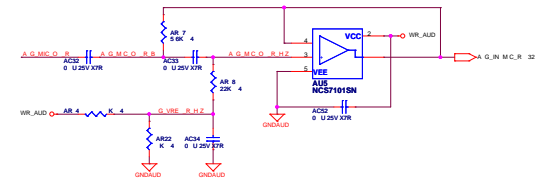
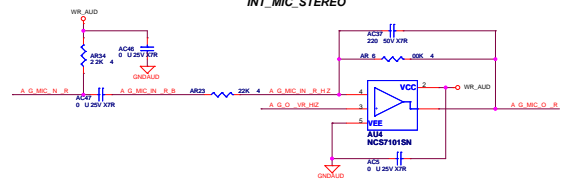
INT_MIC



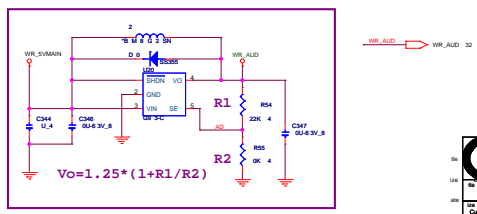
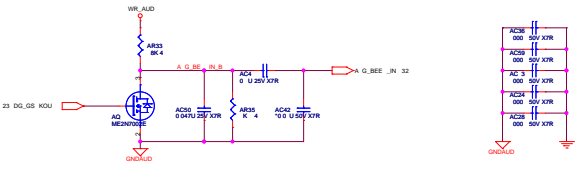
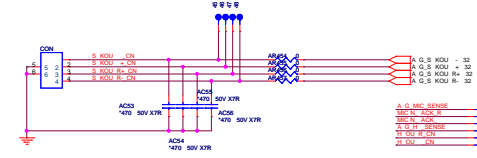
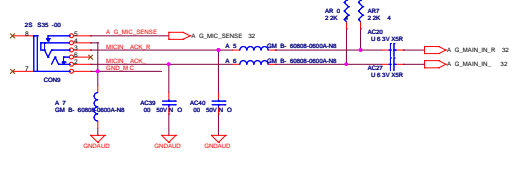
INT_MIC



INT_MIC_STEREO

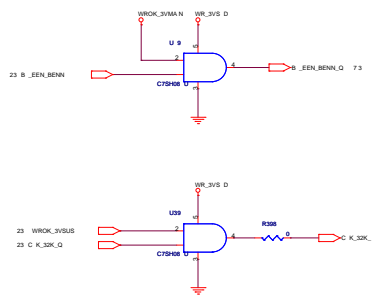
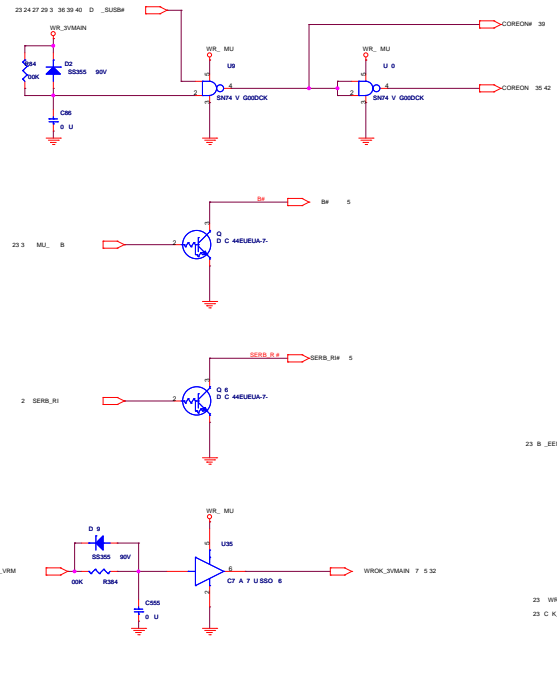
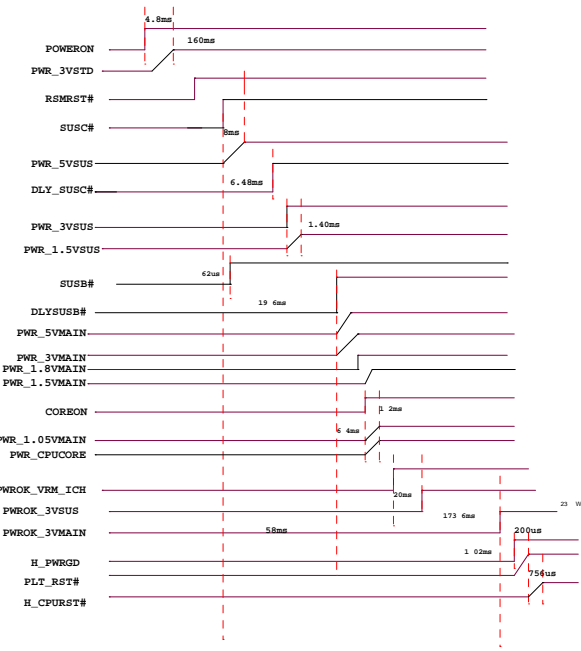


32 A.G.L.VR.L
33 A.G.L.VR.L

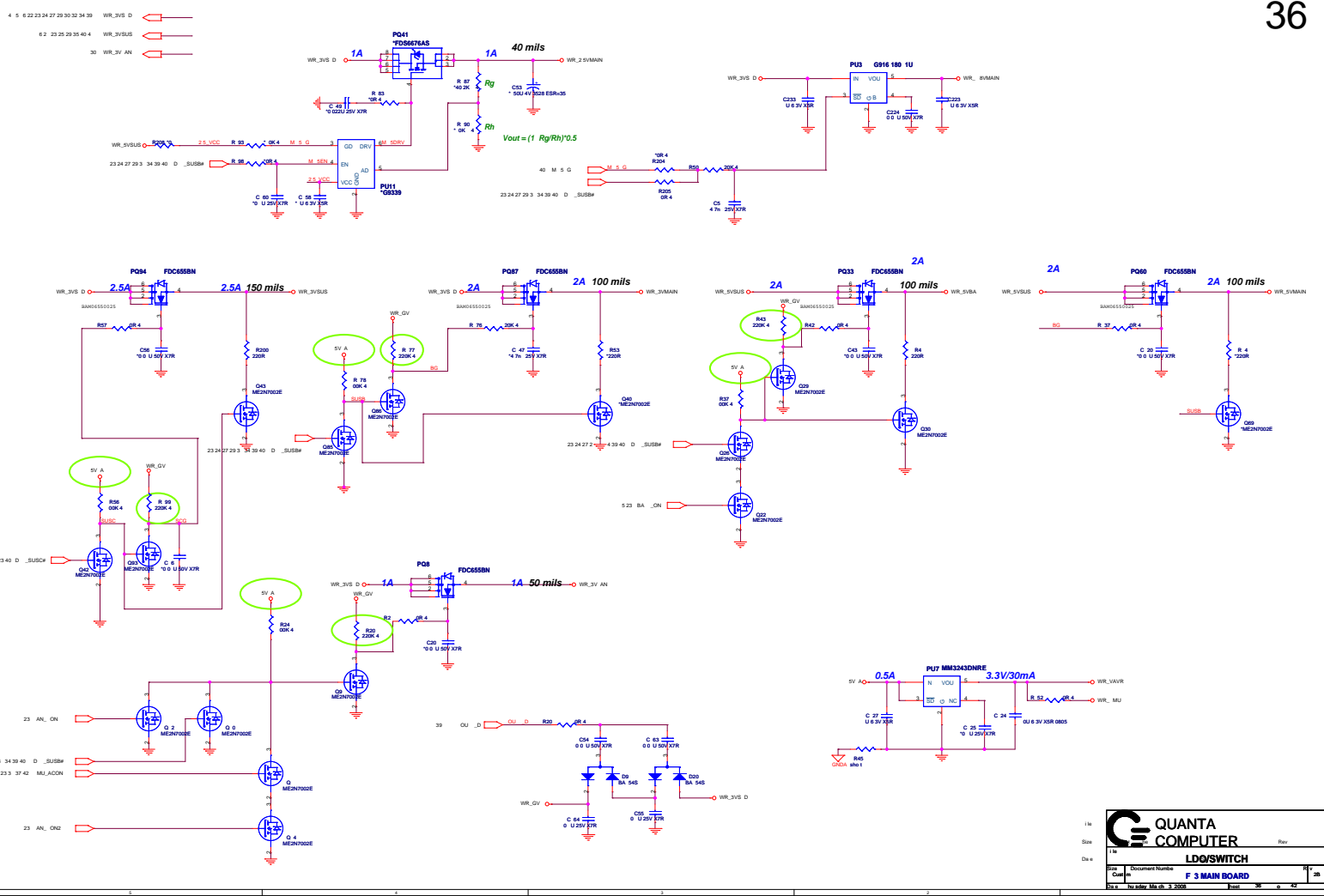


QUANTA COMPUTER
AUDIO AL269 2/2
F.3 MAIN BOARD

FJ3 POWER SEQUENCE



QUANTA COMPUTER
 1st Floor, Document Number: **F.3 MAIN BOARD**
 Date: _____ Rev: _____
 1st Floor, Document Number: **F.3 MAIN BOARD**
 Date: _____ Rev: _____



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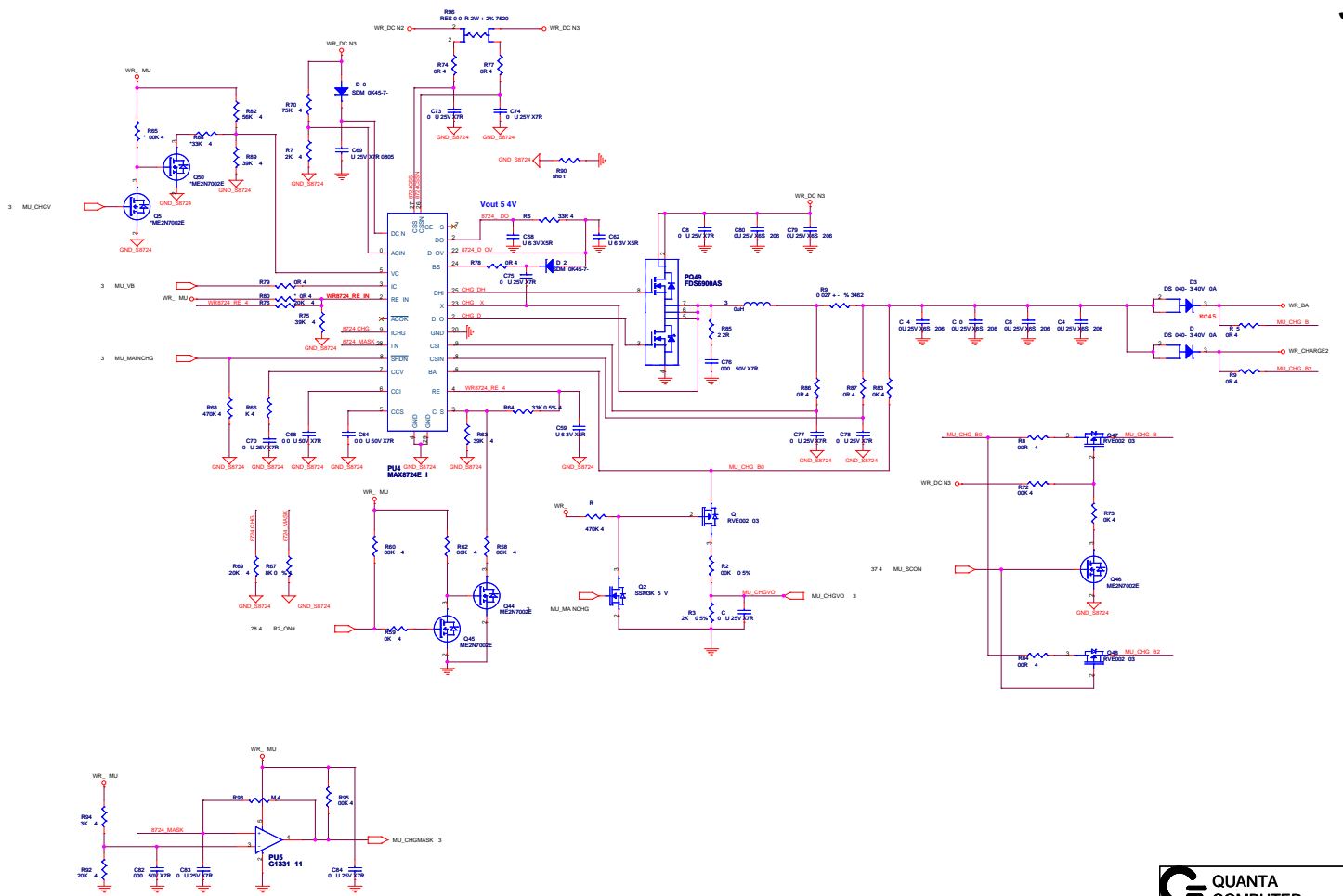
Rev: _____

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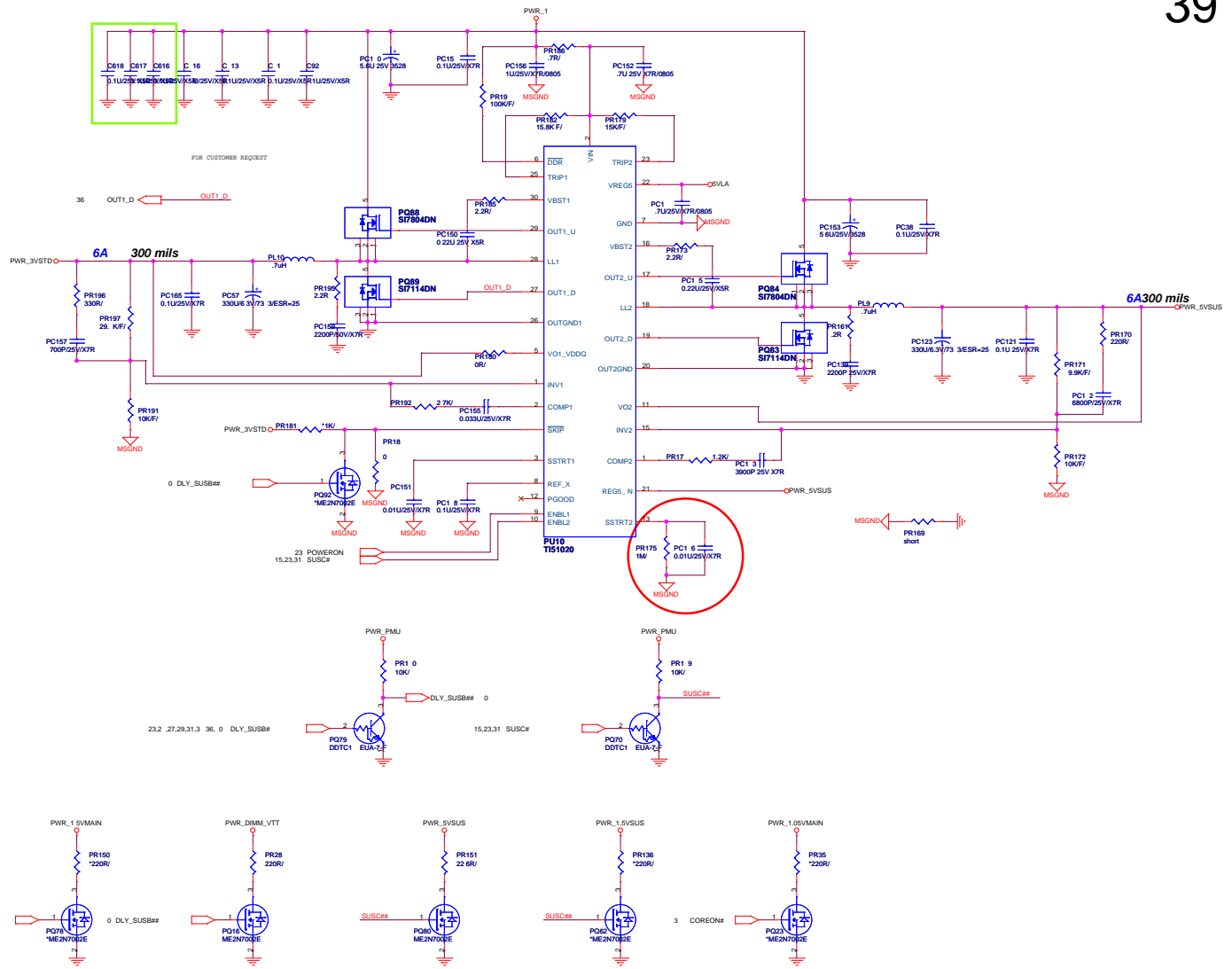
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Rev: _____

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QUANTA COMPUTER	
CHARGER	
Doc. Account Number	REV
Comp	F.3 MAIN BOARD
Doc. No. 2000. Ma. n. 3. 2005	REV. 01

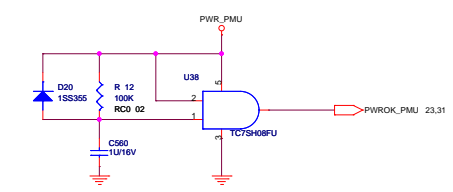
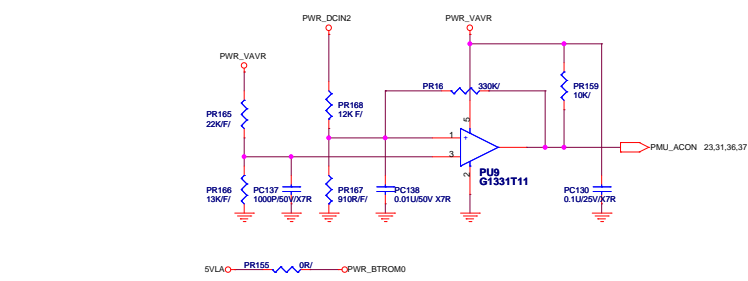
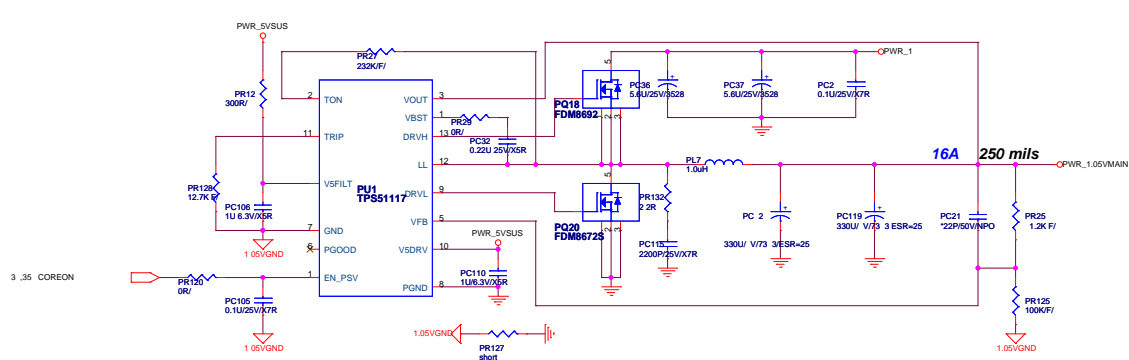


QUANTA COMPUTER

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5VPMU/5VSTD/3VPMU/3VSTD

Size	Document Number	Rev
0.00 in	FJ3 MAIN BOARD	1A
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		1.05V/ETC0	
Title	Document Number	Rev	
S No	FJ3 MAIN BOARD		2B
Date	Thursday, March 13, 2008	Sheet	2 of 2

1 Change list:
 1 Delete Q1 Q11 then copy Q2 to Q3 and Q12
 2 Q11 Footprint change to C1210
 3 Q11 Q18 Footprint change to C1210
 4 Q1 Change footprint R42-04
 5 M Change footprint C1210-11-1-40p-110
 6 R42V R248 move to 88 size and 90 to 90W_200A1W
 Modify R42L cap
 8 Mount R42B R241R40W_200W_VTV discharge circuit
 9 Mount C1210 C1210 R42A R42B and reverse Q12
 10 Change R75 from 4 97K to 4 75K(Interwell recommend)
 11 Remove R41C and add R41A
 12 Change R61 footprint from R07403
 13 Add R62B 1 ohm R0503
 14 Add R62C source sense to 90W_200A1W
 15 Add C1210 C1210 0 1uf for RMI request
 16 Modify R109 off-cage connector
 17 R218 change to 10k and add C1210 20p for fir R2M interference signal quality issue
 18 Update North bridge R1248 pin should be B C pin can't connect to SW
 R218C change out to R.1 100
 19 Mount R269 2 2K for latch error issue
 20 R218 change out to R.1 100
 21 R218C change out to R.1 100
 22 Add R445 0 Ohm for R1A1 recommend schematic
 23 Change C11 pin1 net name to C11_000110
 24 Move C115 5 in to 88 las power
 25 Mount C1210 C1210 C1210 C1210 C1210 for RMI issue
 26 Change RMI to 224-0402



**QUANTA
COMPUTER**
 Change list
 12/12/2012 1:28 PM BA A 1