LCFC Confidential

BE560 Rev1.0 Schematic

Intel SkyLake Processor with DDRIIIL + PCH-LP

AMD Litho XT GDDR5 2GB

2015-07-28 Rev1.0

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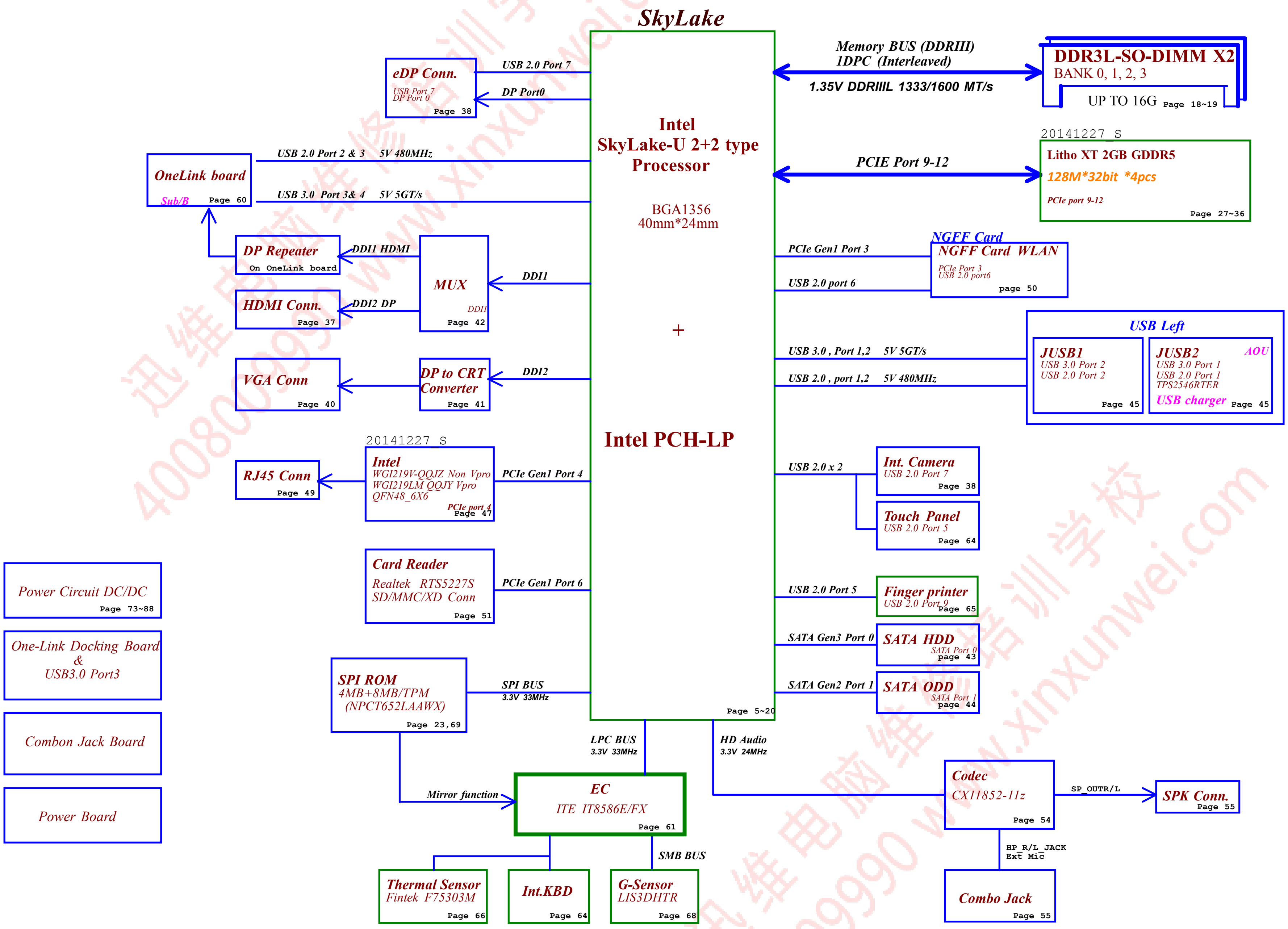

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Title: BLOCK DIAGRAM

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Voltage Rails (O --> Means ON , X --> Means OFF)

Power Plane / State	B+	+3VALW +5VALW +1.8VALW +1VALW	+1.35V	+5VS +3VS +VCC_CORE +VCC_IO +VCC_SA +VCC_ST +VCC_STG +VGA_CORE +3VS_VGA +1.8VS_VGA +1.35VS_VGA +0.675VS
S0	O	O	O	O
S3	O	O	O	X
S5 S4/AC Only	O	O	X	X
S5 S4 Battery only	O	X	X	X
S5 S4 AC & Battery don't exist	X	X	X	X

STATE / SIGNAL	SLP_A#	SLP_S3#	SLP_S4#	SLP_S5#	EC_ON2	EC_ON	SUSP#
Full ON	HIGH	HIGH	HIGH	HIGH	ON	ON	ON
S1(Power On Suspend)	HIGH	HIGH	HIGH	HIGH	ON	ON	ON
S3 (Suspend to RAM)	LOW	LOW	HIGH	HIGH	ON	ON	OFF
S4 (Suspend to Disk)	LOW	LOW	LOW	HIGH	ON	ON	OFF
S5 (Soft OFF)	LOW	LOW	LOW	LOW	ON	ON	OFF

SMBUS Control Table

	SOURCE	Main VGA	BATT	SODIMM	WLAN WiMAX	Thermal Sensor	PCH	CP Module	Security ROM	LAN PHY	G sensor
EC_SMB_CK1 EC_SMB_DA1	IT8580F +3VL	X	V +3VALW	X	X	X	X	X	X	X	X
EC_SMB_CK3 EC_SMB_DA3	IT8580F +3VS	V +3VS_VGA	X	X	X	V +3VS	V +3V_PCH	X	X	X	V +3VALW_GS
PCH_SMB_CLK PCH_SMB_DATA	PCH +3V_PCH	X	X	V +3VS	X	X	X	V +5VS	V +3VS	X	X
PCH_SML0_CLK PCH_SML0_DAT	PCH +3V_PCH	X	X	X	X	X	X	X	X	V +3VALW	X

USB2 Port

Port	Device
1	JUSB2
2	JUSB3
3	Sub Board
4	Docking
5	Touch Panel
6	BT
7	CMOS
8	FP/Smart

USB3 Port

Port	Device
1	JUSB2
2	JUSB3
3	Sub Board
4	Docking
5	3D CCD(PCIE1)

PCIE Port

Port	Device
1	3D CCD(USB3)
2	X
3	WLAN
4	LAN
5	X
6	CardReader
7	X
8	X
9	GPU
10	GPU
11	GPU
12	GPU

SATA Port

Port	Device
1	HDD
2	ODD
3	X
4	X

Title: NOTE LIST
Size: Custom Document Number: BE560 Rev: 0.1
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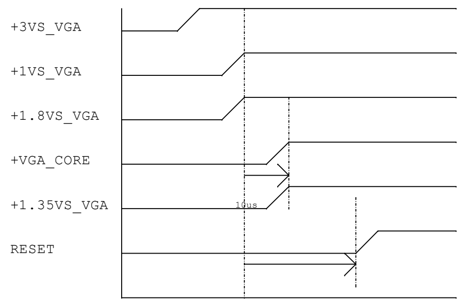
VGA and DDR3 Voltage Rails (Litho XT 2GB DDR3)

20141227_S

GPIO	I/O	ACTIVE	Function Description
GPIO0	OUT	N/A	
GPIO5	IN	-	GPIO5_AC_BATT
GPIO6	IN	-	GPIO6
GPIO7	OUT	N/A	
GPIO8	OUT	-	GPIO8_ROMSO
GPIO9	OUT	-	GPIO9_ROMSI
GPIO10	OUT	-	GPIO10_ROMSCK
GPIO11	OUT	N/A	
GPIO12	OUT	N/A	
GPIO13	OUT	N/A	
GPIO15	IN	N/A	SVI2_SVD
GPIO16	OUT	N/A	
GPIO17	OUT	N/A	
GPIO19	OUT	N/A	GPIO19_CTF
GPIO20	IN	IN	GPIO20
GPIO21	OUT	N/A	
GPIO22	OUT	N/A	GPIO22_ROMCSB
GPIO29	OUT	N/A	
GPIO30	OUT	N/A	



1. all power rail ramp up time should be within 20ms

	Device ID
JET-XT	0xFFFF

	setting	I2C Slave addrees ID
SMB_ALT_ADDR (ROM_SO Bit 1)	0	0xFF
	1	0xFF

BOM Structure Table

BOM Structure	NOTE
PCB@	For PCB load BOM
XDP@	Debug port
UMA@	UMA SKU ID
DIS@	Optimus SKU ID
DIMM2@	For DIMM2 function
DIMM1@	For DIMM1 function
VPRO@	For VPRO function
ME@	ME Connector
EMC@	For EMC function
EMC_2D@	For EMC function
EMC_NS@	For EMC function
RF_NS@	For RF function
S2G@	For VRAM Strap
CHA@	For VRAMA function
CHB@	For VRAMB function
RANKA@	GPU DDR5 Setting
X76@	GPU VRAM Setting
3DCCD@	3D Camera Setting
VGA@	VGA Setting
MUX@	MUX Setting
ODD@	ODD Setting
TPM@	Trusted Platform Module (TPM)
NVPRO@	For Non-VPRO function
MIRROR@	For mirror function

GPU		Litho XT 2GB DDR3	
FB Memory (DDR3L)		PS_3 (RV114)	PS_3 (RV117)
Samsung 1000MHz	K4W4G1646D-BC1A		
	256Mx16	PH 3.4K	PD 10K
Hynix 1000MHz	H5TC4G63AFR-11C		
	256Mx16	PH 4.75K	NC
Micro 1000MHz	MT41J256M16HA-093G		
	256Mx16	PH 3.24K	PD 5.62K

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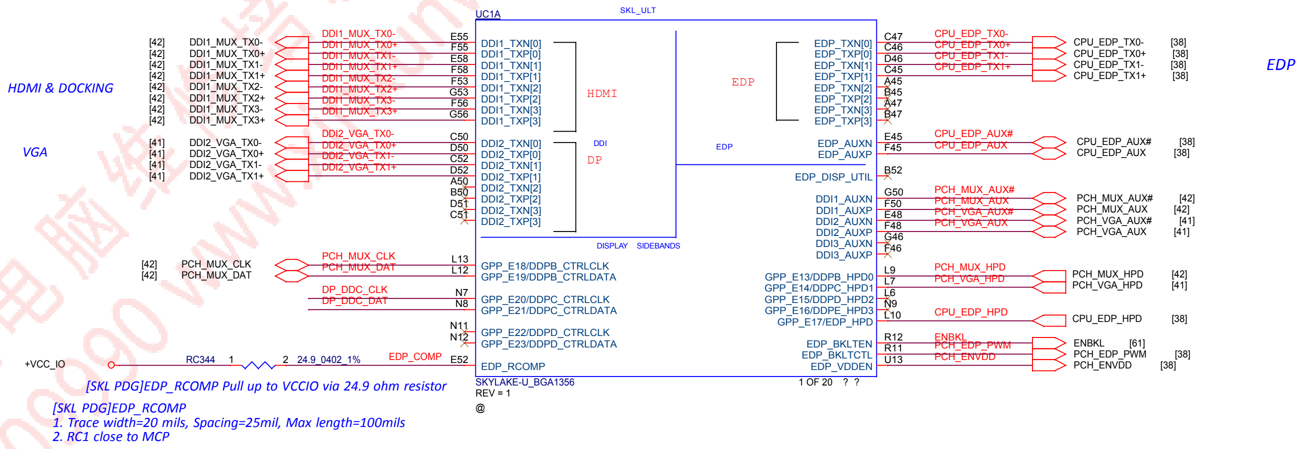
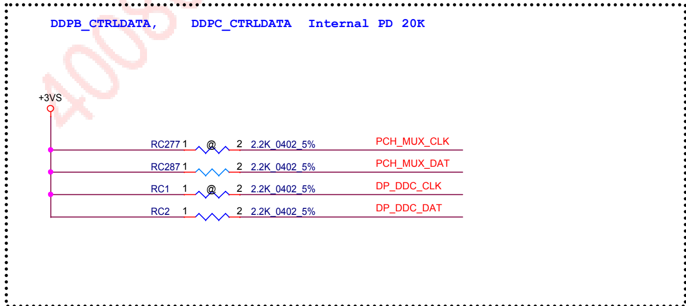
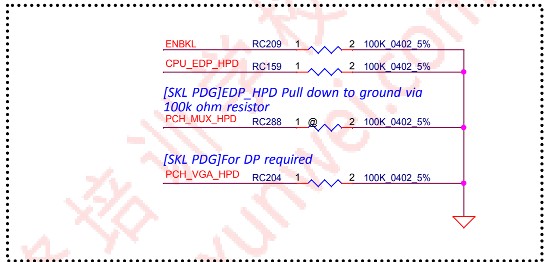




DDPB_CTRLDATA	Port B Detected	This signal has an integrated weak pull-down (20 KΩ nominal) resistor. When this signal is pulled up to VCC3_3 through a 1−3.6 KΩ ±5% resistor at the rising edge of PCH_PPWROK the Digital Display Port B will be detected.
DDPC_CTRLDATA	Port C Detected	This signal has an integrated weak pull-down (20 KΩ nominal) resistor. When this signal is pulled up to VCC3_3 through a 1−3.6 KΩ ±5% resistor at the rising edge of PCH_PPWROK the Digital Display Port C will be detected.



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Title: SKL(1/16):DDI/EDP

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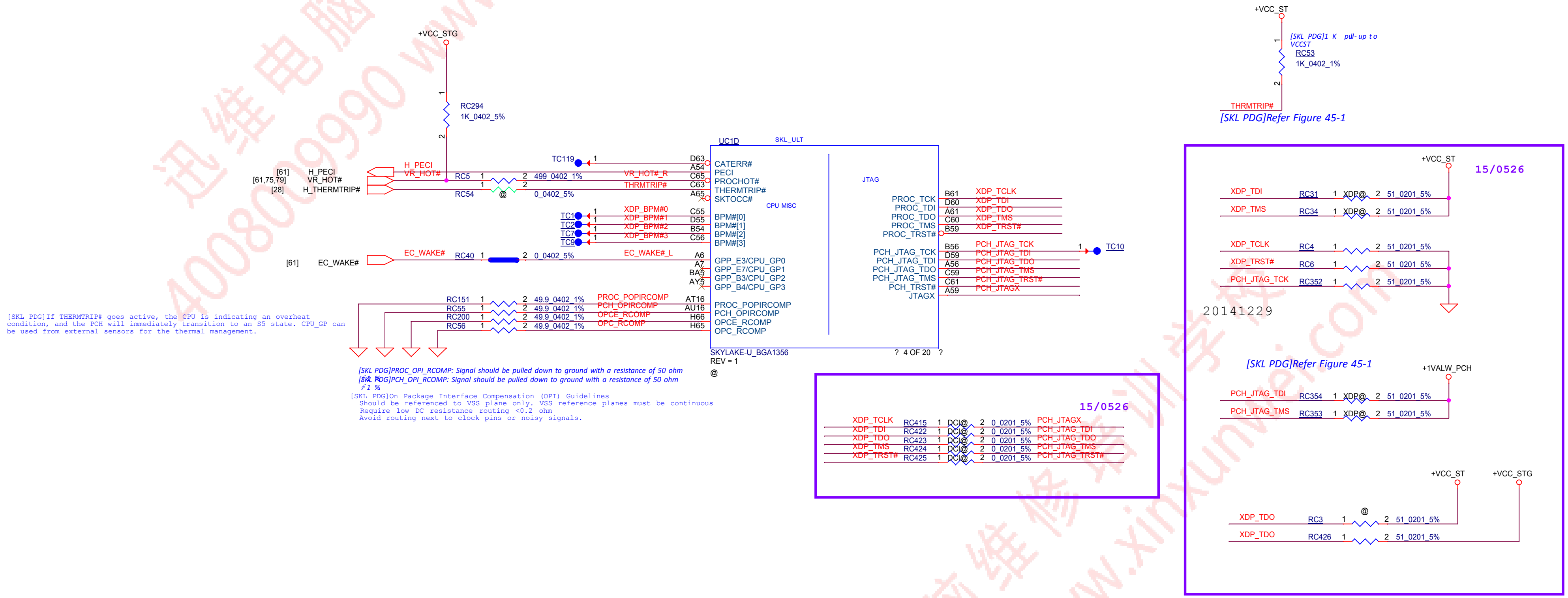

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Title
SKL(2/16):MISC/JTAG

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[25] DDR_A_D[0..63]
[25] DDR_A_DQS#[0..7]
[25] DDR_A_DQS[0..7]
[25] DDR_A_MA[0..15]

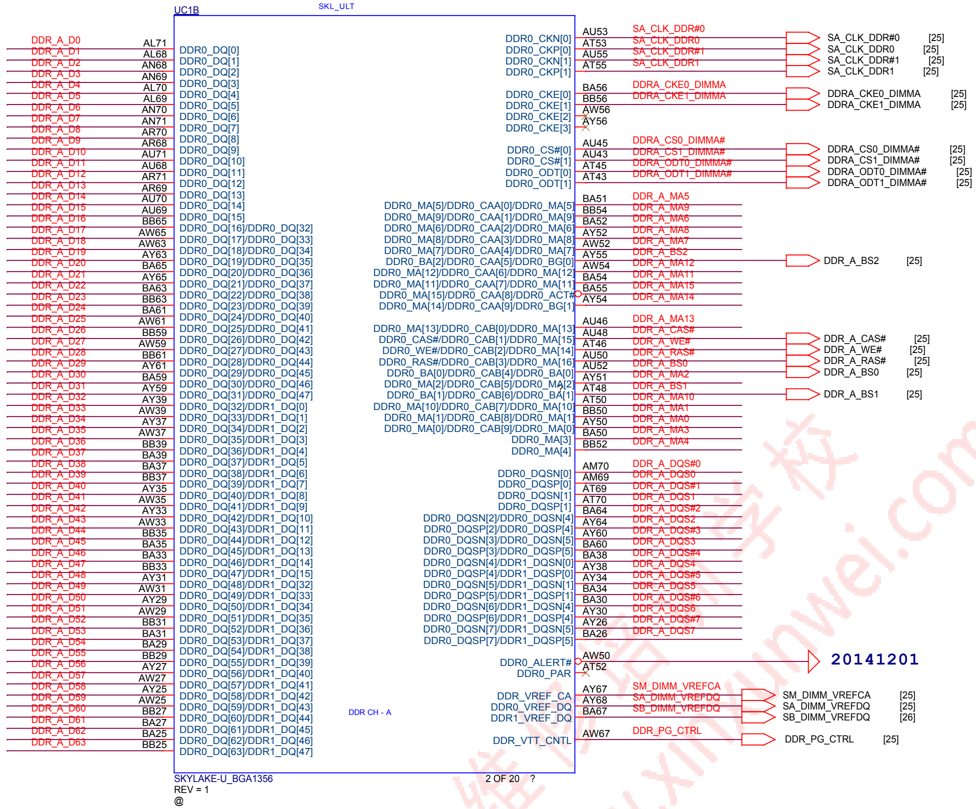
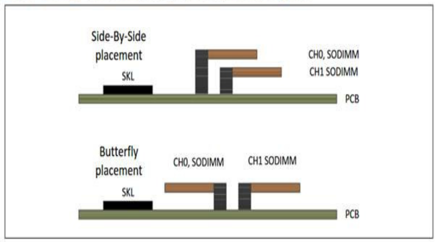


Figure 4-14. SKL U DDR3L/-RS SODIMM T3/8L IL Placement Options



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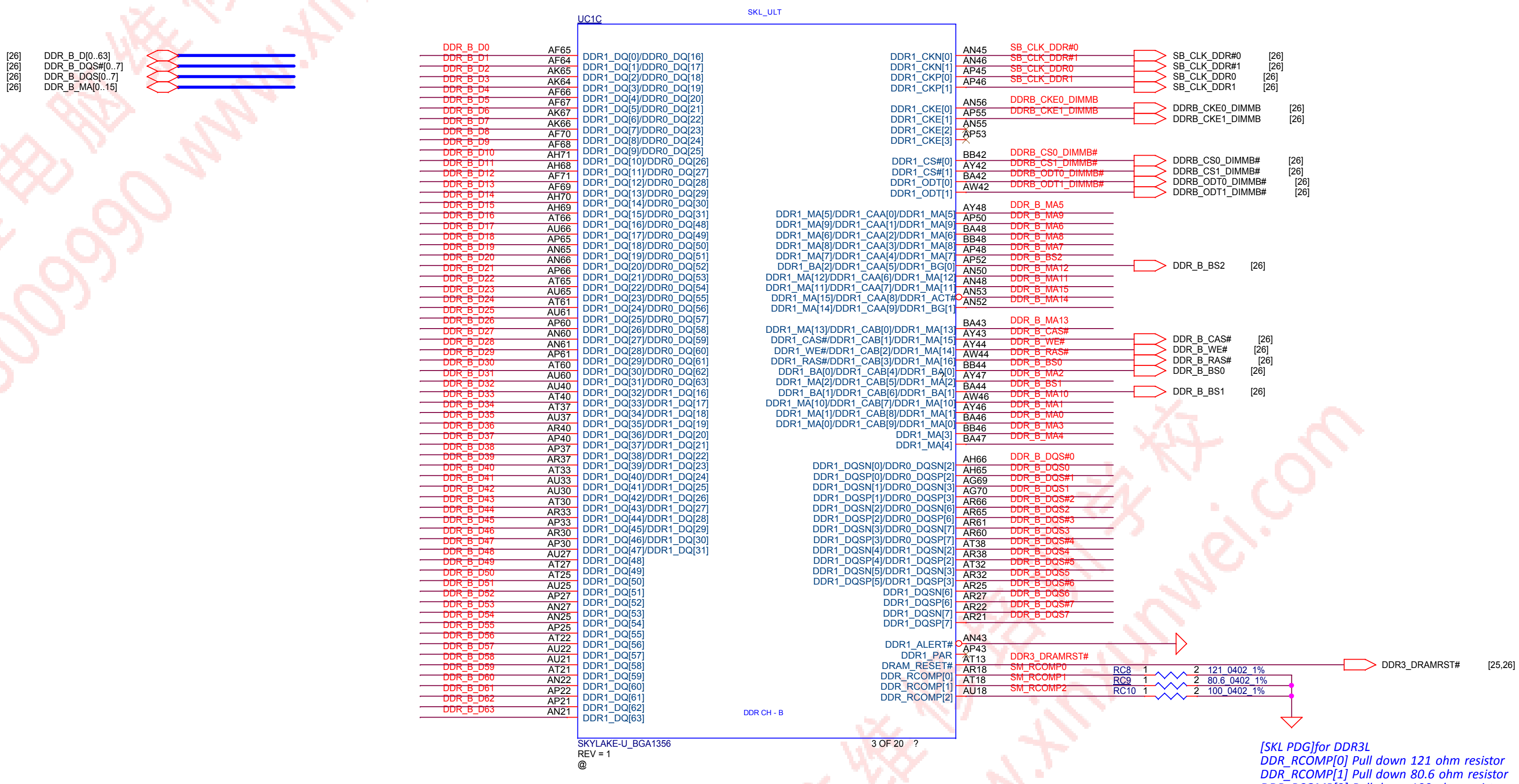

[SKL PDG]for DDR3L
DDR_RCOMP[0] Pull down 121 ohm resistor
DDR_RCOMP[1] Pull down 80.6 ohm resistor
DDR_RCOMP[2] Pull down 100 ohm resistor

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Title: SKL(4/16):DDR3L CH.B

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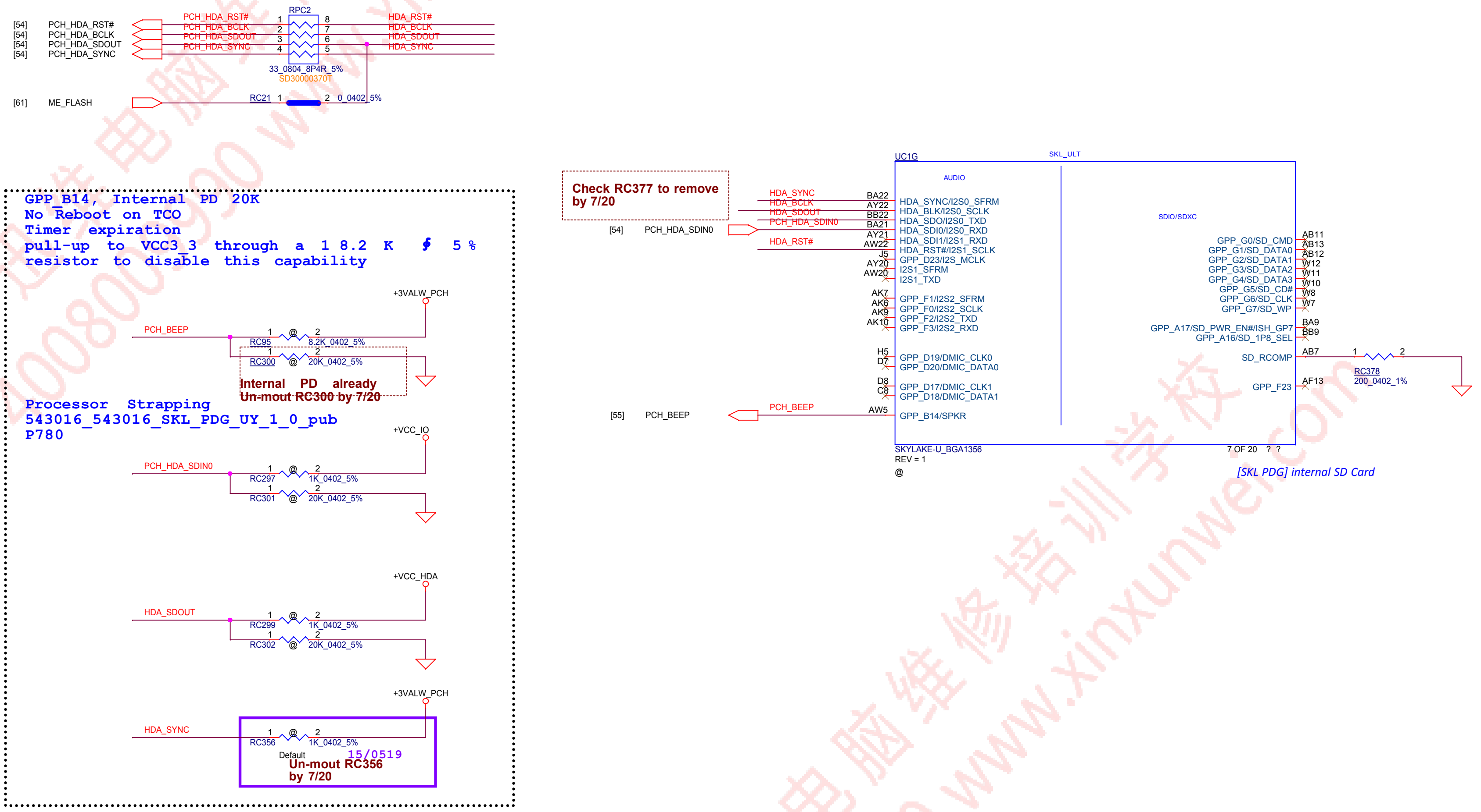
[SKL PDG]Manufacturing Mode Jumper
1. If strap is sampled low, the security measures defined in the Flash Descriptor will be in effect (default)
2. If sampled high, the Flash Descriptor Security will be overridden.



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Title: SKL(5/16):HDA/SDIO

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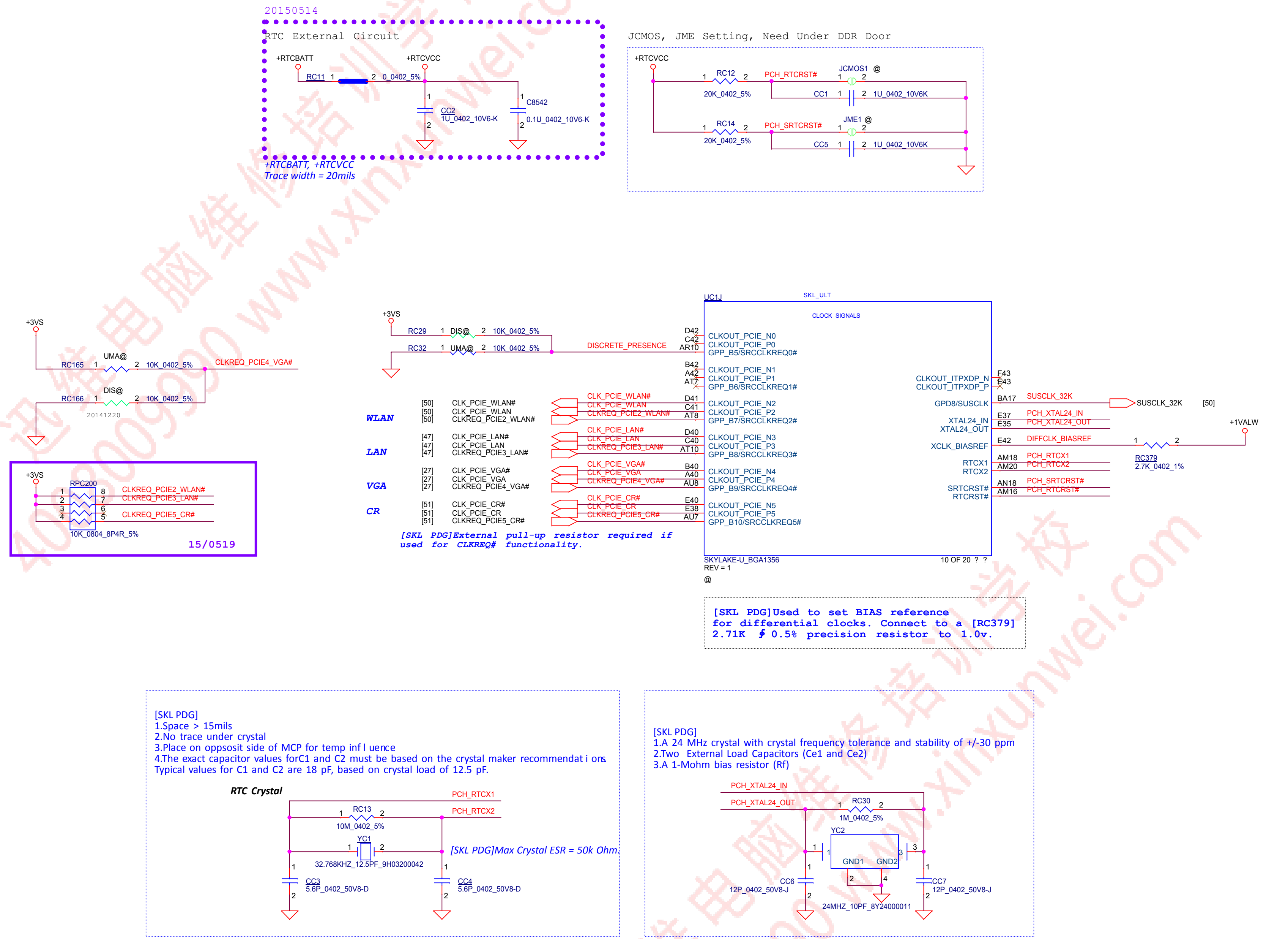

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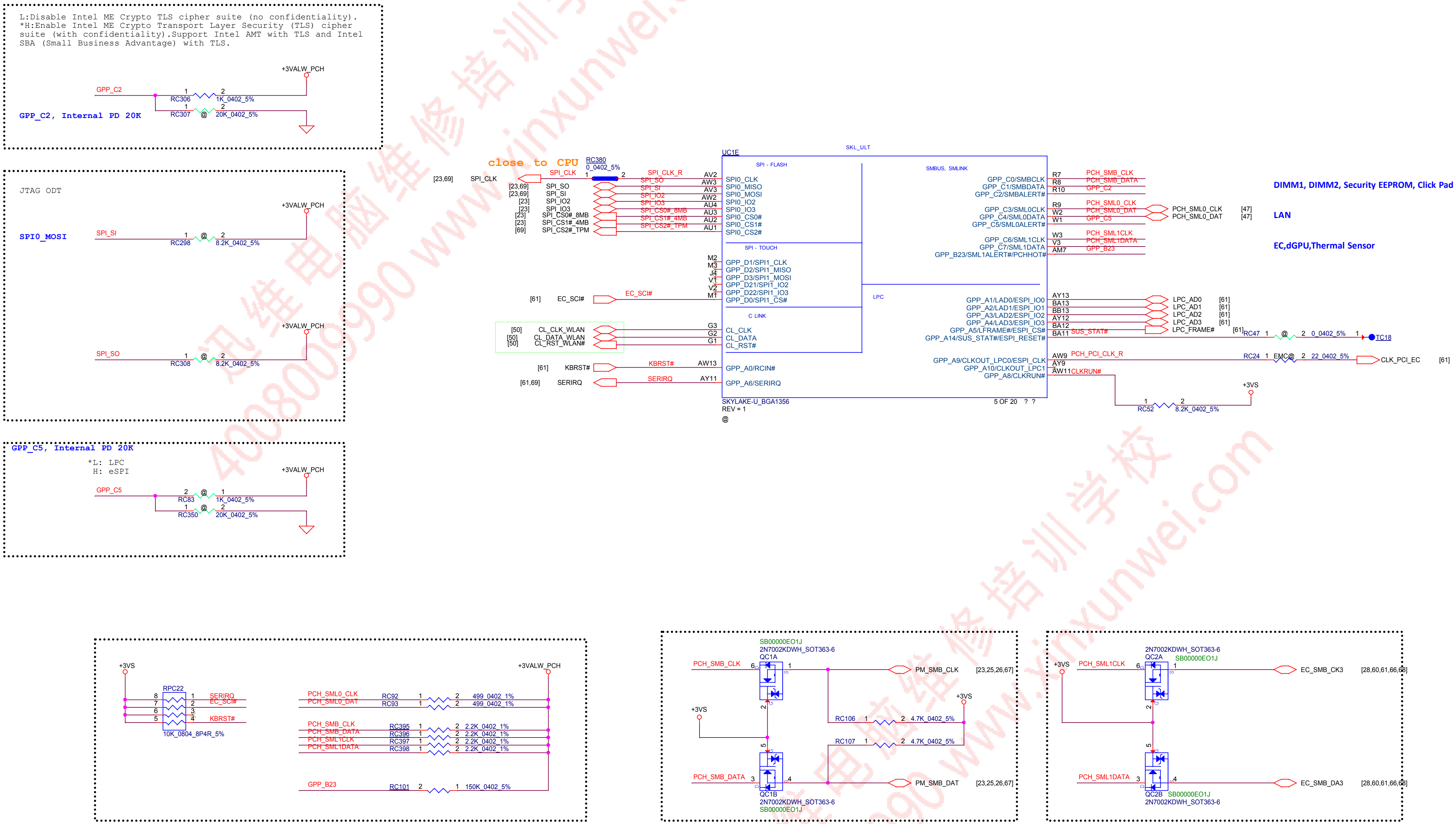
Functional Strap Definitions



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Title: SKL(7/16):LPC/SPI/SMBUS/CL

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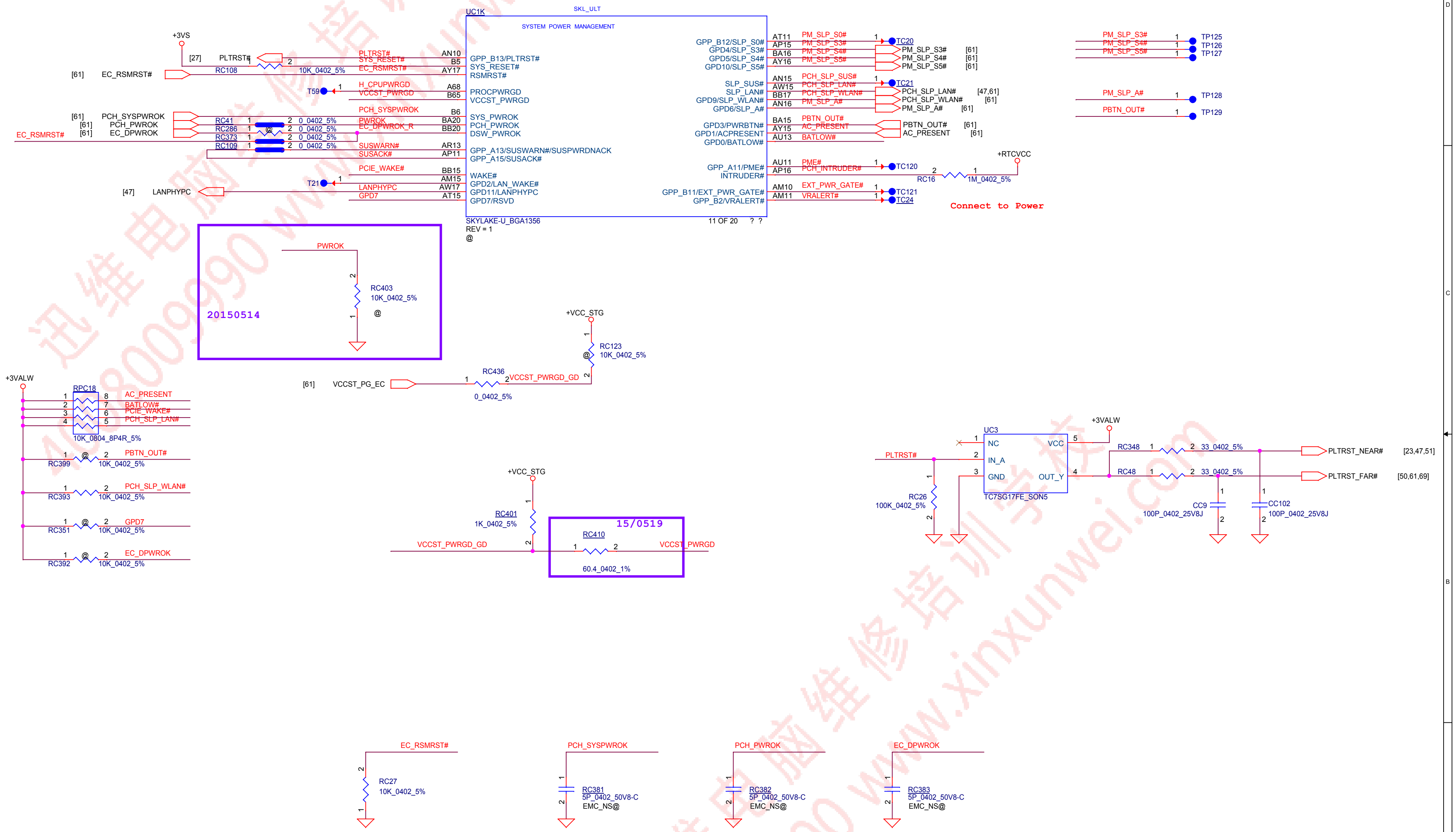

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Title: **SKL(8/16):SYSTEM PM**

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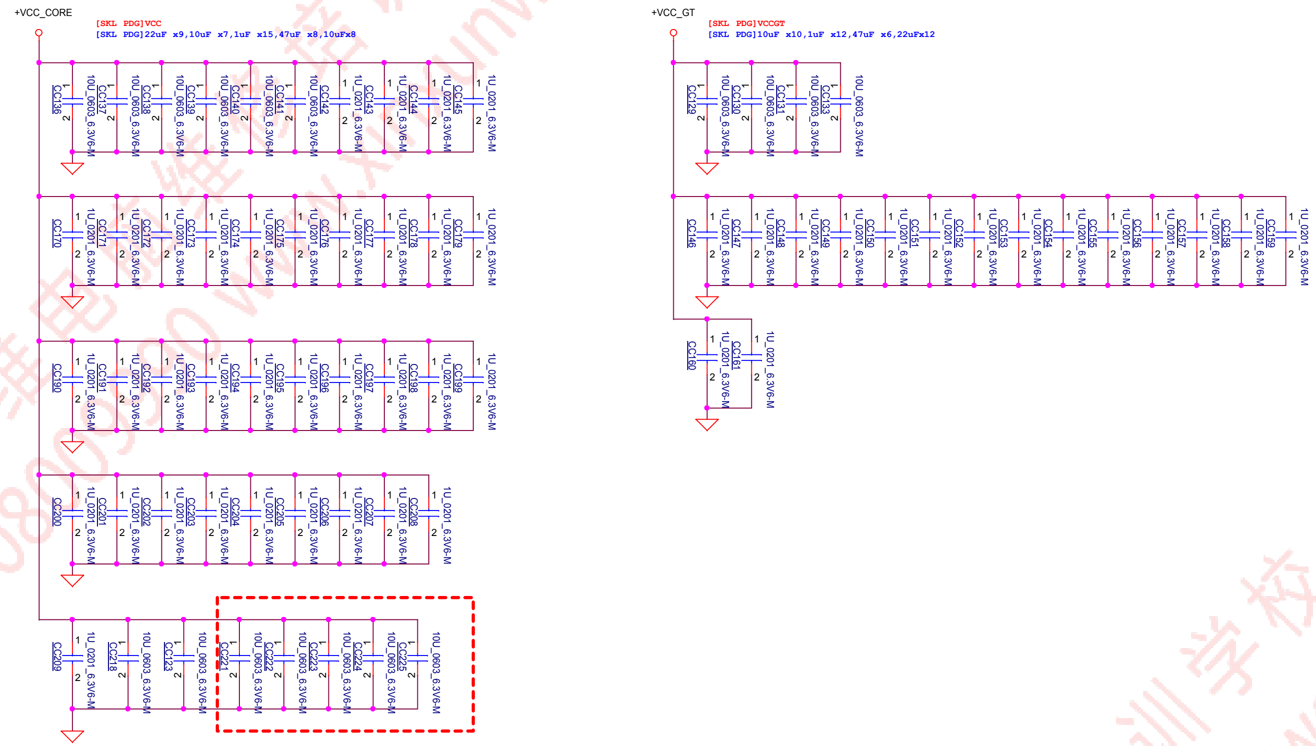

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Title
SKL(9/16):Decoupling

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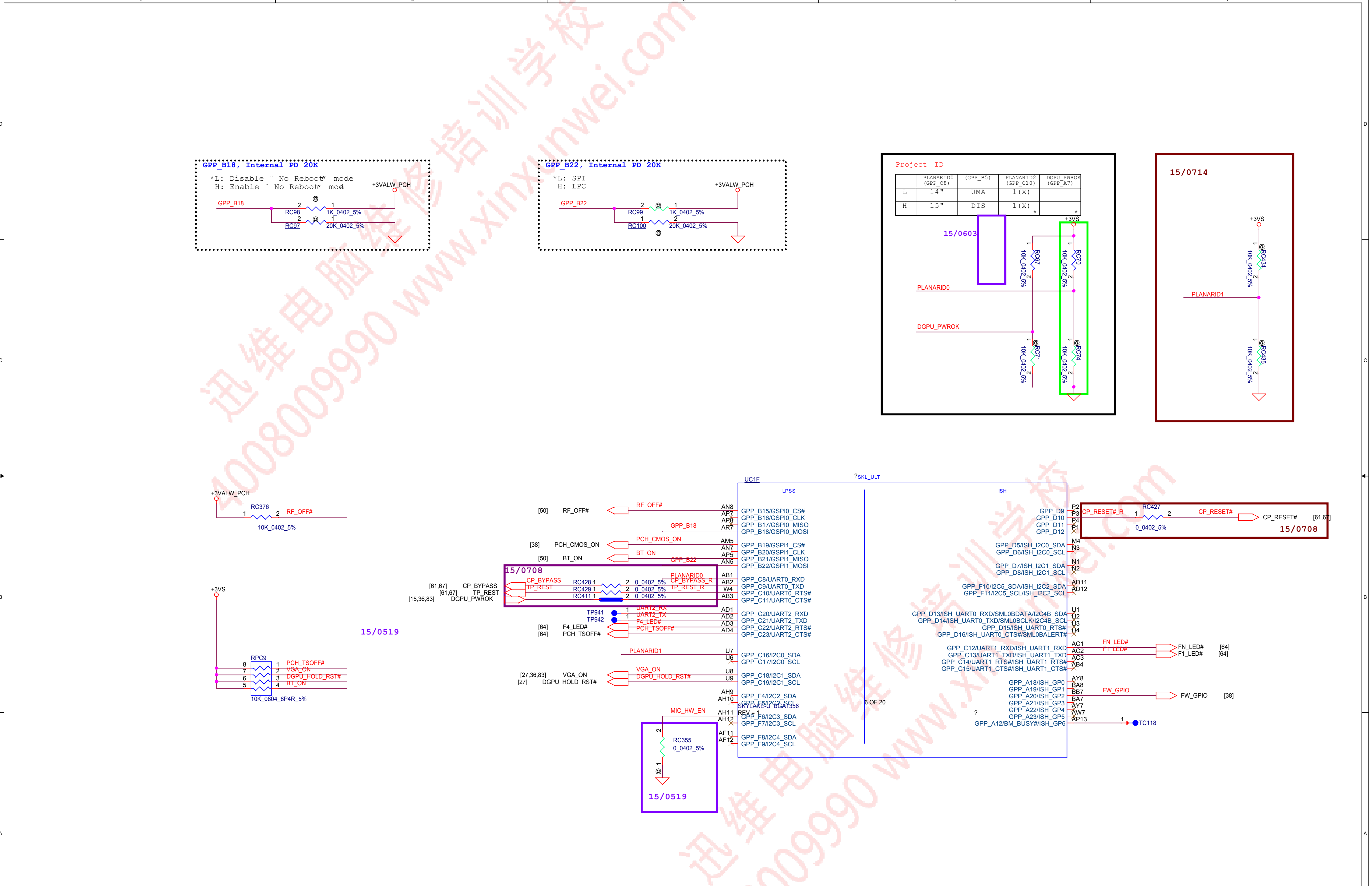

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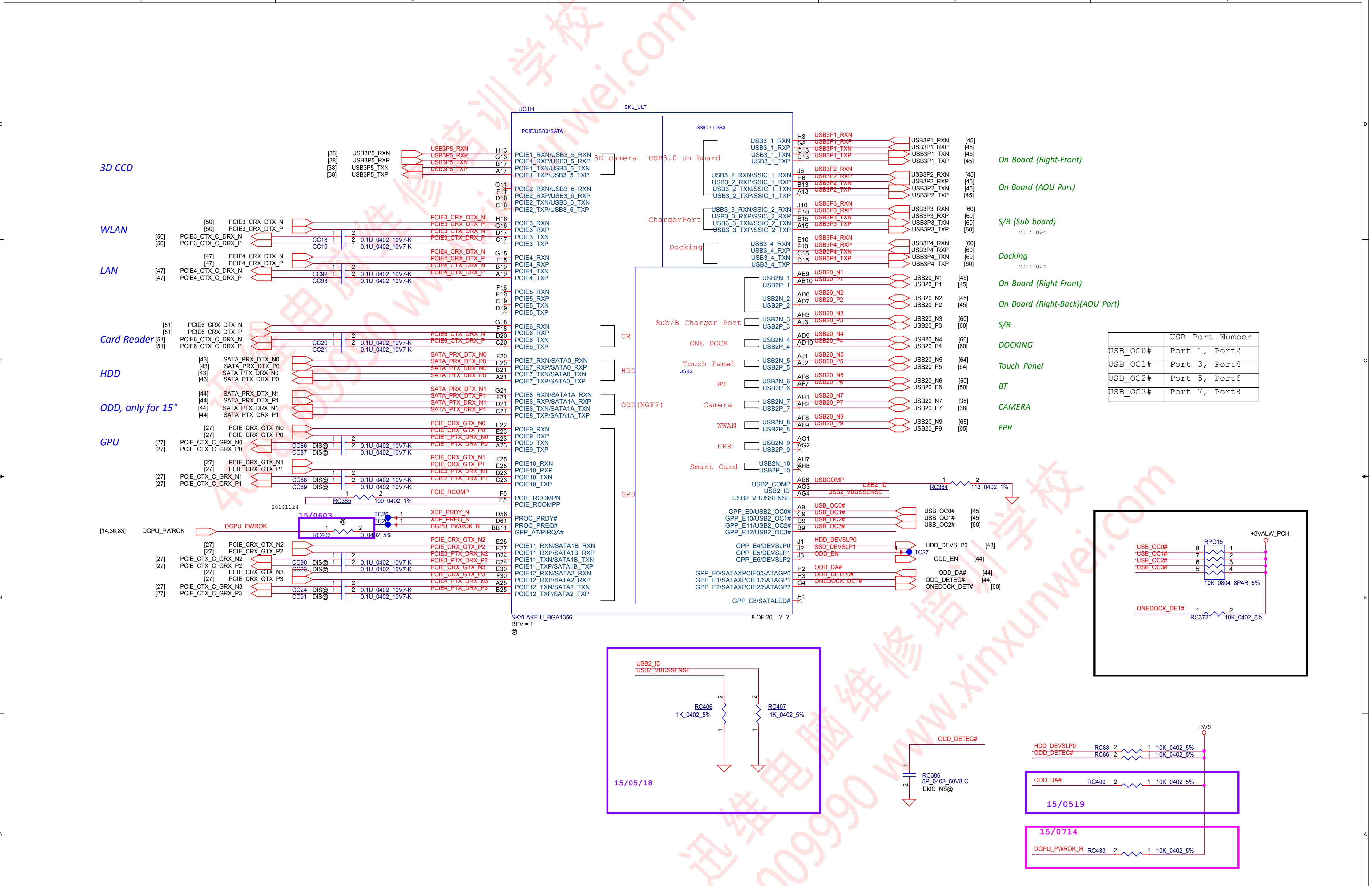

	USB Port Number
USB_OC0#	Port 1, Port2
USB_OC1#	Port 3, Port4
USB_OC2#	Port 5, Port6
USB_OC3#	Port 7, Port8

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Title: SKL(11/16):PCIE/USB/SATA

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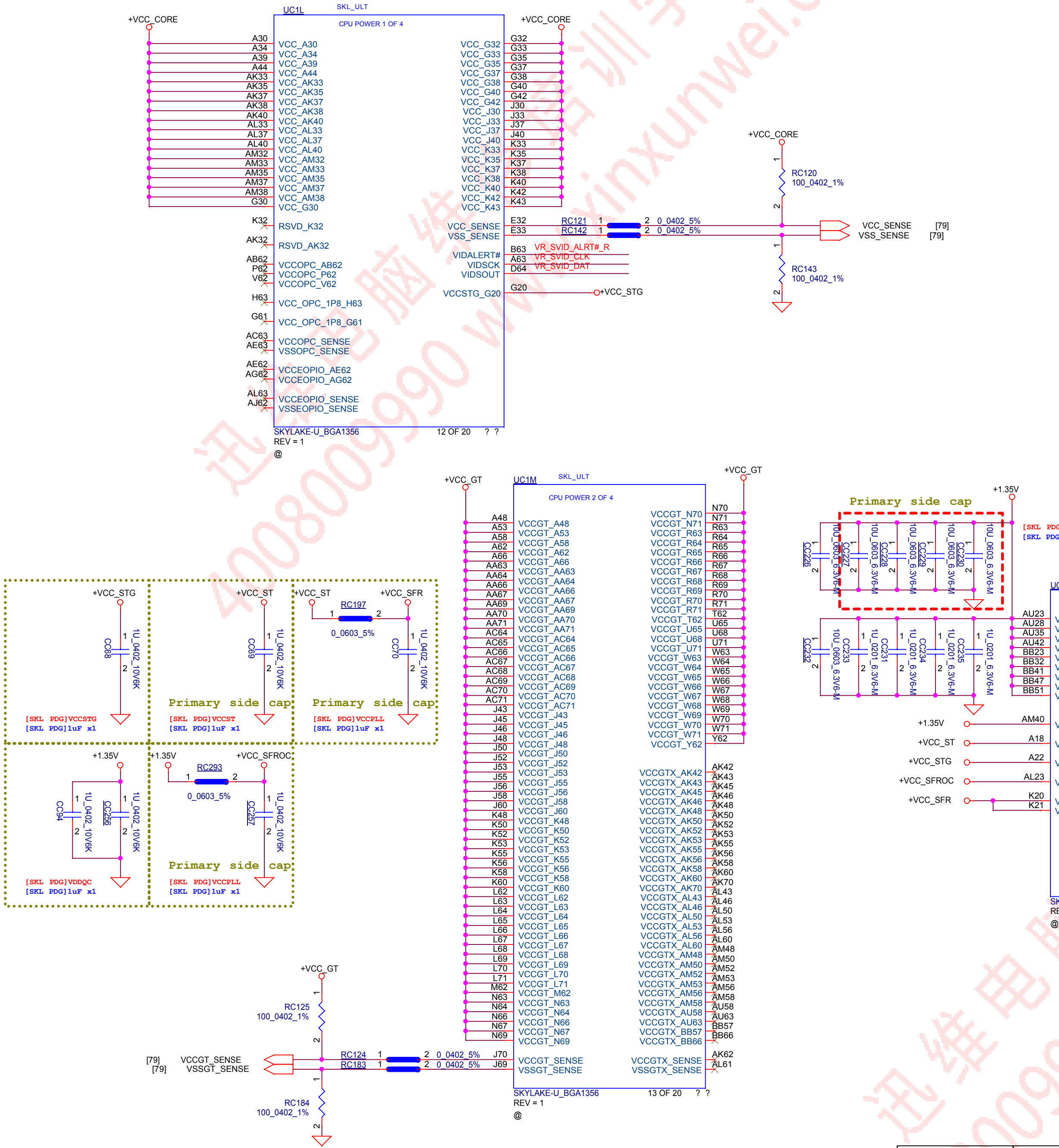
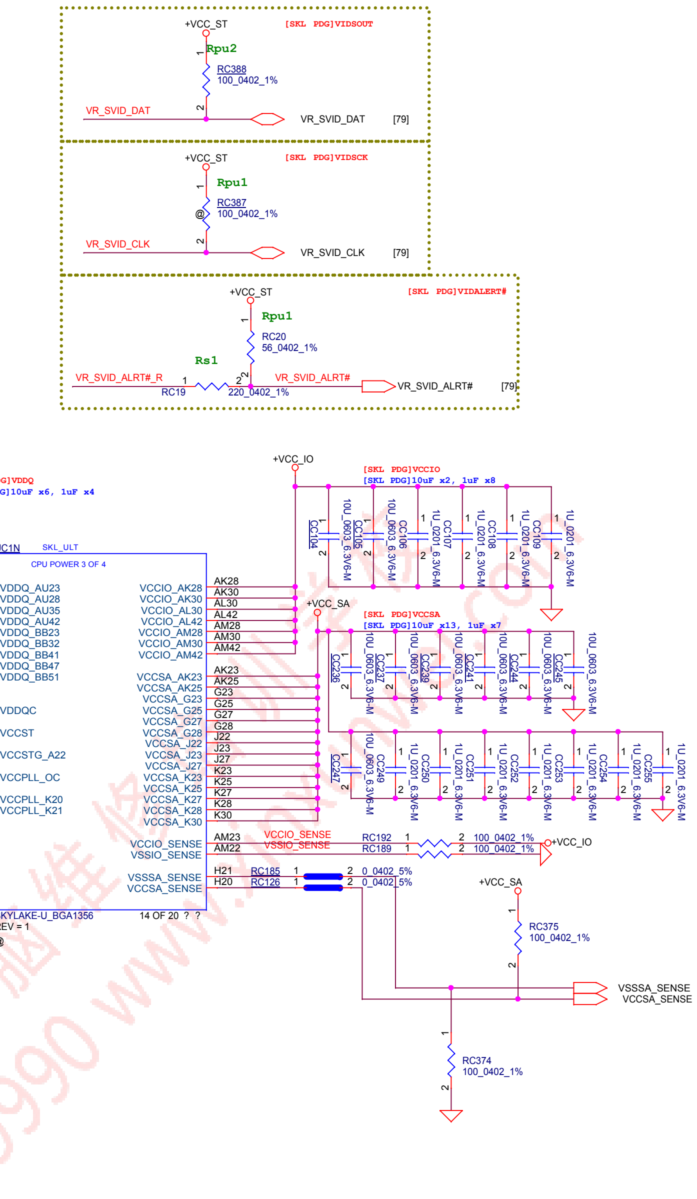


Table 10-10. SVID Bus Routing Guidelines

Signal	W1 [inches]	W2 [inches]	W3/4/5 [inches]	W2+W3+W4+W5 [inches]	W51 [inches]	W52 [inches]	R_{PU1} [Ω]	R_{PU2} [Ω]	R_{S1} [Ω]	R_{S2} [Ω]	VCC_{ST} [v]
VIDSOUT	0.5-3	1-15	0.5-4	3-17	<0.1	<0.1	100	100	0	10	1.0
VIDSCK							Empty	45	0	50	
VIDALERT#							56	Empty	220	0	



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Title: SKL(12/16):POWER

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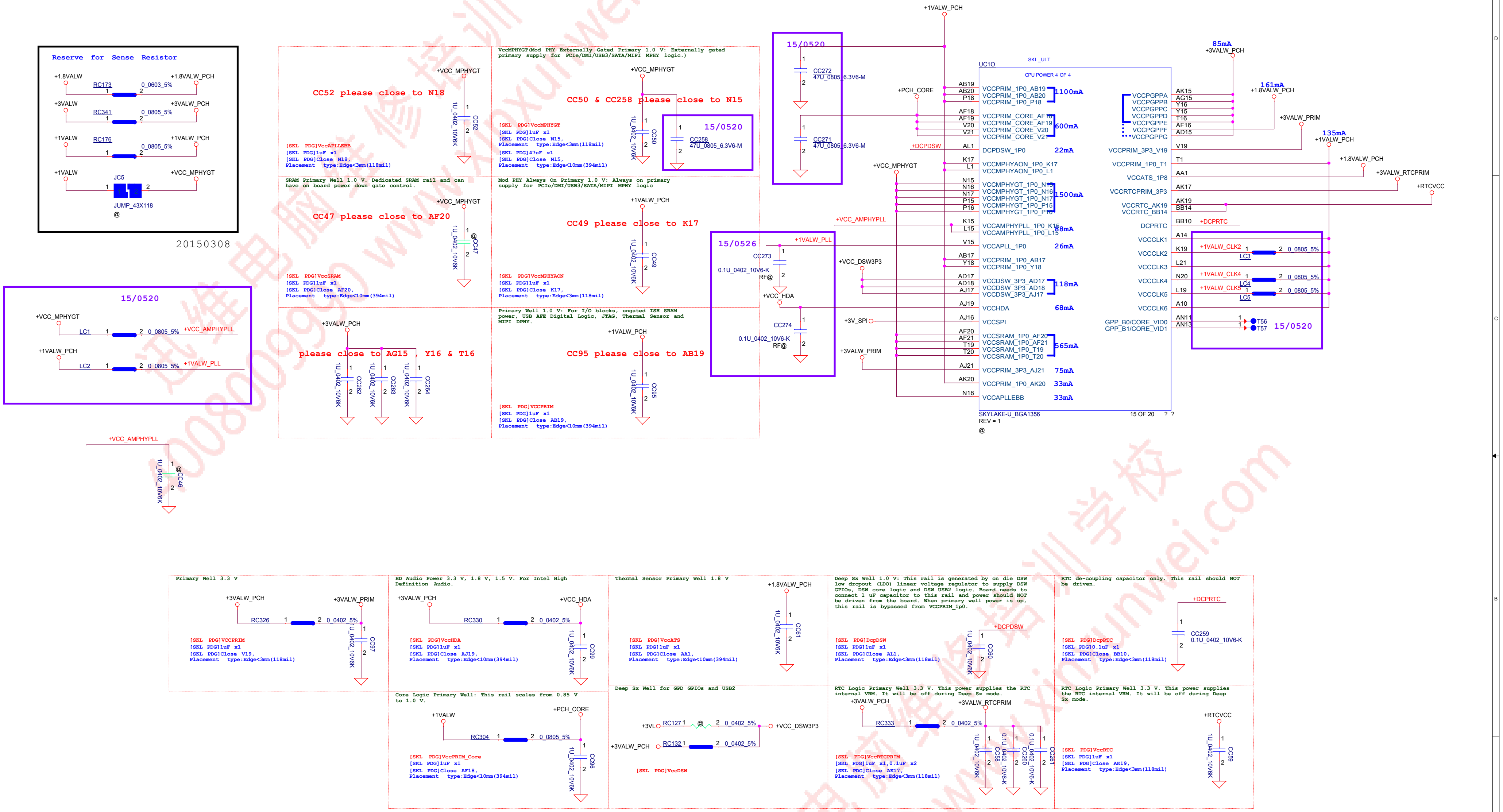

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Title: SKL(13/16):POWER

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Title: SKL(14/16):GND

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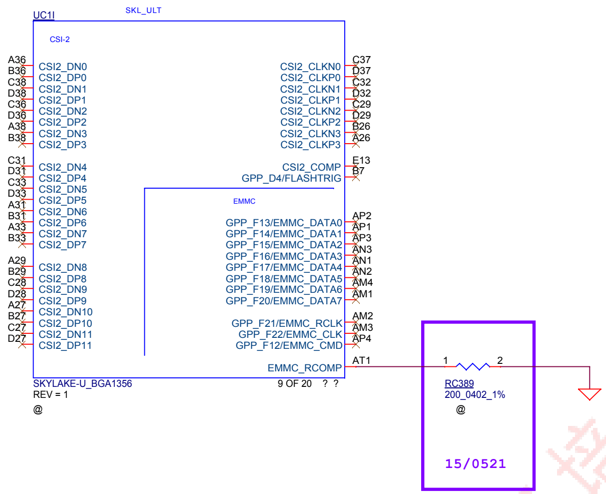

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Title
SKL(15/16):CSI-2/EMMC

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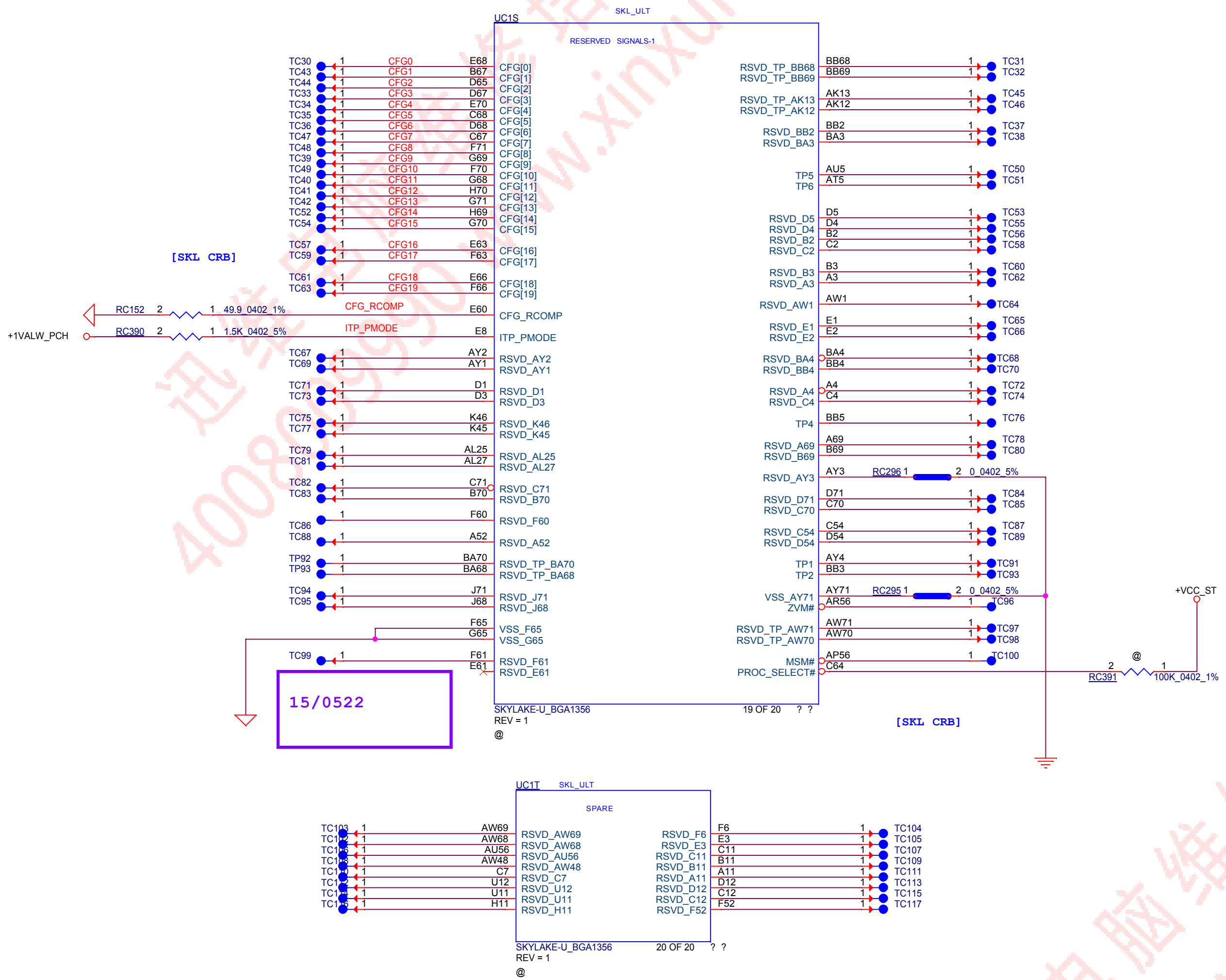
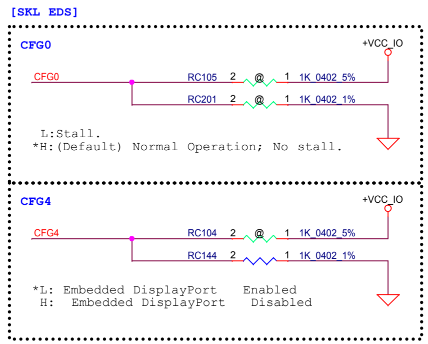


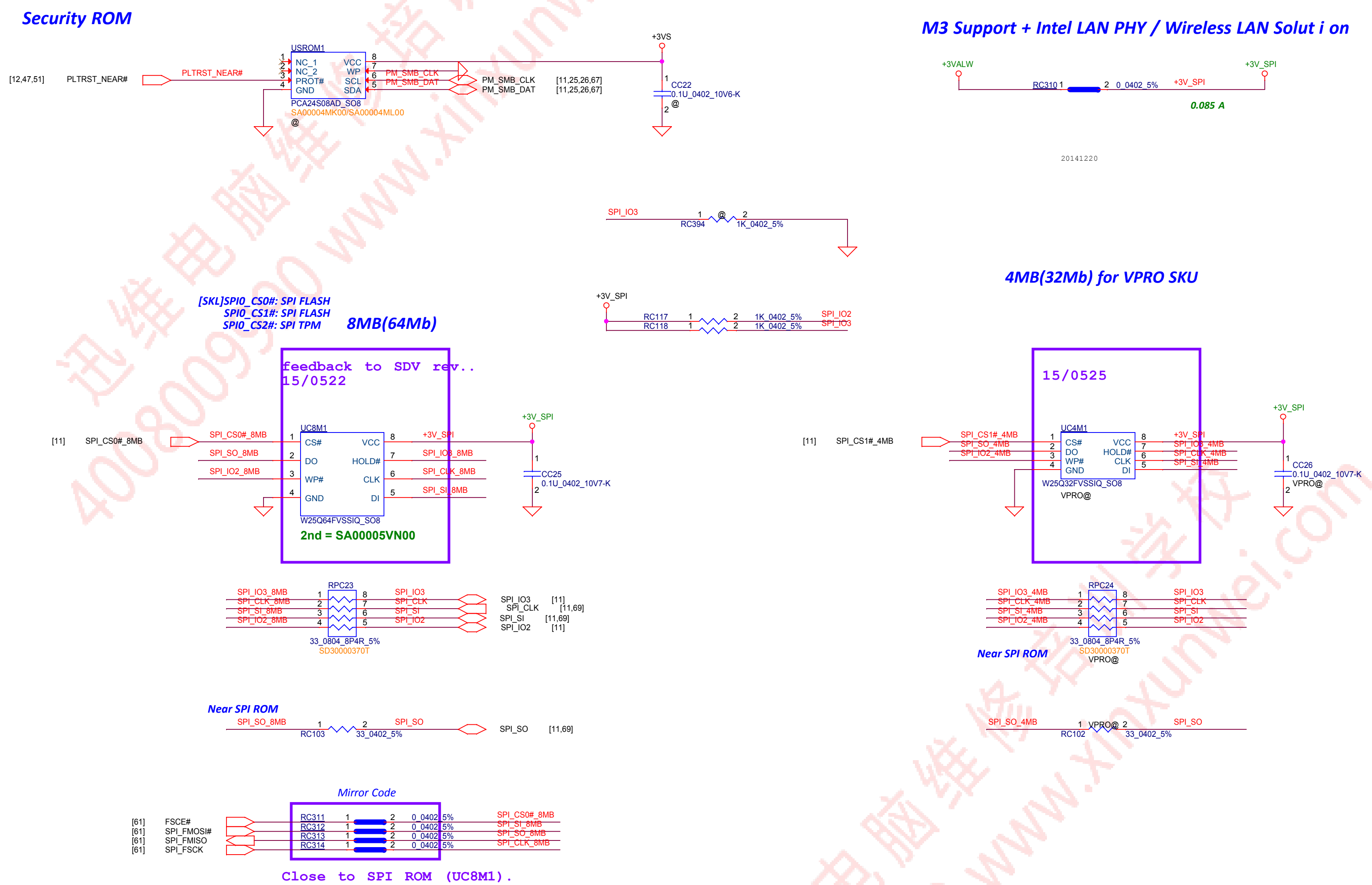


TABLE

CFG0 : Stall Reset Sequence after PCU PLL Lock until de-asserted **1 : No Stall** **0 : Stall**
CFG4 : eDP Enable **1 : Disabled** **0 : Enabled**
CFG9 : SVID Bus Communication **1 : Enabled** **0 : Disabled**

ZVM# state	VCCOPC
0V	0V
1V	1V

ZVM# state	MSM# state	VCCEOPIO
0V	X	0V
1V	0V	0.8V
1V	1V	1V



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Title: SPI ROM

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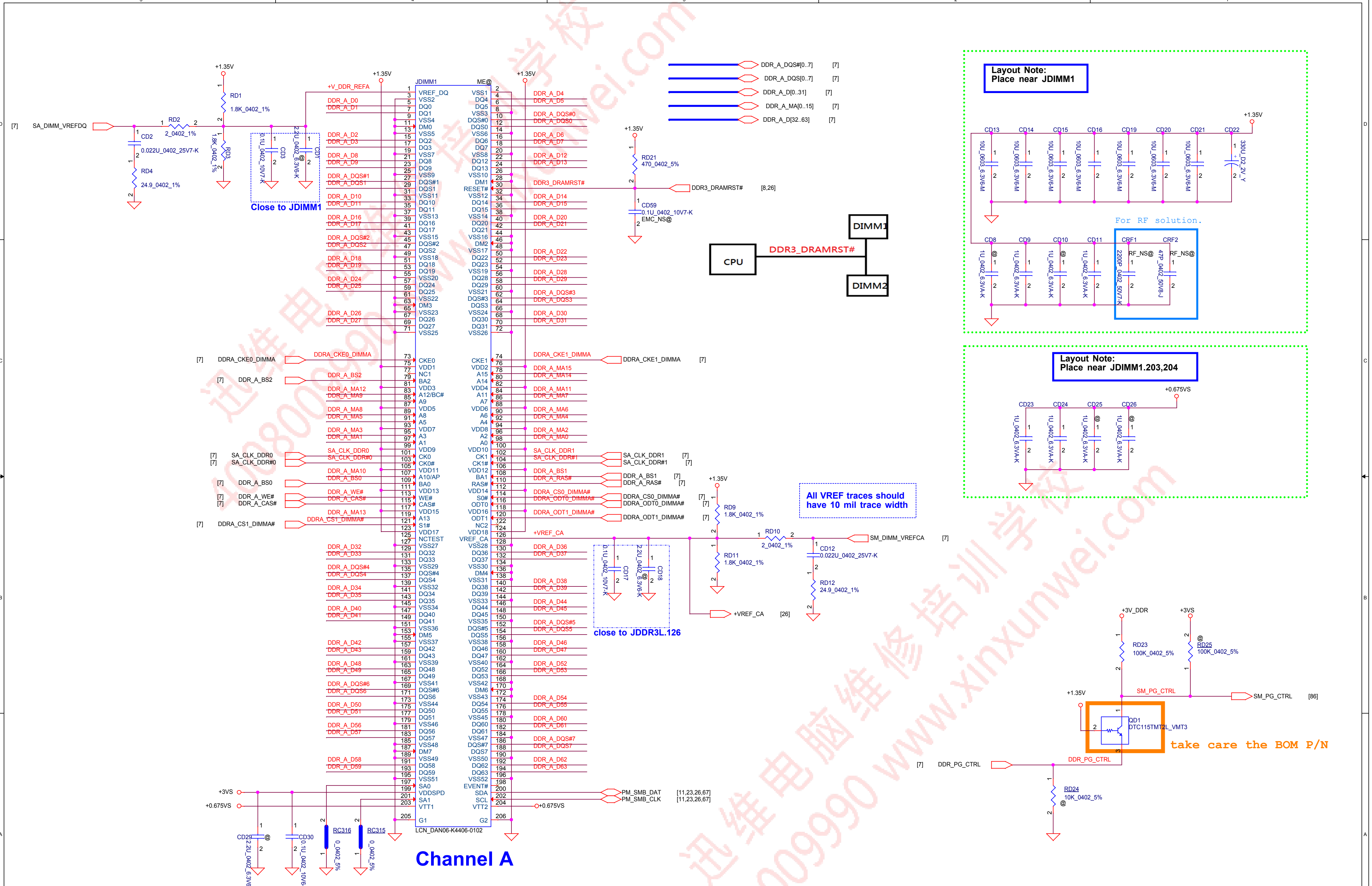

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Title: DDR3L DIMM1

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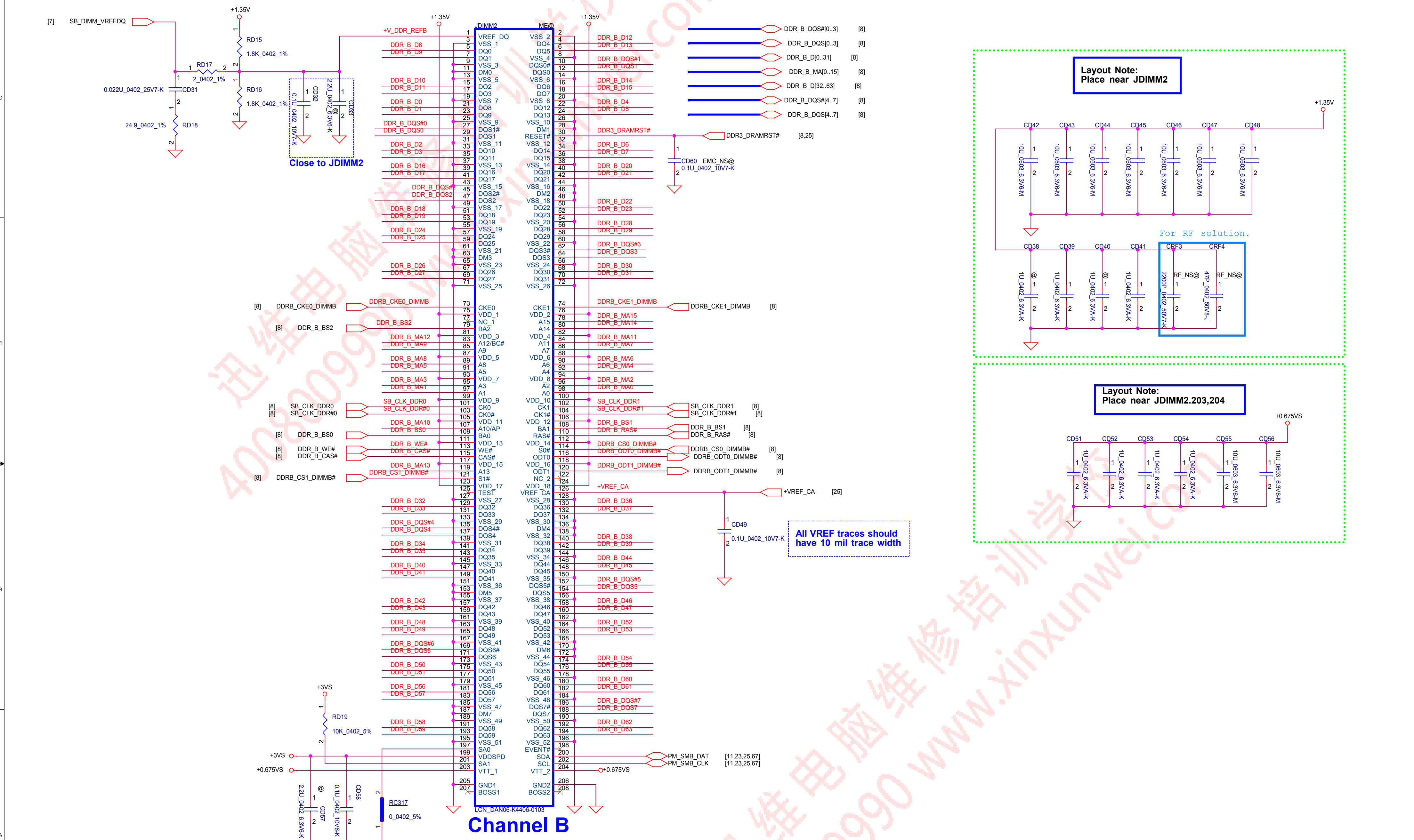

Channel B

<Address: SA1:SA0=10>

DIMM_2 STD H:4mm

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Title: **DDR3L DIMM2**

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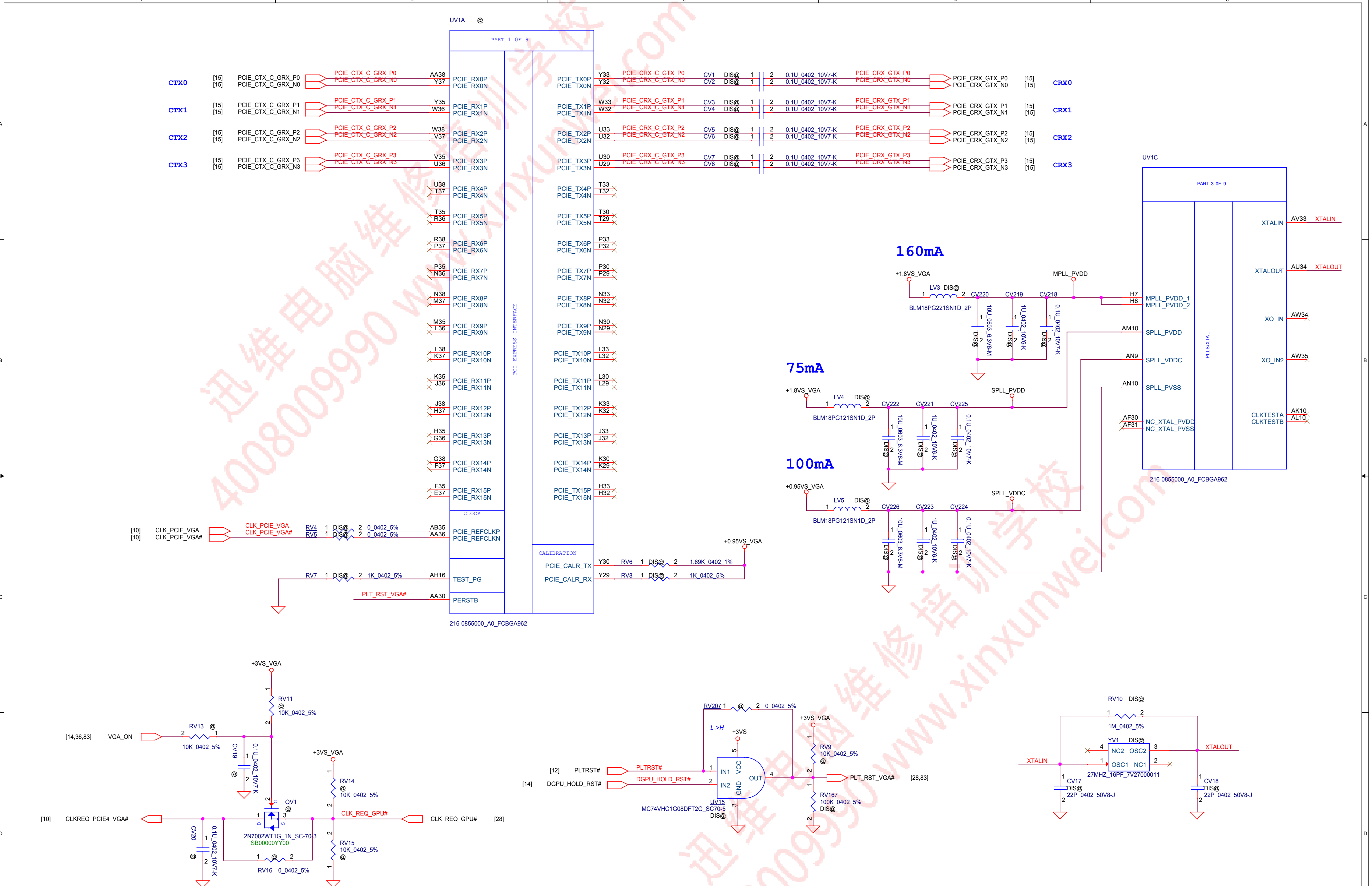

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Title: Graphic

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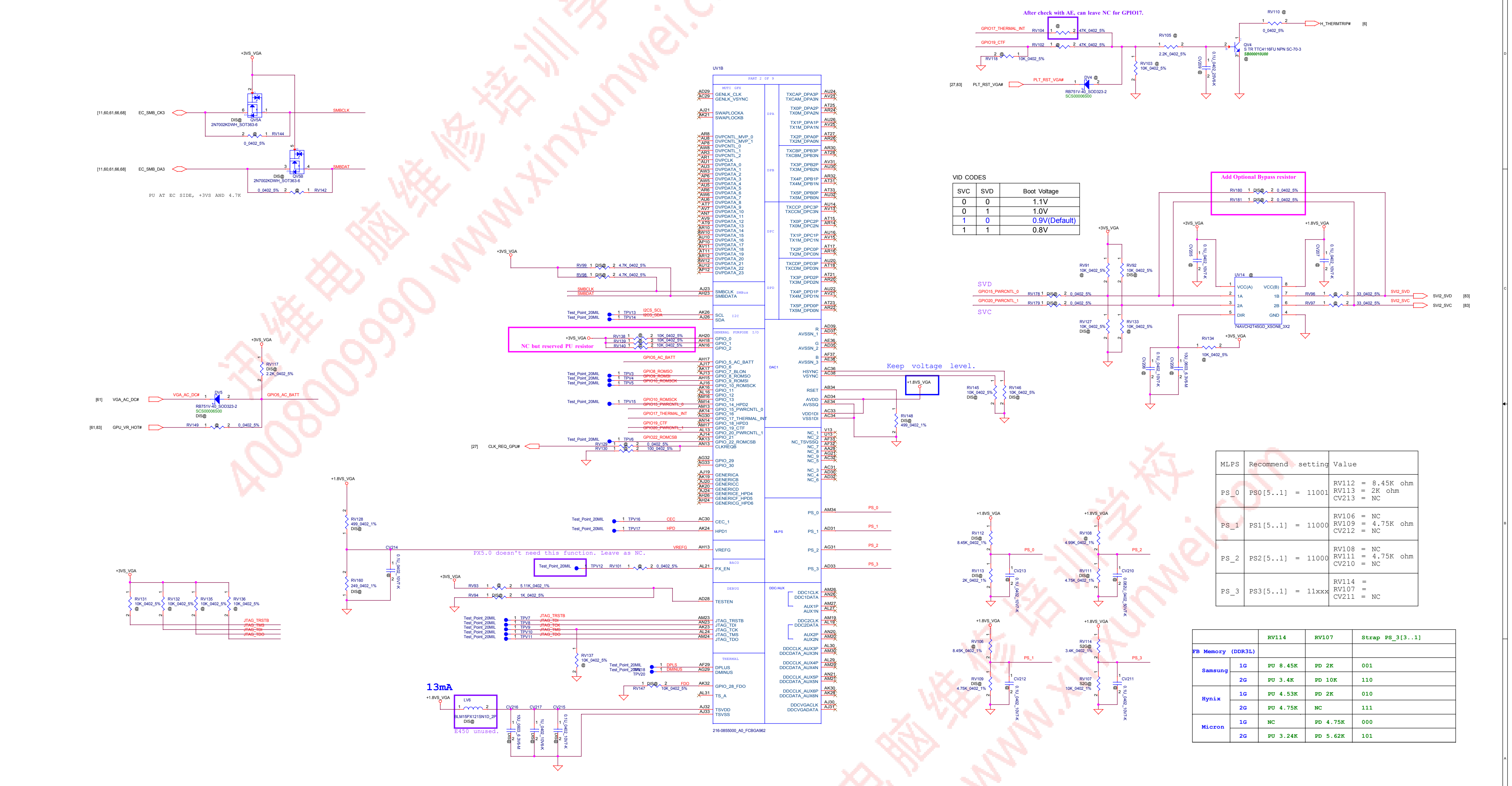


VID CODES

SVC	SVD	Boot Voltage
0	0	1.1V
0	1	1.0V
1	0	0.9V(Default)
1	1	0.8V

MLPS	Recommend setting	Value
PS_0	PS0[5..1] = 11001	RV112 = 8.45K ohm RV113 = 2K ohm CV213 = NC
PS_1	PS1[5..1] = 11000	RV106 = NC RV109 = 4.75K ohm CV212 = NC
PS_2	PS2[5..1] = 11000	RV108 = NC RV111 = 4.75K ohm CV210 = NC
PS_3	PS3[5..1] = 11xxx	RV114 = RV107 = CV211 = NC

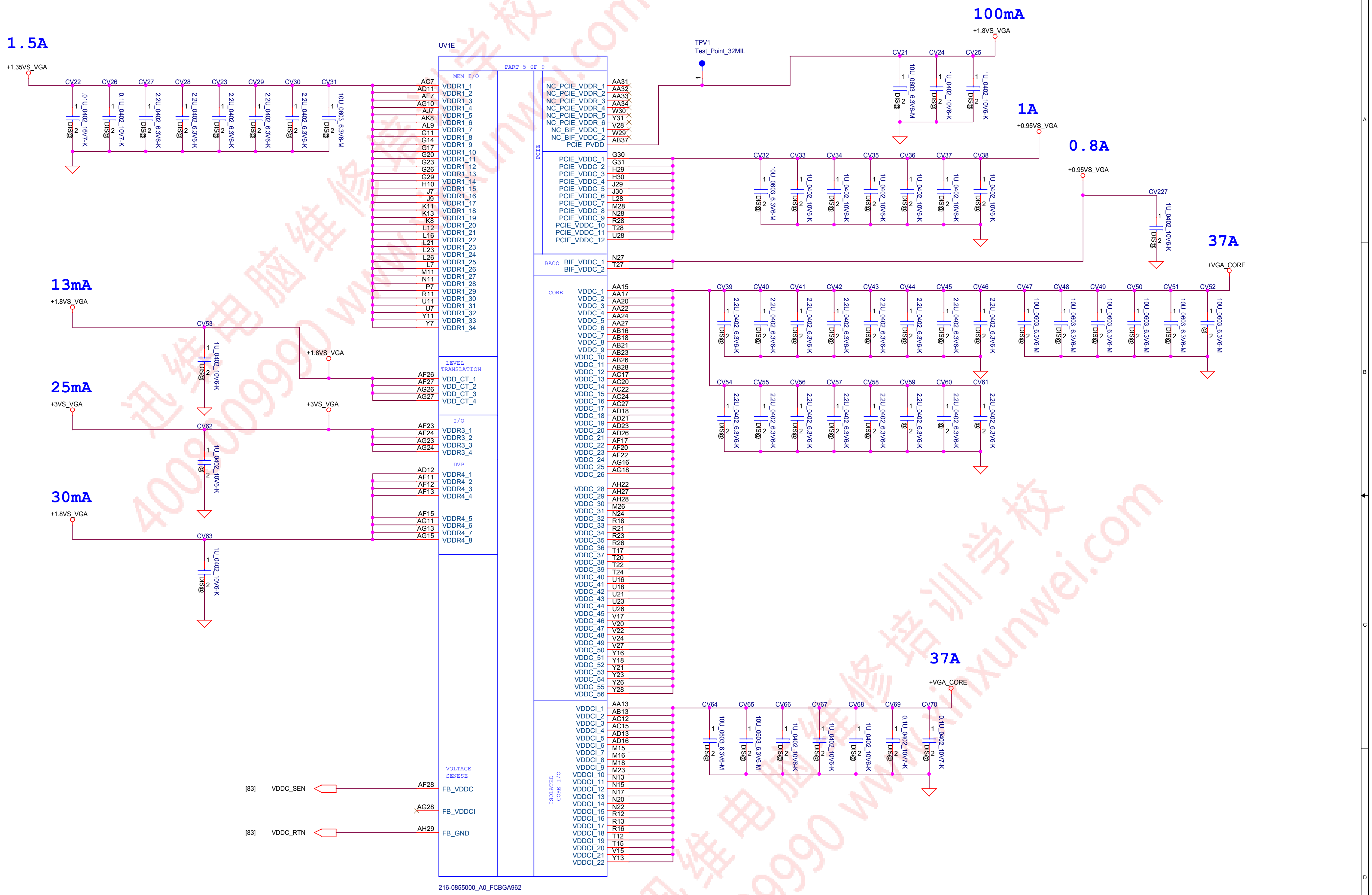
		RV114	RV107	Strap PS_3[3..1]
FB Memory (DDR3L)				
Samsung	1G	PU 8.45K	PD 2K	001
	2G	PU 3.4K	PD 10K	110
Hynix	1G	PU 4.53K	PD 2K	010
	2G	PU 4.75K	NC	111
Micron	1G	NC	PD 4.75K	000
	2G	PU 3.24K	PD 5.62K	101

Security Classification	LC Future Center Secret Data		
Issued Date	2012/07/01	Deciphered Date	2014/07/01

Title: Graphic

Size: D | Document Number: BE560 | Rev: 0.1

Date: Wednesday, September 23, 2015



Security Classification	LC Future Center Secret Data	
Issued Date	2012/07/01	Deciphered Date 2014/07/01

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Title: Graphic

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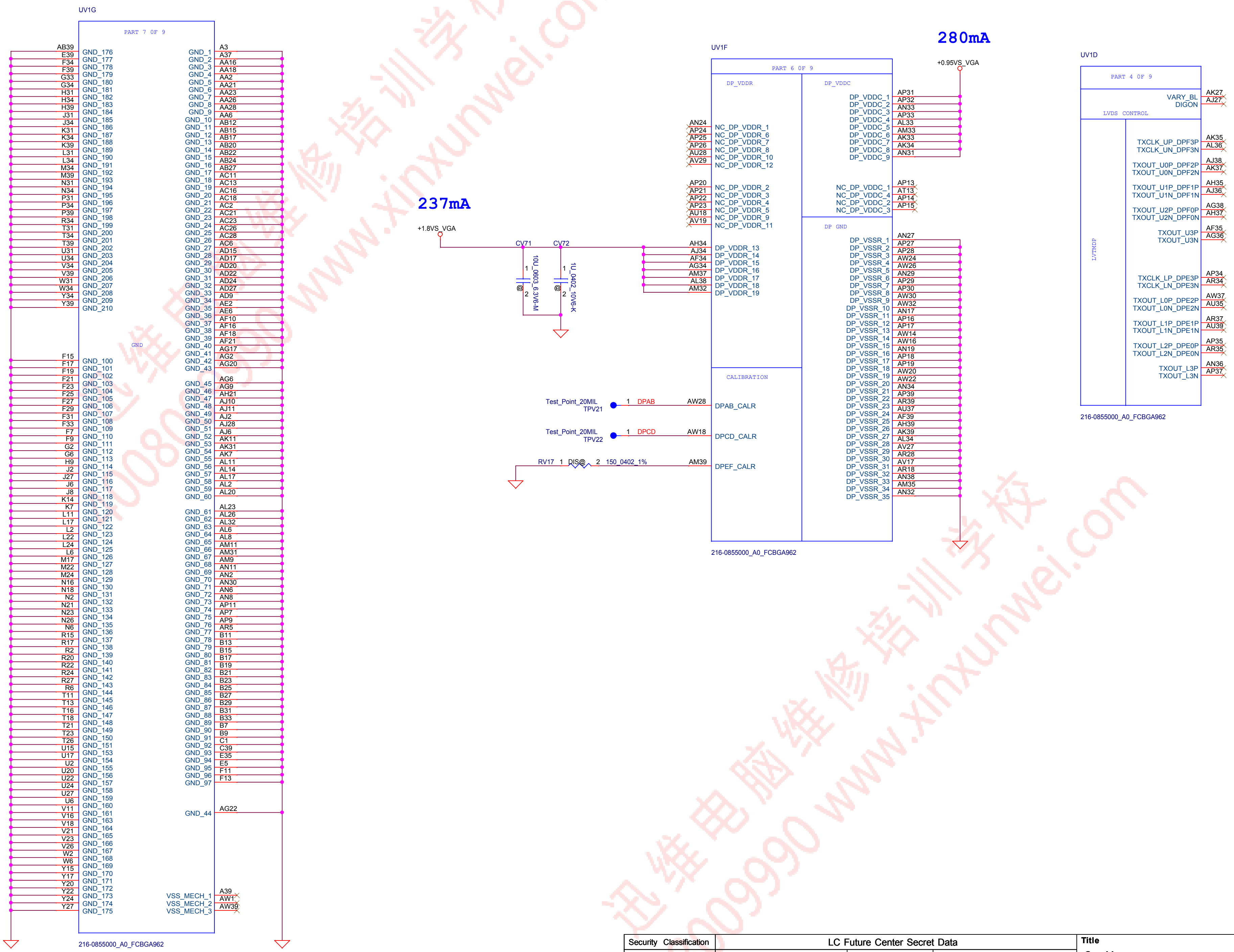
Date: Wednesday, September 23, 2015 | Sheet 29 of 99

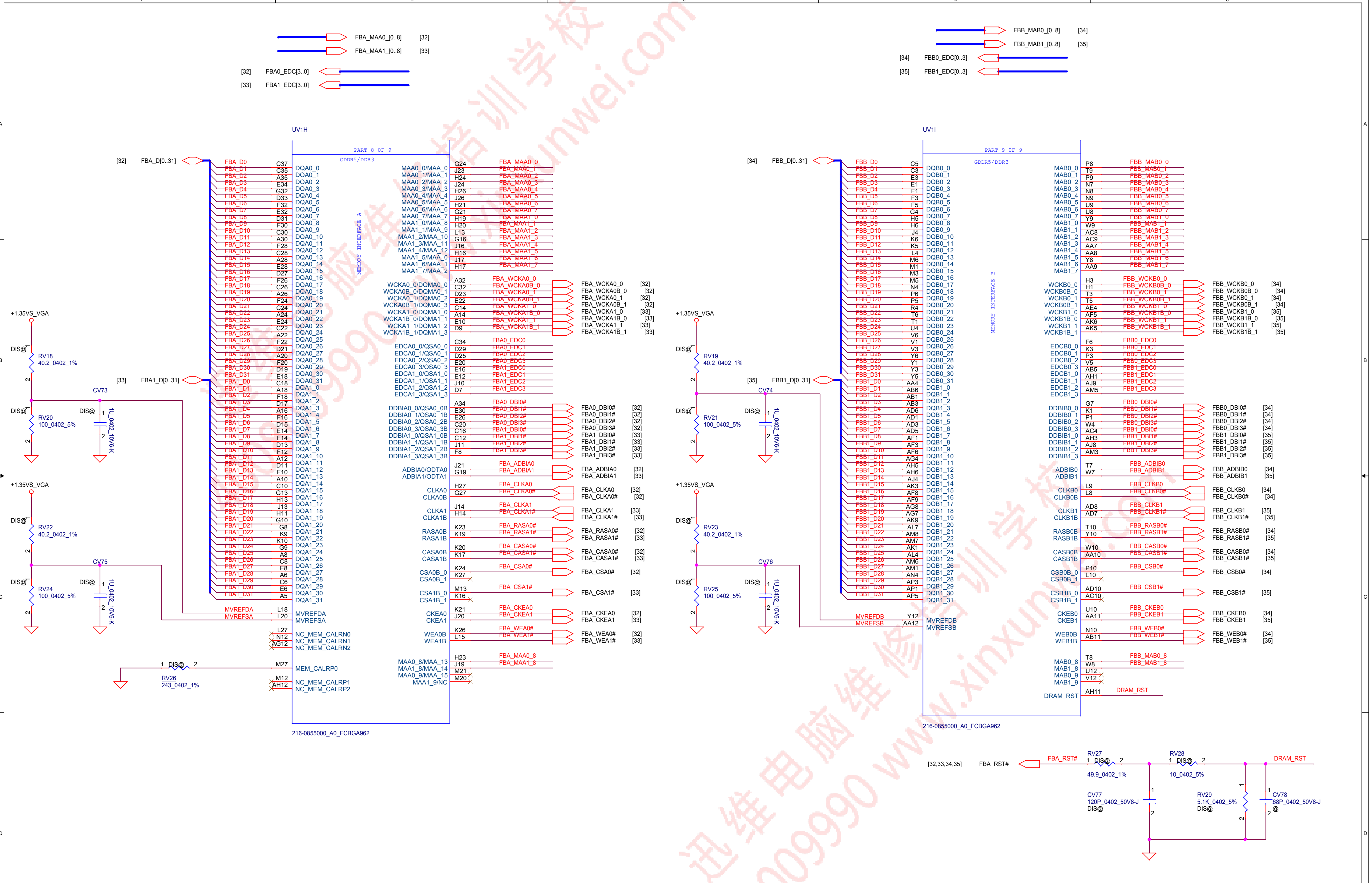


Security Classification	LC Future Center Secret Data		
Issued Date	2012/07/01	Deciphered Date	2014/07/01

Title
Graphic

Size Custom | Document Number **BE560** | Rev 0.1

Date: Wednesday, September 23, 2015 | Sheet 30 of 99



Security Classification	LC Future Center Secret Data		
Issued Date	2012/07/01	Deciphered Date	2014/07/01

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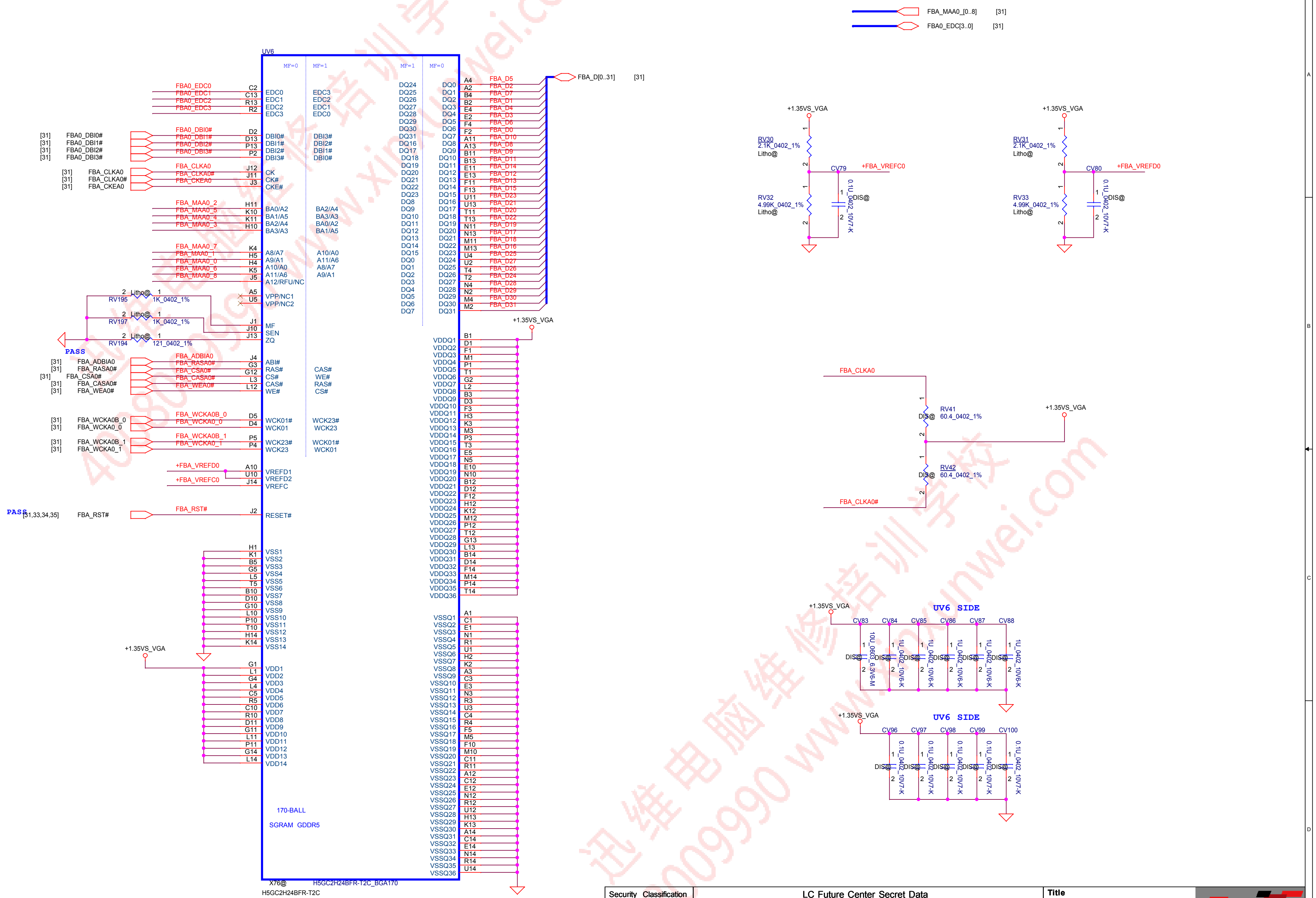
Title: GDDR5

Size: Custom | Document Number: BE560 | Rev: 0.1

Date: Wednesday, September 23, 2015 | Sheet 31 of 99

Memory Partition A - Lower 32 bits



Security Classification	LC Future Center Secret Data		
Issued Date	2012/07/01	Deciphered Date	2014/07/01

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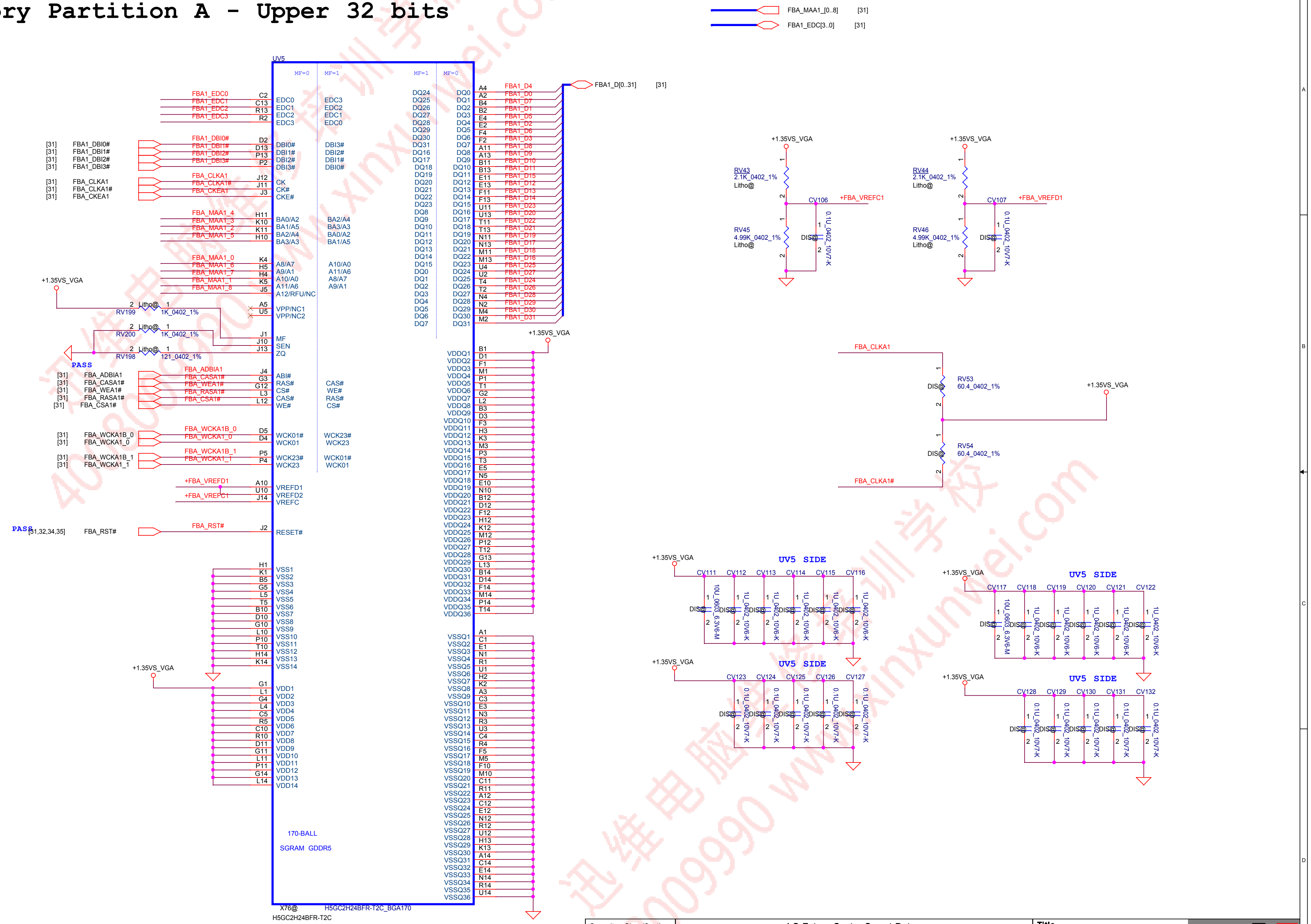
Title: GDDR5

Size: Custom | Document Number: BE560 | Rev 0.1

Date: Wednesday, September 23, 2015 | Sheet 32 of 99

Memory Partition A - Upper 32 bits



Security Classification	LC Future Center Secret Data		
Issued Date	2012/07/01	Deciphered Date	2014/07/01

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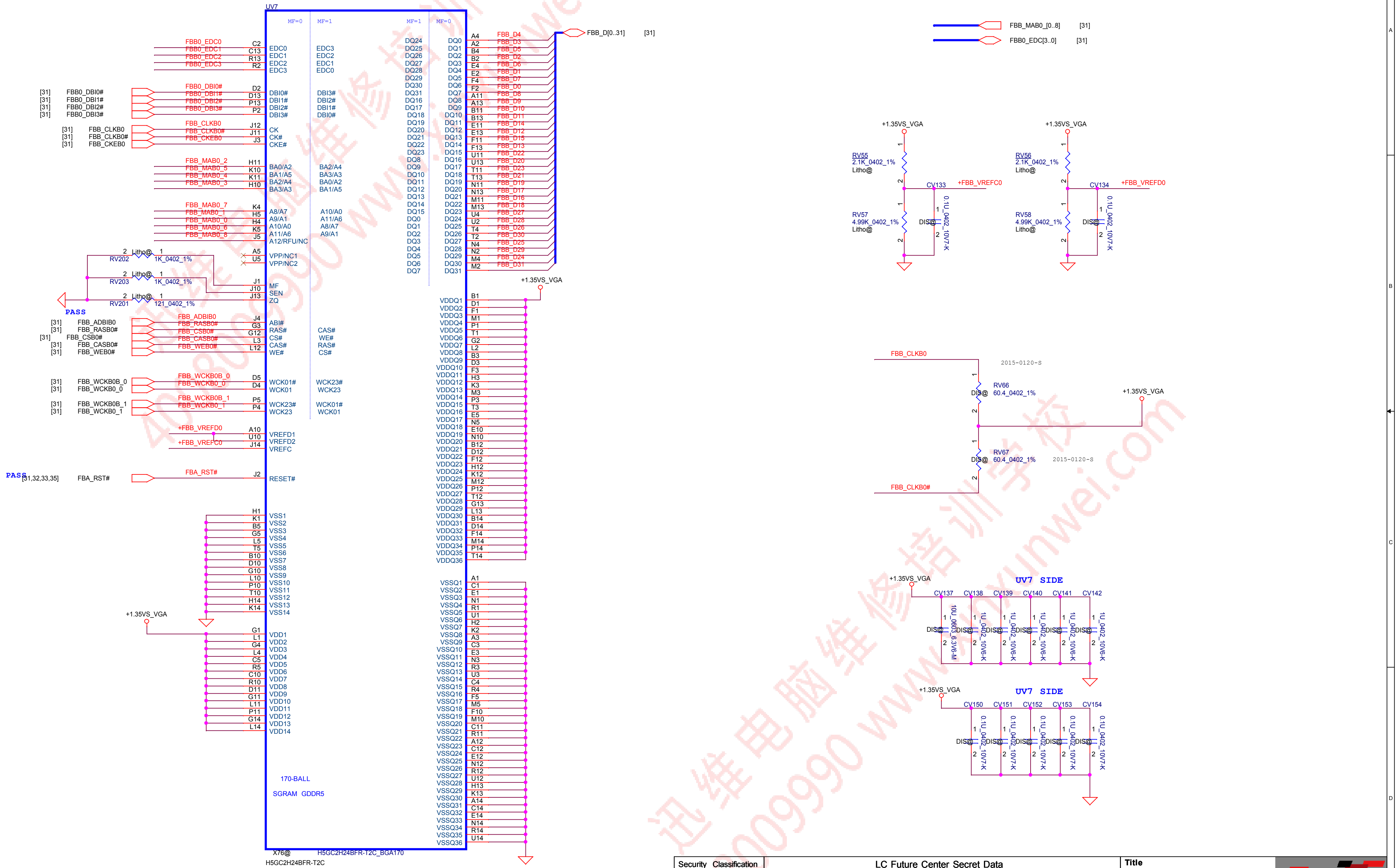
Title: GDDR5

Size: Custom | Document Number: BE560 | Rev: 0.1

Date: Wednesday, September 23, 2015 | Sheet 33 of 99

Memory Partition A - Lower 32 bits



Security Classification	LC Future Center Secret Data		
Issued Date	2012/07/01	Deciphered Date	2014/07/01

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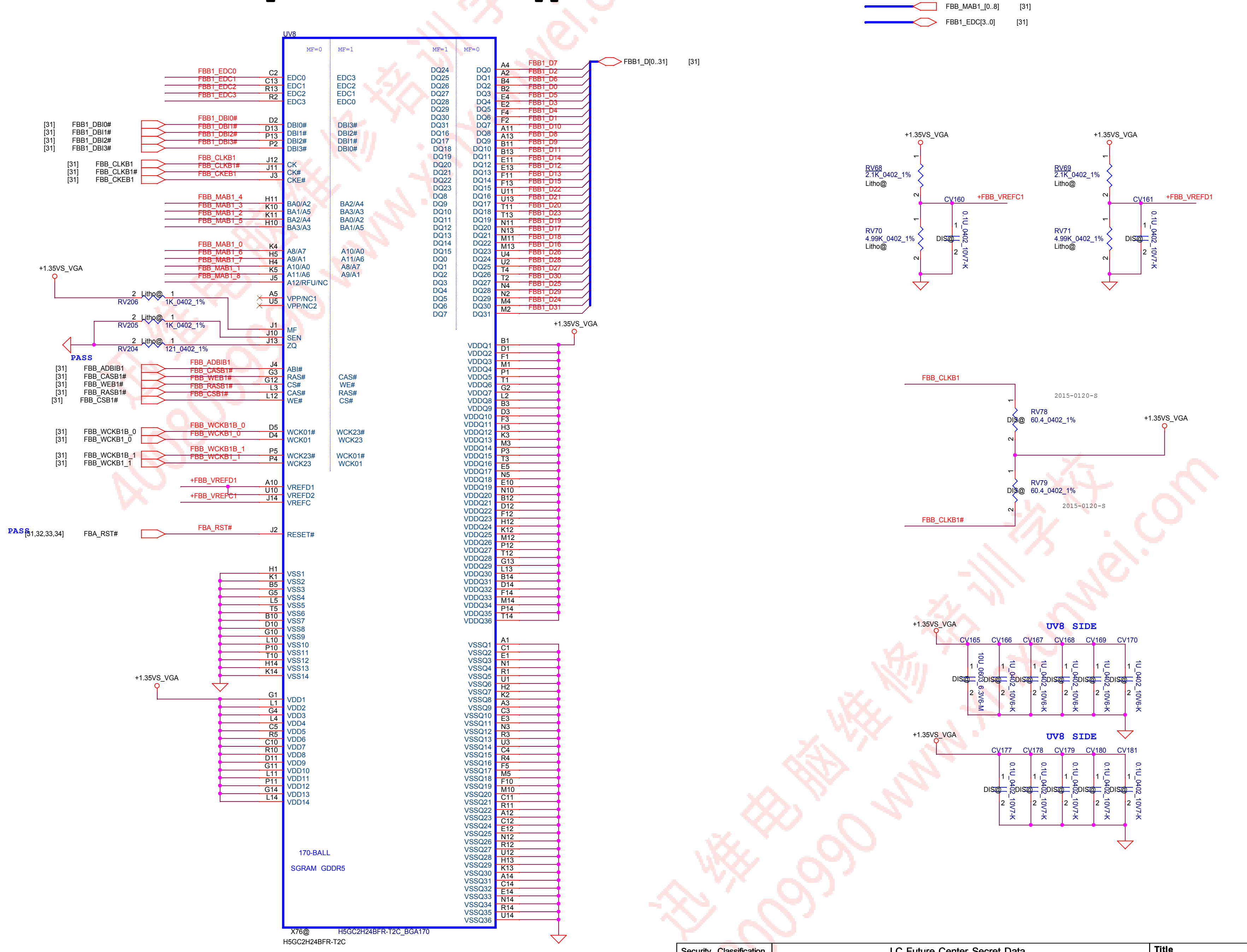
Title: GDDR5

Size: Custom | Document Number: BE560 | Rev: 0.1

Date: Wednesday, September 23, 2015 | Sheet 34 of 99

Memory Partition A - Upper 32 bits



Security Classification	LC Future Center Secret Data		
Issued Date	2012/12/05	Deciphered Date	2014/12/05

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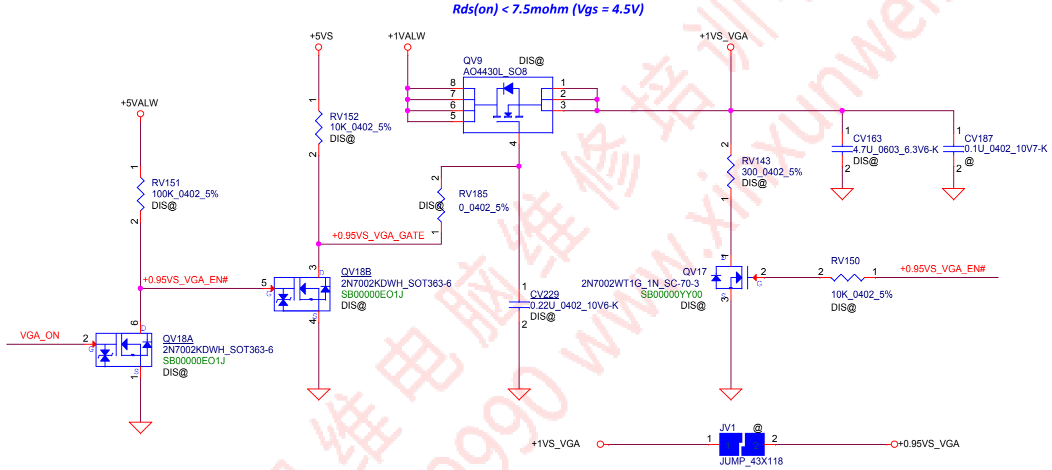
Title: GDDR5

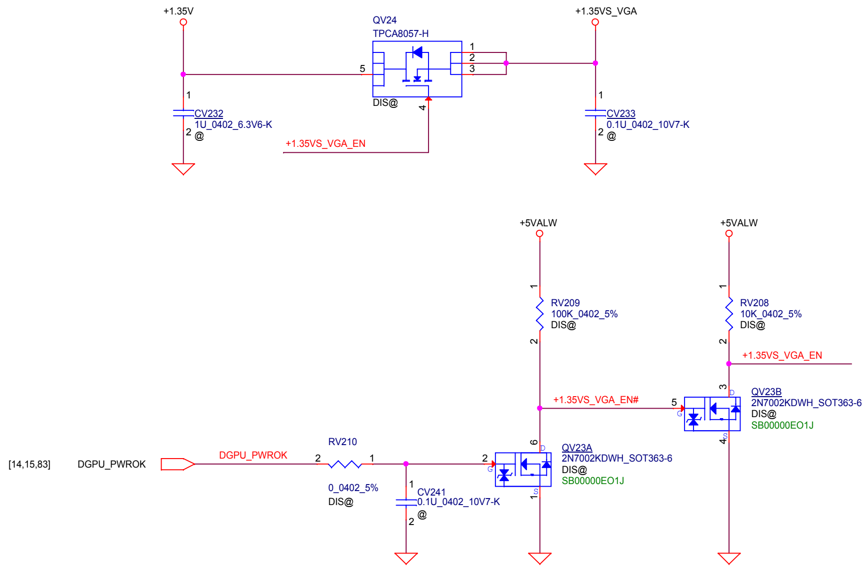
Size: Custom | Document Number: BE560 | Rev: 0.1

Date: Wednesday, September 23, 2015 | Sheet 35 of 99

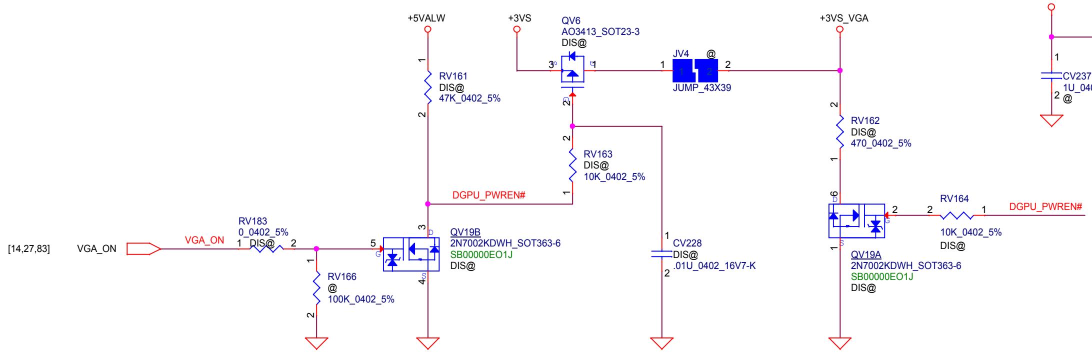
+1.0VALW to +0.95VS_VGA



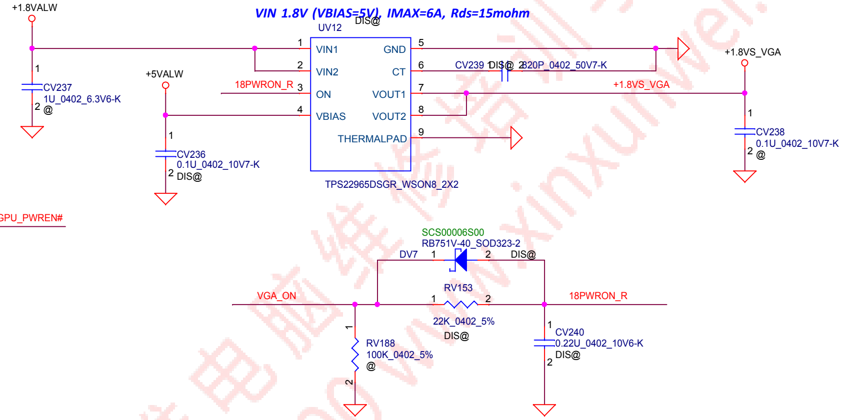
+1.35V to +1.35VS_VGA



+3VS to +3VS_VGA



+1.8VALW to +1.8VS_VGA



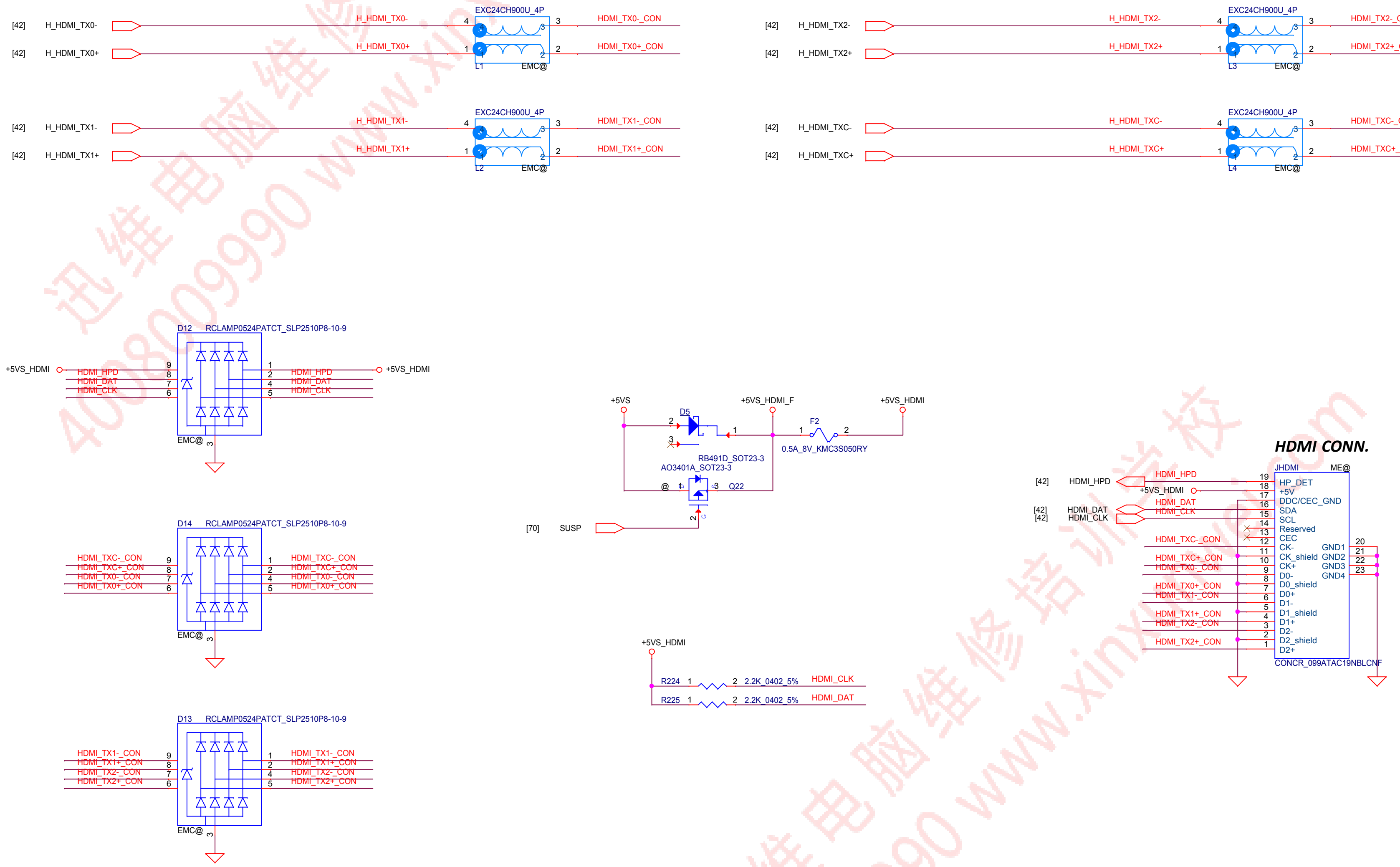
MLPS	Bit				
	5	4	3	2	1
PS_0[5:1]	1	1	0	0	1
PS_1[5:1]	1	1	0	0	0
PS_2[5:1]	1	1	0	0	0
PS_3[5:1]	1	1	X	X	X

Security Classification	LC Future Center Secret Data		
Issued Date	2012/12/05	Deciphered Date	2014/12/05

Title: Litho swich power

Size: Custom | Document Number: BE560 | Rev: 0.1

Date: Wednesday, September 23, 2015 | Sheet 36 of 99

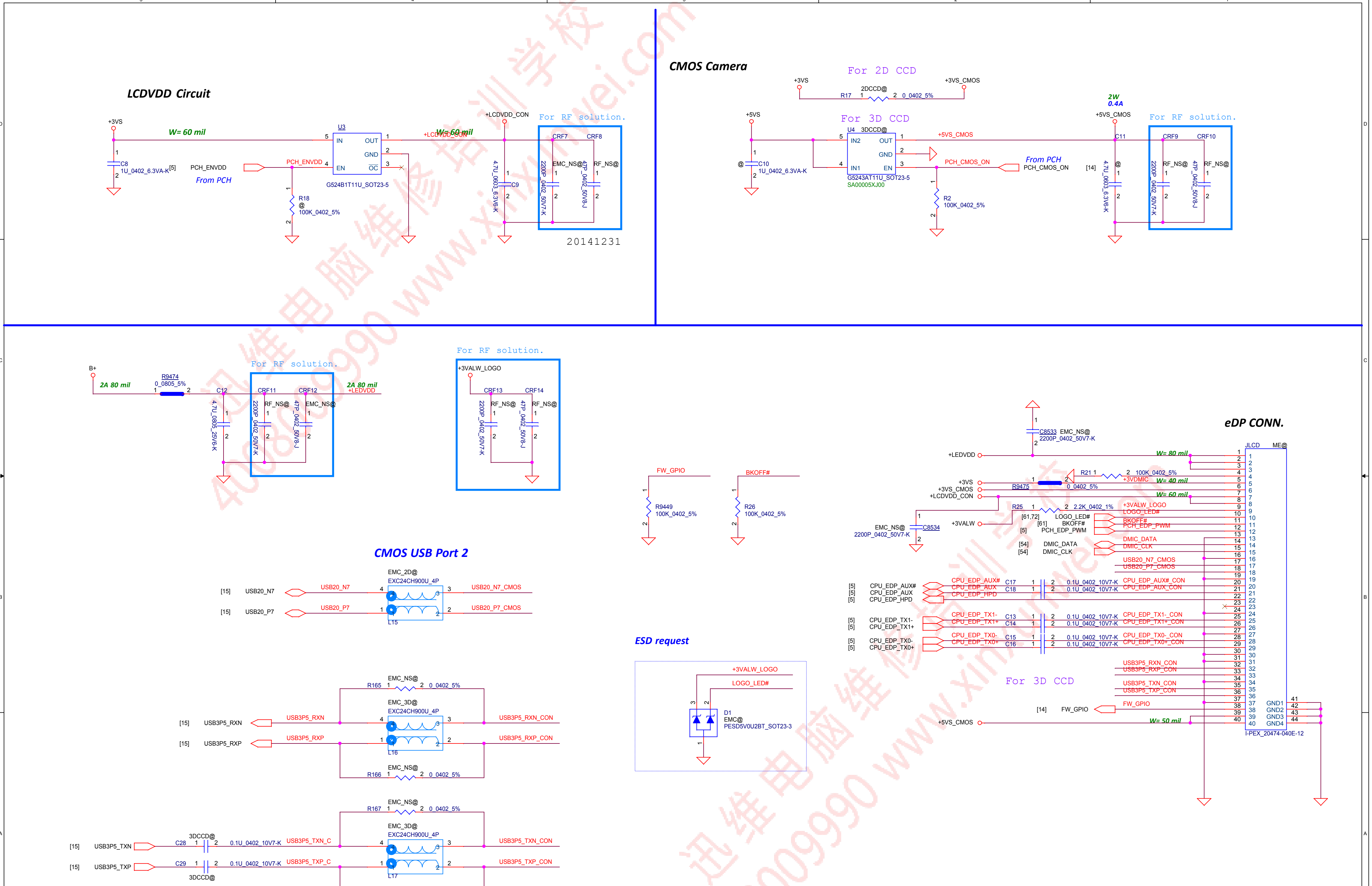


Security Classification	LC Future Center Secret Data		
Issued Date	2013/09/07	Deciphered Date	2014/09/07

Title
HDMI CONN

Size Custom | Document Number **BE560** | Rev 0.1

Date: Wednesday, September 23, 2015 | Sheet 37 of 99



Security Classification	LC Future Center Secret Data		
Issued Date	2013/09/07	Deciphered Date	2014/09/07

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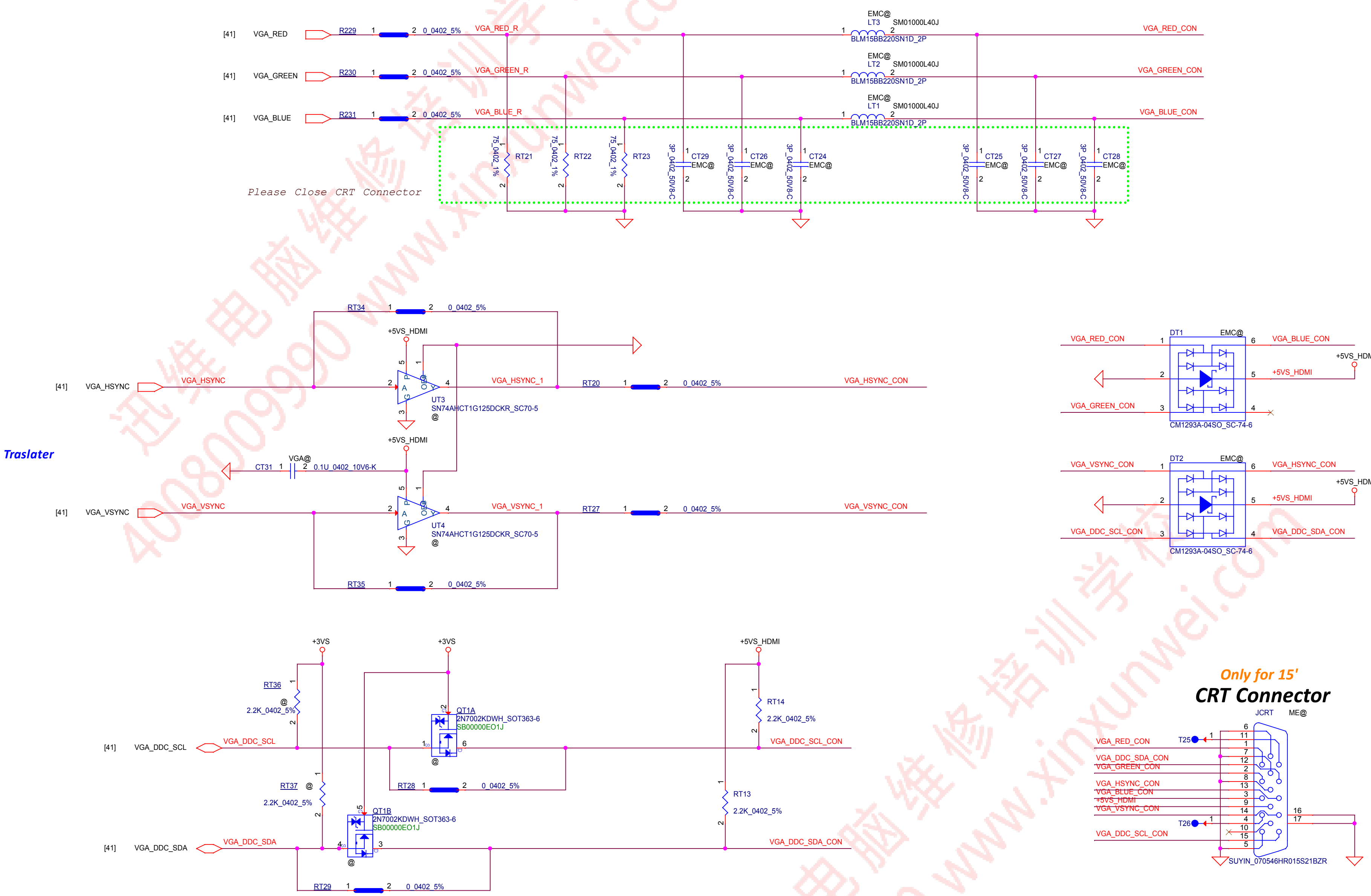
Title: eDP CONN

Size: Custom | Document Number: BE560 | Rev: 0.1

Date: Wednesday, September 23, 2015 | Sheet 38 of 99



Security Classification	LC Future Center Secret Data		
Issued Date	2013/09/07	Deciphered Date	2014/09/07

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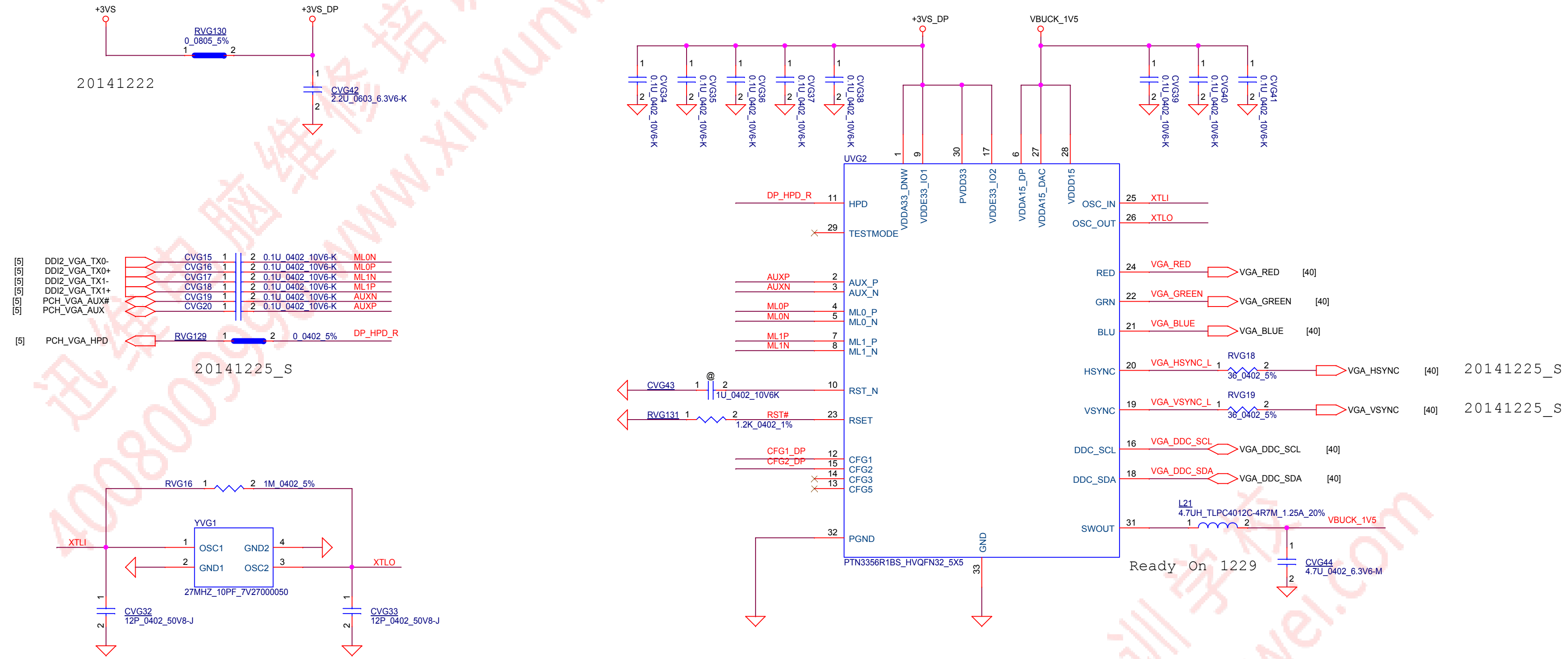
Title: CRT CONN

LCFC

Size: Custom | Document Number: BE560 | Rev: 0.1

Date: Wednesday, September 23, 2015 | Sheet 40 of 99



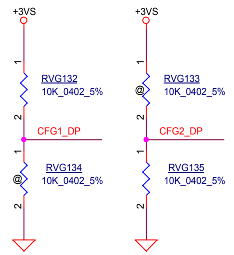


Table 7. CFG1/CFG2 pin definitions

Pin value	System behavior
00	Compliant HPD behavior
01	Most interoperable (non-compliant) HPD behavior
10	Most interoperable (non-compliant) HPD behavior
11	(Default) Compliant behavior

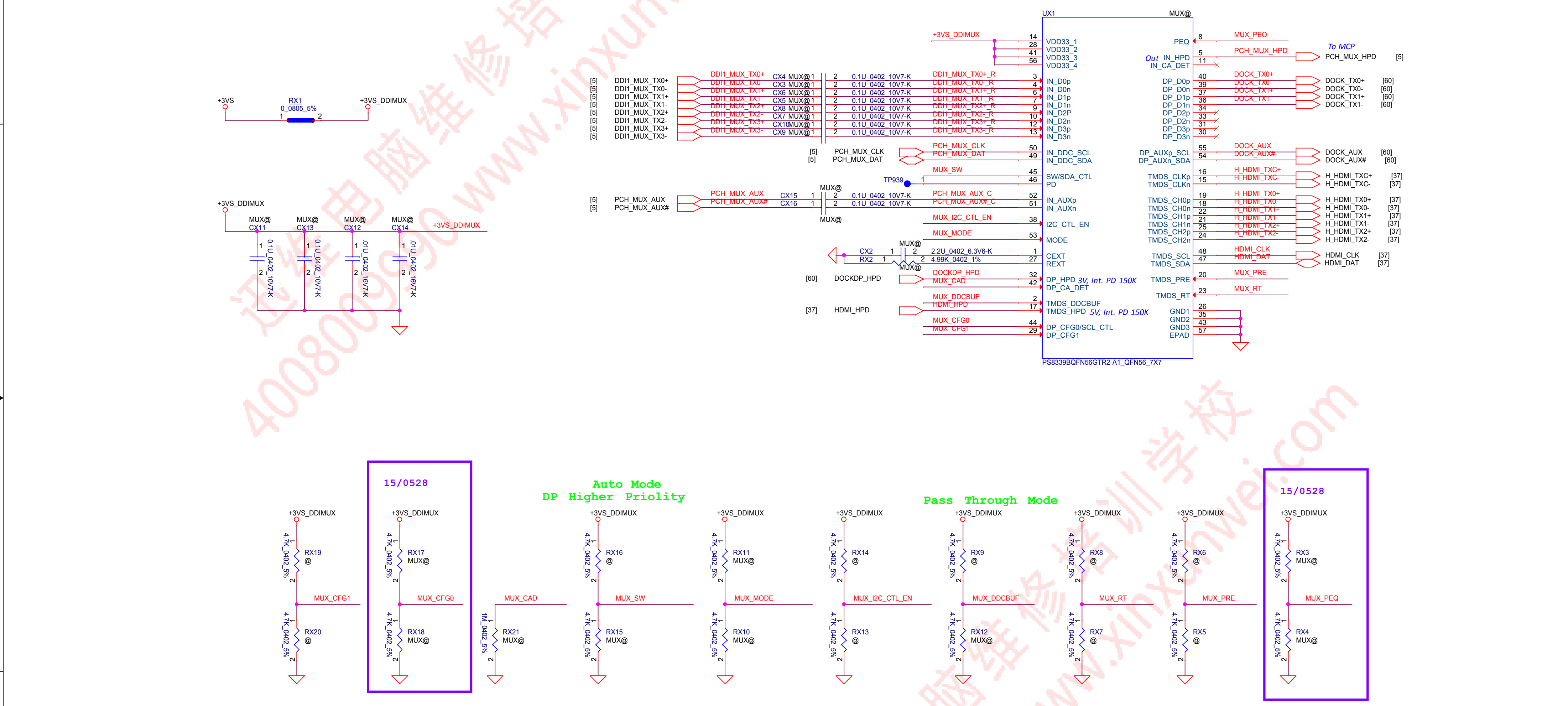
Security Classification	LC Future Center Secret Data		
Issued Date	2013/09/07	Deciphered Date	2014/09/07

Title: DP to VGA

LCFC

Size: Custom | Document Number: BE560 | Rev: 0.1

Date: Wednesday, September 23, 2015 | Sheet 41 of 99



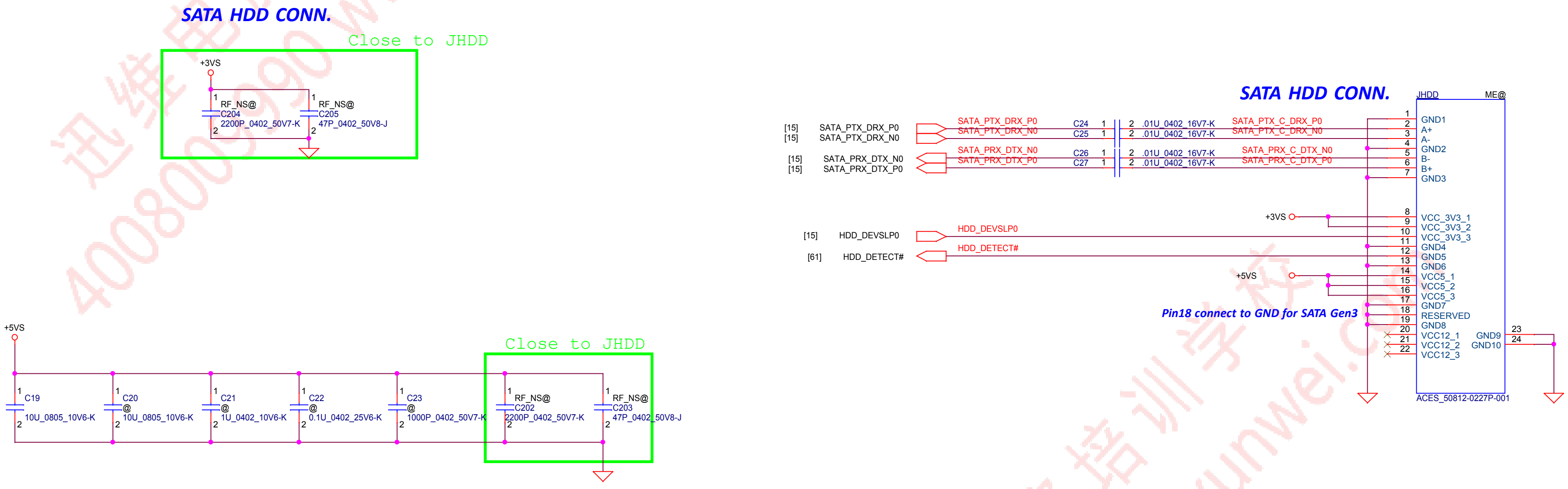
Security Classification	LC Future Center Secret Data		
Issued Date	2013/09/07	Deciphered Date	2014/09/07

Title: **Mux IC**

LCFC

Size: Custom | Document Number: BE560 | Rev: 0.1

Date: Wednesday, September 23, 2015 | Sheet 42 of 99

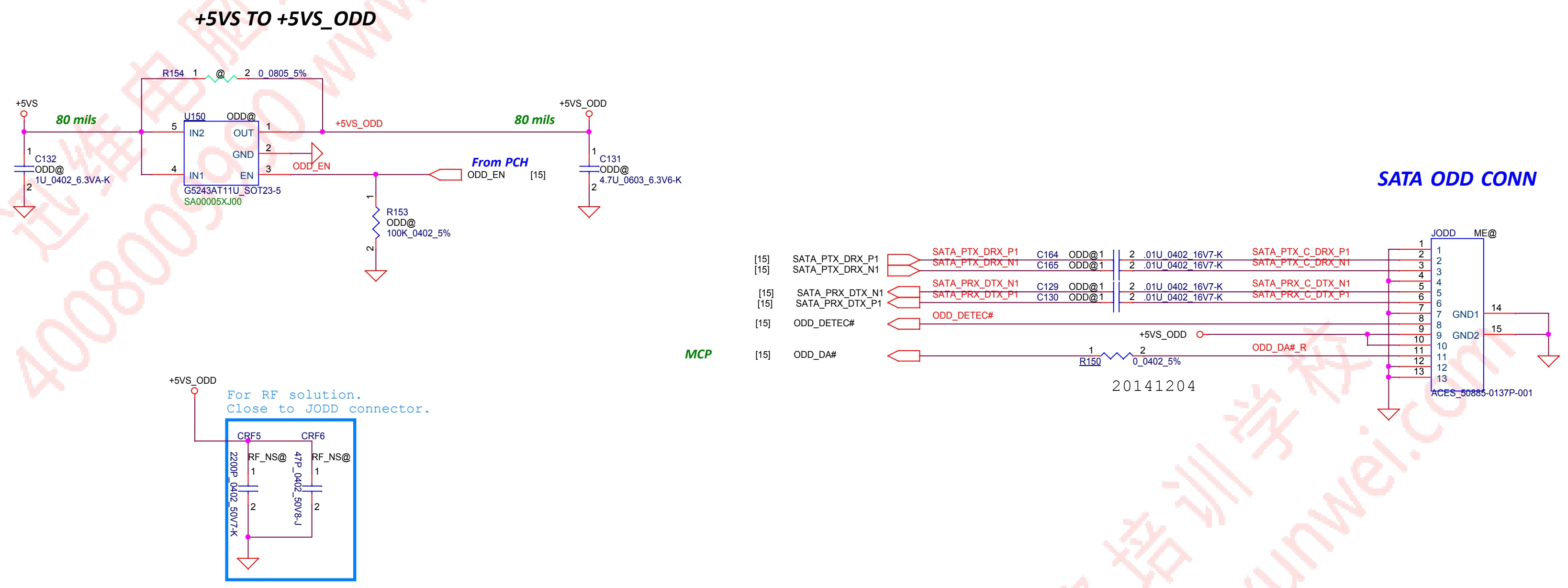


Security Classification	LC Future Center Secret Data		
Issued Date	2013/09/07	Deciphered Date	2014/09/07

Title: SATA HDD

Size: Custom | Document Number: BE560 | Rev: 0.1

Date: Wednesday, September 23, 2015 | Sheet 43 of 99



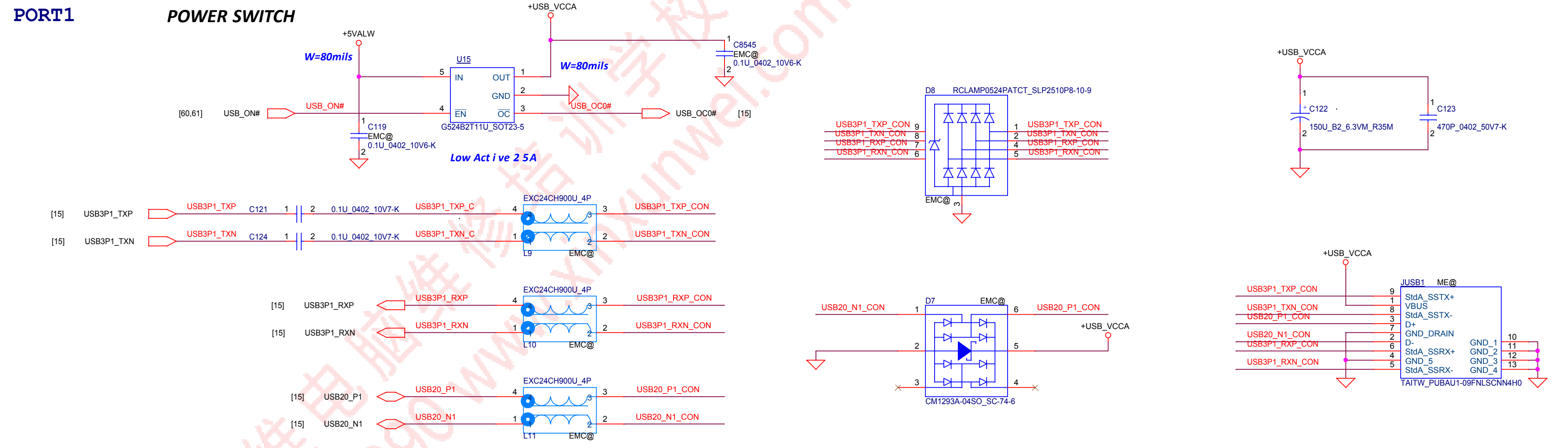
Security Classification	LC Future Center Secret Data		
Issued Date	2013/09/07	Deciphered Date	2014/09/07

Title: ODD

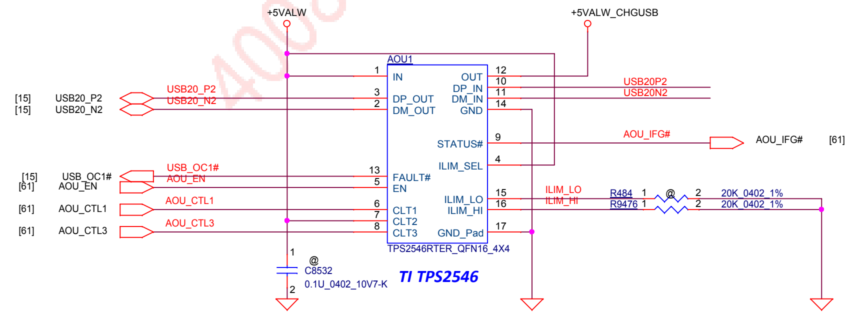
Size: Custom | Document Number: BE560 | Rev: 0.1

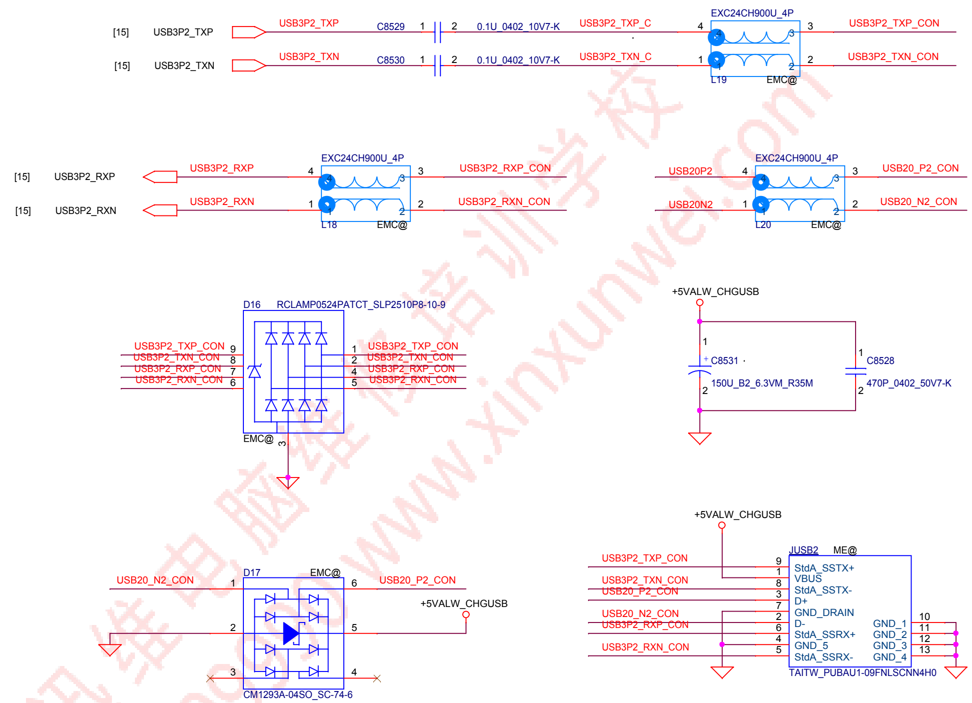
Date: Wednesday, September 23, 2015 | Sheet 44 of 99

USB3 PORT1



PORT2 (AOU)





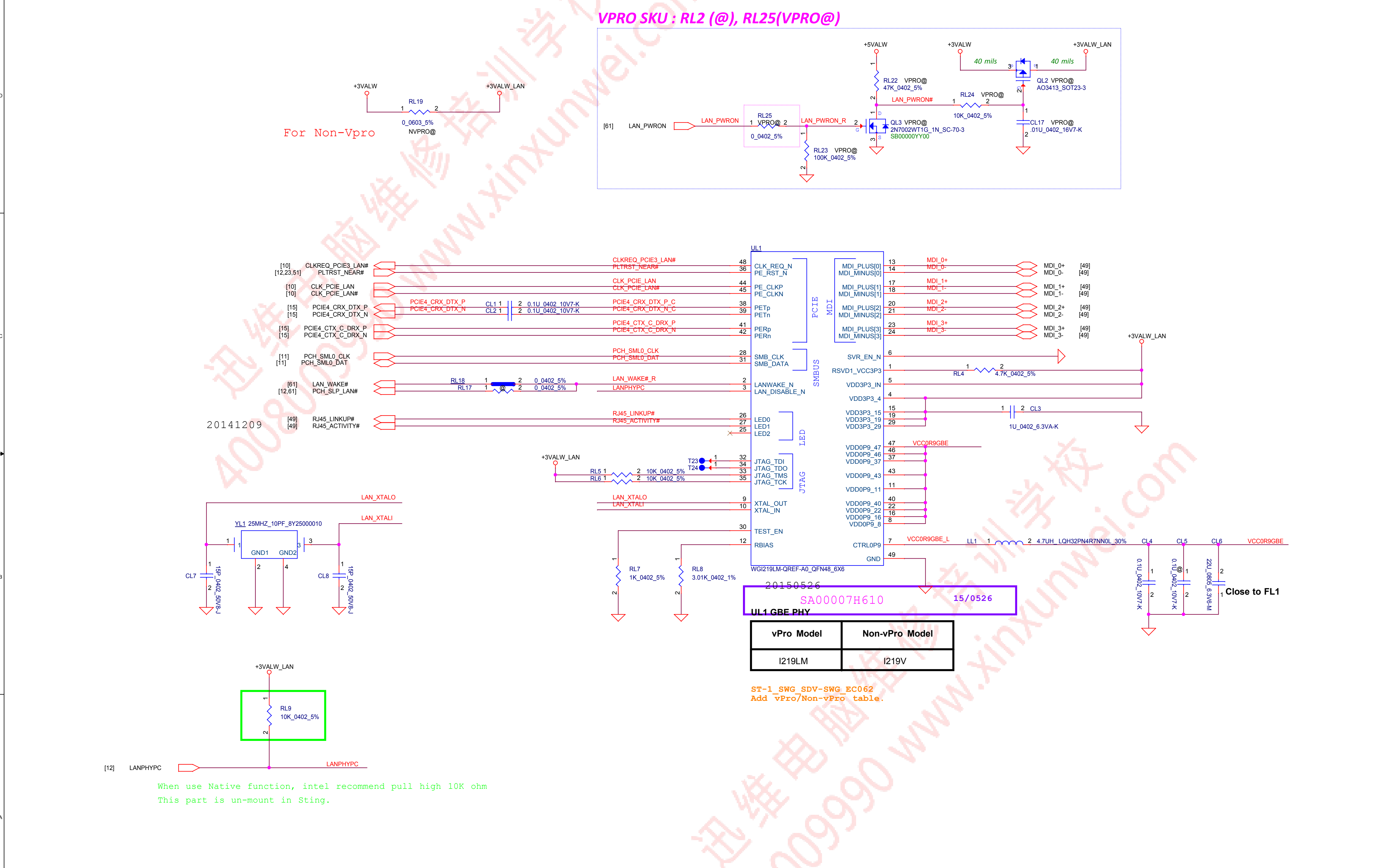
	CLT1	CLT2	CLT3	ILIM_SEL	MOD	
	0	0	0	X	DCH	OUT held low
*	1	1	1	1	CDP	Data Connected and Port Power Mgt. Function Active
*	1	1	1	0	SDP2	Data Connected
*	1	1	0	X	SDP1	Data Connected
*	0	1	0	X	SDP1	Data Connected
	1	0	0	X	DCP_Short	Device Forced to stay in DCP BC 1.2 charging mode
	1	0	1	X	DCP_Divider	Device Forced to stay in DCP Divider 1 Charging Mode
*	0	1	1	X	DCP_Auto	Data Disconnected and Port Power Mgt. Function Active
	0	0	1	X	DCP_Auto	Data Disconnected and Power Wake Function Active

Security Classification	LC Future Center Secret Data		
Issued Date	2013/09/07	Deciphered Date	2014/09/07

Title: USB3 PORT1/PORT2(AOU)

Size: Custom | Document Number: BE560 | Rev: 0.1

Date: Wednesday, September 23, 2015 | Sheet 45 of 99



vPro Model	Non-vPro Model
I219LM	I219V

Security Classification	LC Future Center Secret Data			Title: LAN
Issued Date	2013/09/07	Deciphered Date	2014/09/07	Size: Custom; Document Number: BE560; Rev 0.1
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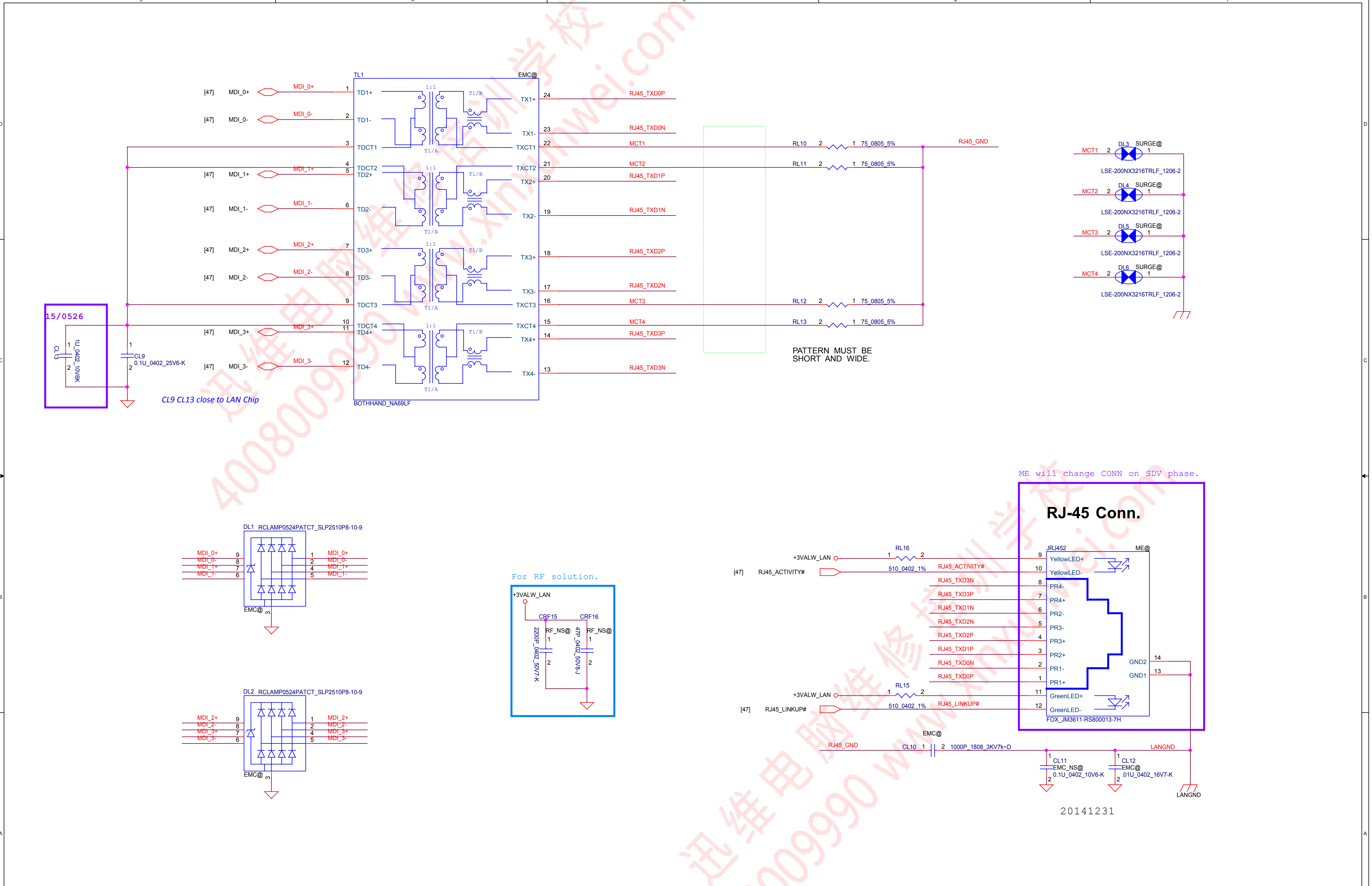

Security Classification	LC Future Center Secret Data		
Issued Date	2013/09/07	Deciphered Date	2014/09/07

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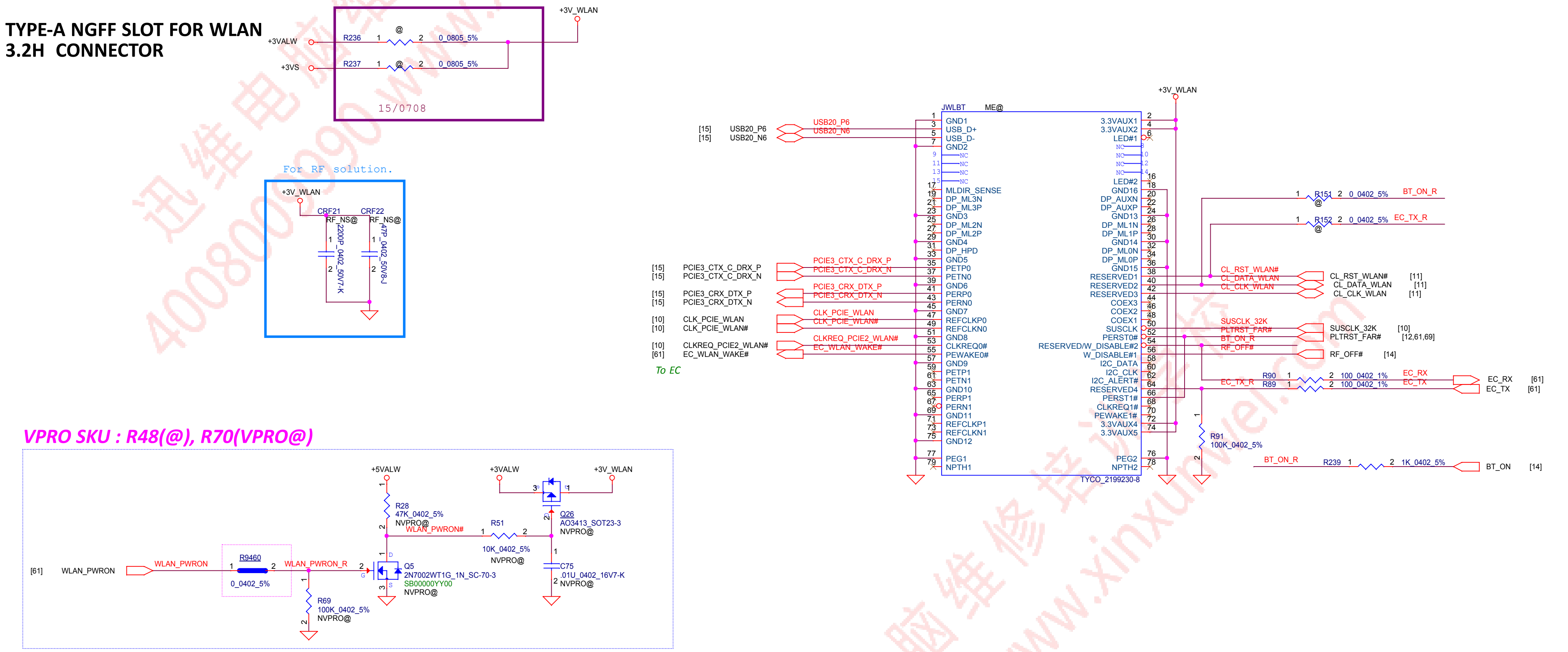
Title: RJ45 CONN

Size: Custom | Document Number: BE560 | Rev: 0.1

Date: Wednesday, September 23, 2015 | Sheet 49 of 99

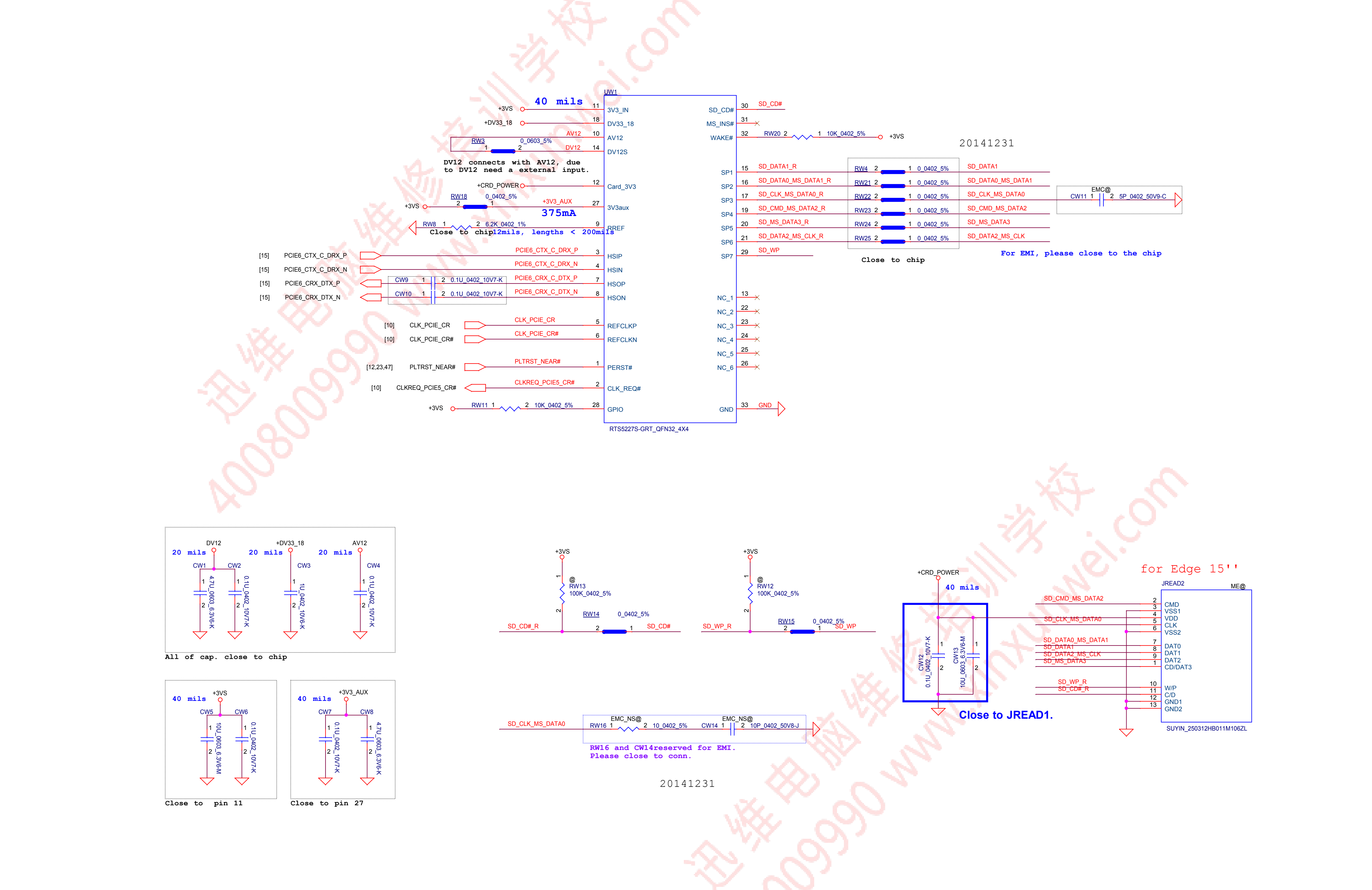


Security Classification	LC Future Center Secret Data		
Issued Date	2013/09/07	Deciphered Date	2014/09/07

Title: **WWAN/WLAN NGFF CONN.**

Size: Custom | Document Number: BE560 | Rev: 0.1

Date: Wednesday, September 23, 2015 | Sheet 50 of 99

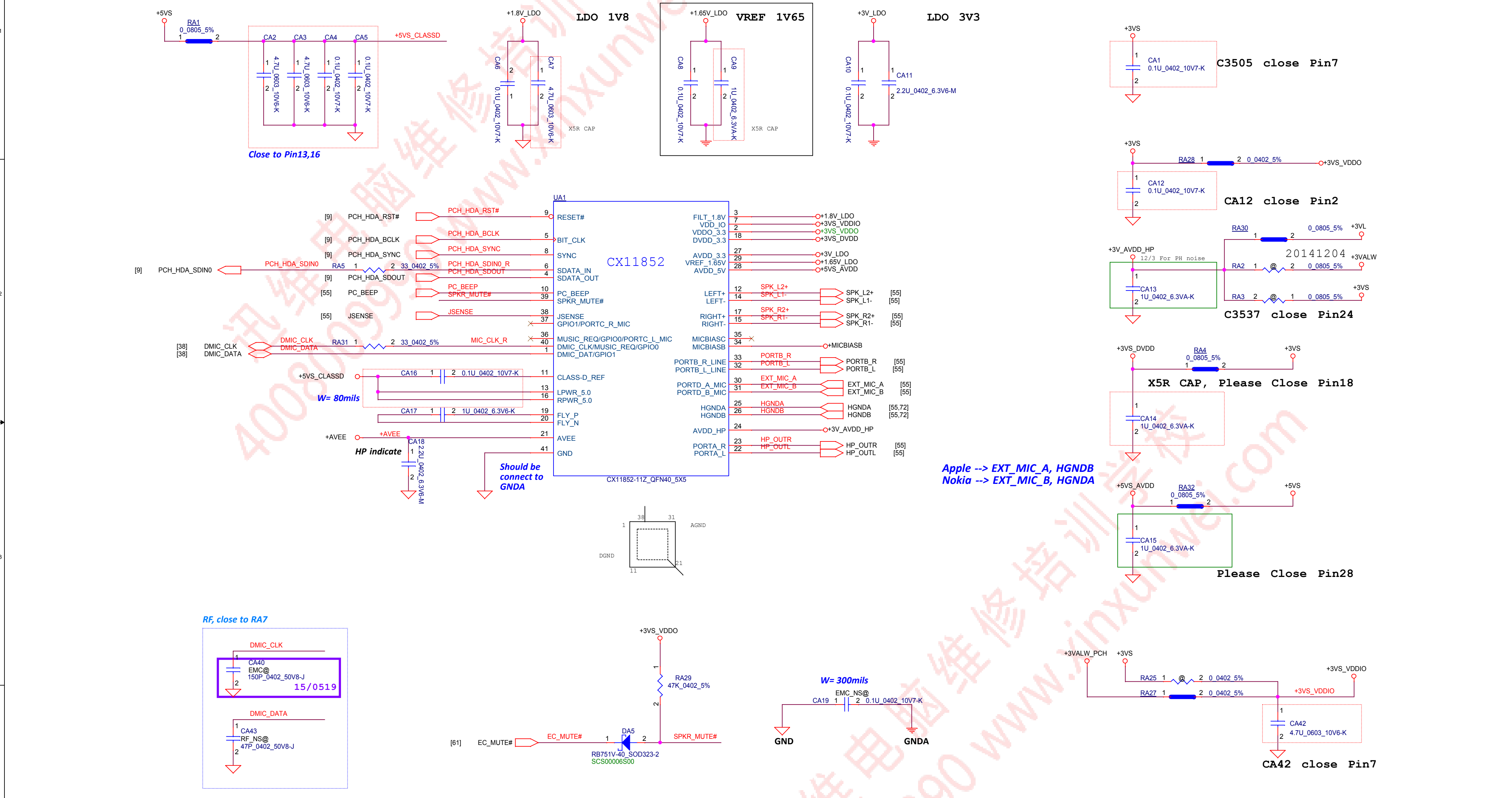


Security Classification	LC Future Center Secret Data		
Issued Date	2013/09/07	Deciphered Date	2014/09/07

Title: Card Reader

Size: Custom | Document Number: BE560 | Rev: 0.1

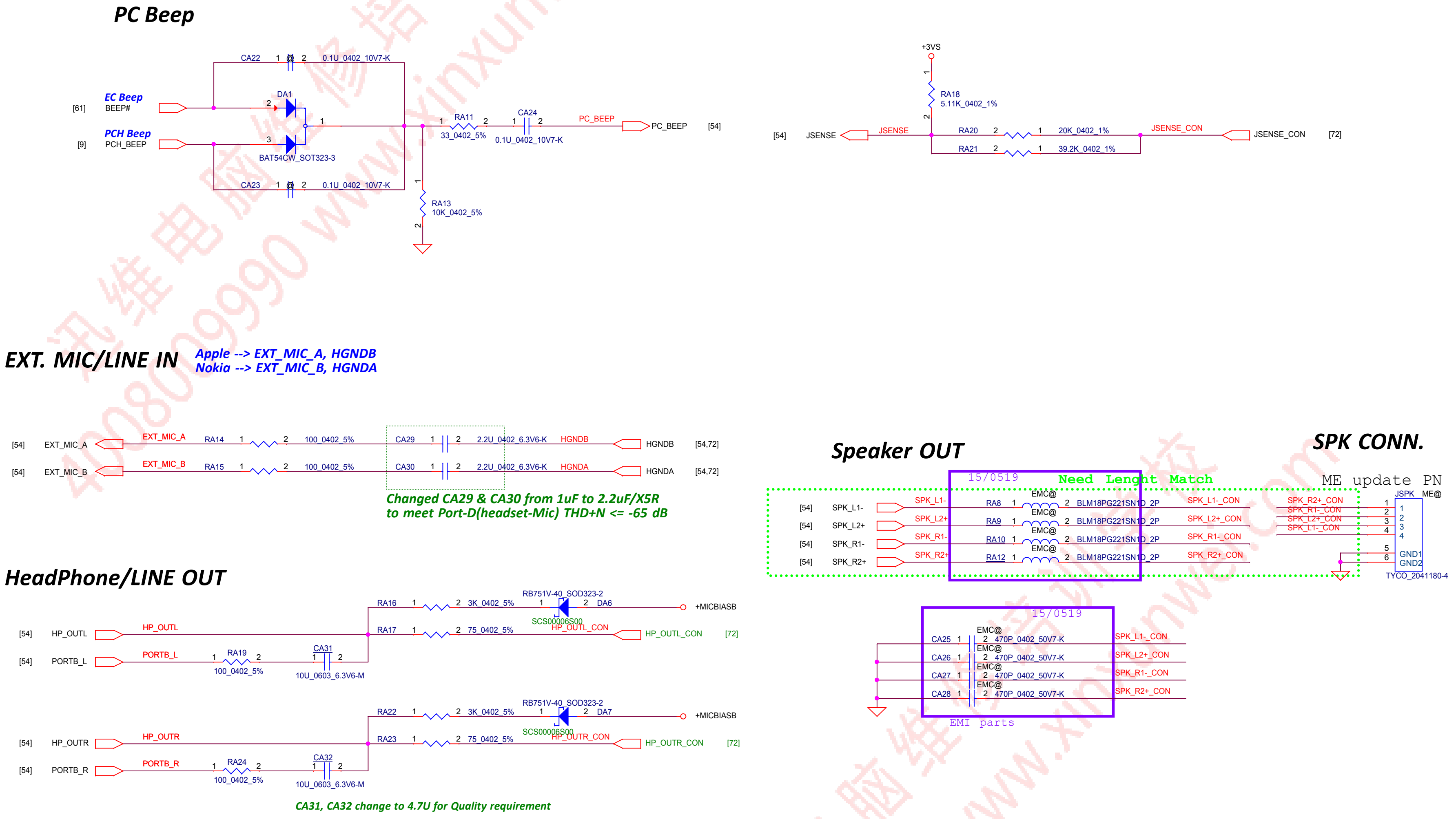
Date: Wednesday, September 23, 2015 | Sheet 51 of 99



Security Classification	LC Future Center Secret Data		
Issued Date	2013/09/07	Deciphered Date	2014/09/07

Title: **Audio Codec**

Size: Custom | Document Number: **BE560** | Rev: 0.1

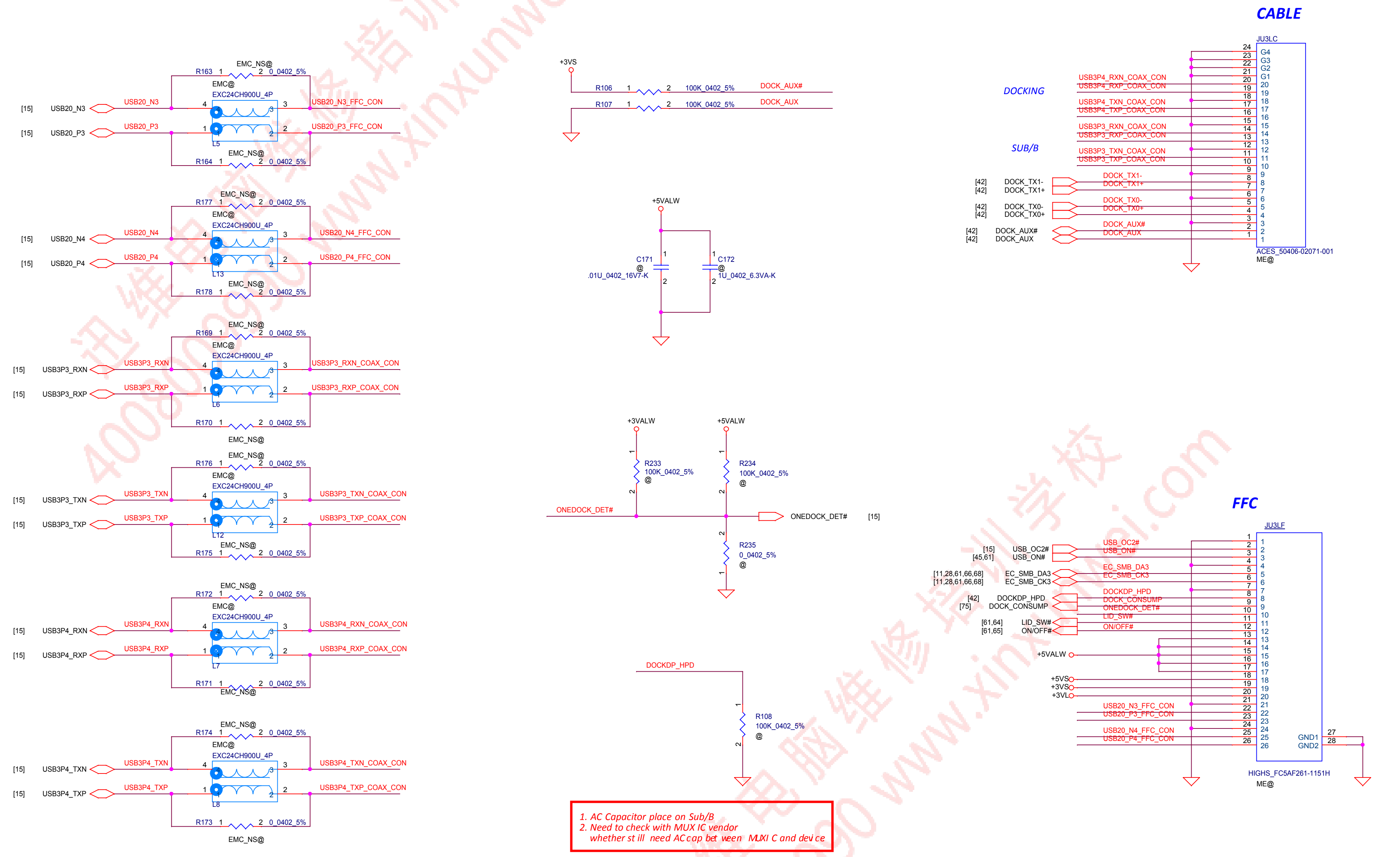


Security Classification	LC Future Center Secret Data		
Issued Date	2013/09/07	Deciphered Date	2014/09/07

Title: HP/MIC JACK/Speaker

Size: Custom | Document Number: BE560 | Rev: 0.1

Date: Wednesday, September 23, 2015 | Sheet 55 of 99



1. AC Capacitor place on Sub/B
2. Need to check with MUX IC vendor
whether still need AC cap between MUXIC and device

Security Classification	LC Future Center Secret Data		
Issued Date	2013/09/07	Deciphered Date	2014/09/07

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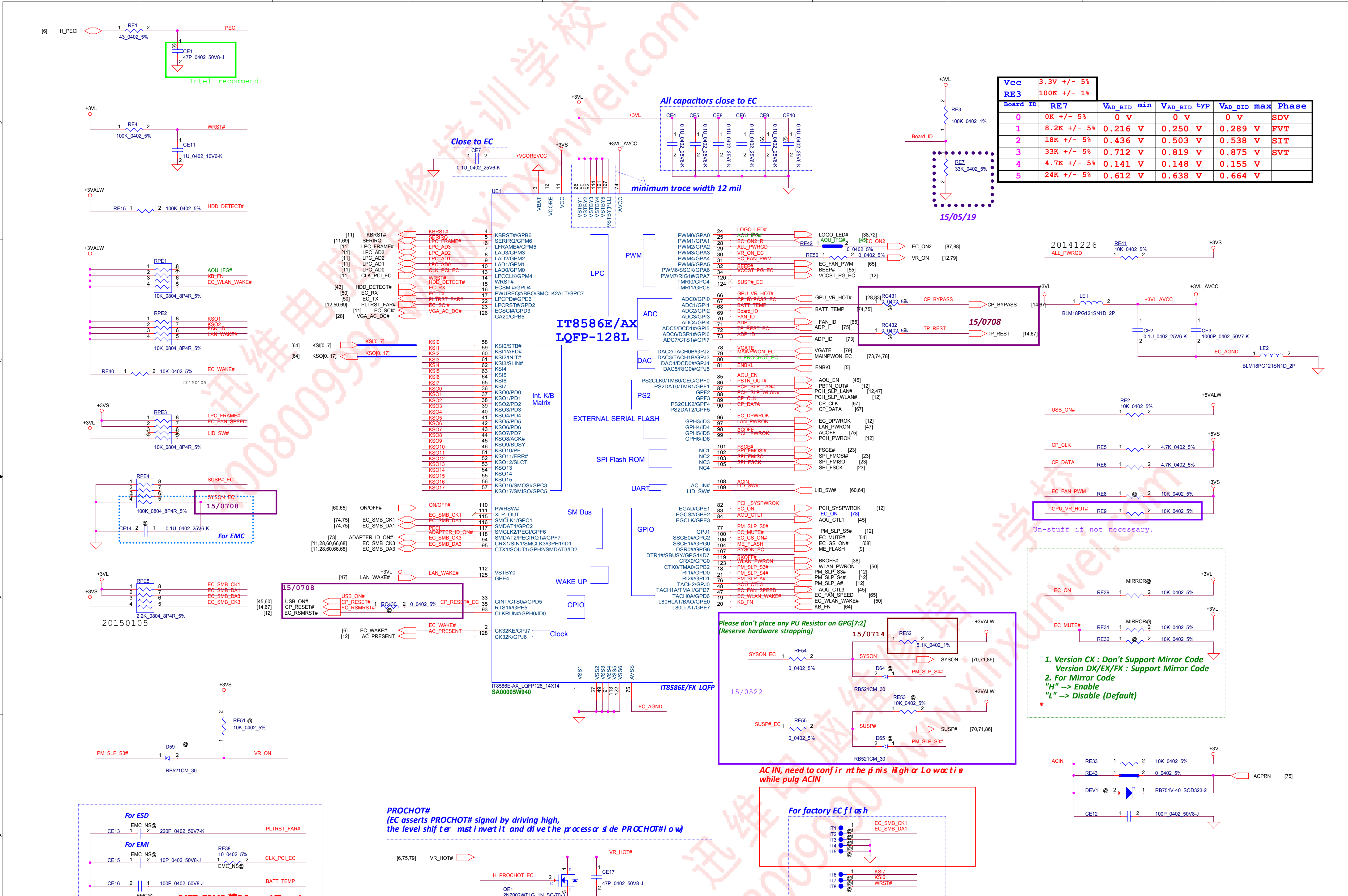
Title: **DOCKING/ DCIN CONN**

LCFC

Size: Custom | Document Number: BE560 | Rev: 0.1

Date: Wednesday, September 23, 2015 | Sheet 60 of 99



Vcc	3.3V +/- 5%
RE3	100K +/- 1%

Board ID	RE7	V_{AD_BID} min	V_{AD_BID} typ	V_{AD_BID} max	Phase
0	0K +/- 5%	0 V	0 V	0 V	SDV
1	8.2K +/- 5%	0.216 V	0.250 V	0.289 V	FVT
2	18K +/- 5%	0.436 V	0.503 V	0.538 V	SIT
3	33K +/- 5%	0.712 V	0.819 V	0.875 V	SVT
4	4.7K +/- 5%	0.141 V	0.148 V	0.155 V	
5	24K +/- 5%	0.612 V	0.638 V	0.664 V	

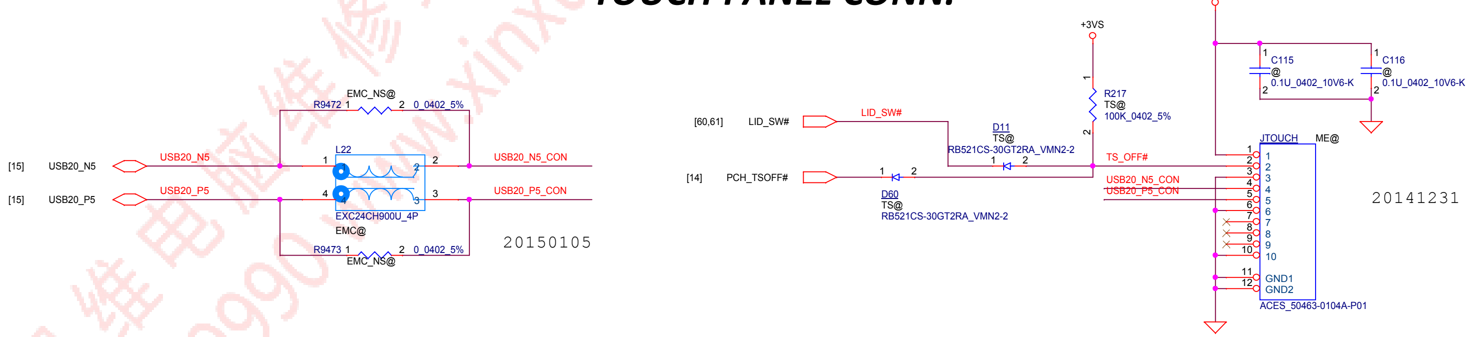
Security Classification	LC Future Center Secret Data		
Issued Date	2013/09/07	Deciphered Date	2014/09/07

Title: EC

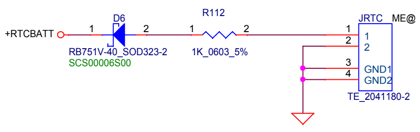
Size: A2 | Document Number: BE560 | Rev: 0.1

Date: Wednesday, September 23, 2015 | Sheet 61 of 99

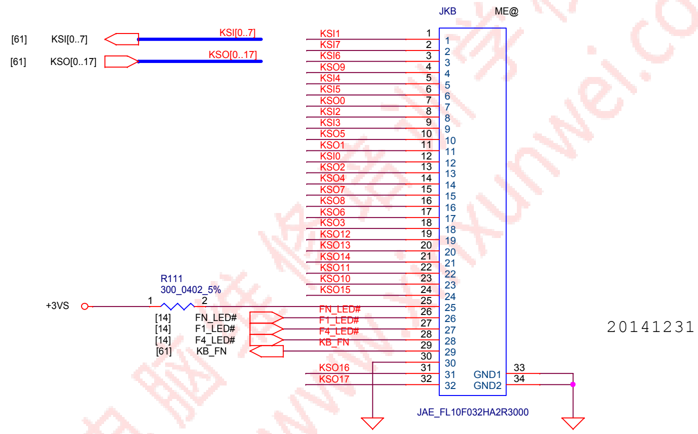
TOUCH PANEL CONN.



RTC CONN.



KB CONN



Security Classification	LC Future Center Secret Data			Title
Issued Date	2013/09/07	Deciphered Date	2014/09/07	KB/RTC/TOUCH PAN CONN.

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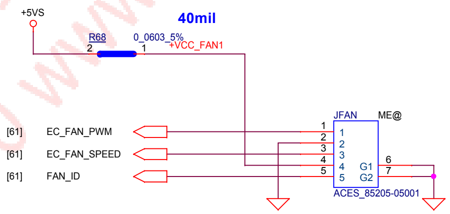
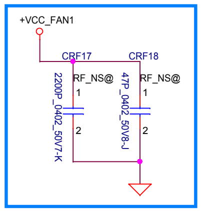
FAN CONN.

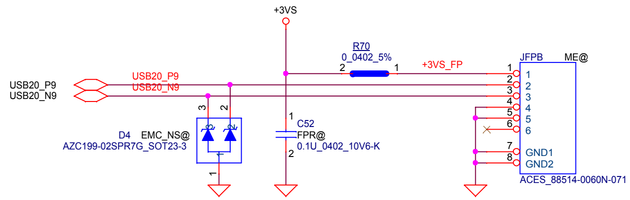


For RF solution.



FingerPrint CONN.

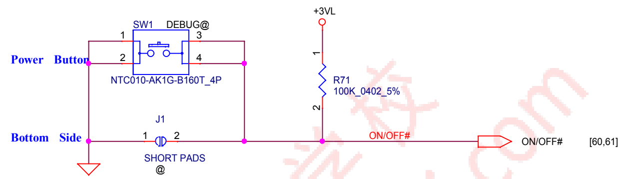
20141126



PWR BTN/LID SW CONN.

For 14" on board
For 15" on Sub/B

ON/OFF Switch --> Sub/B



Lid Switch --> Sub/B

Security Classification	LC Future Center Secret Data		
Issued Date	2013/09/07	Deciphered Date	2014/09/07

Title: PBTN/LID/FP/FAN CONN.

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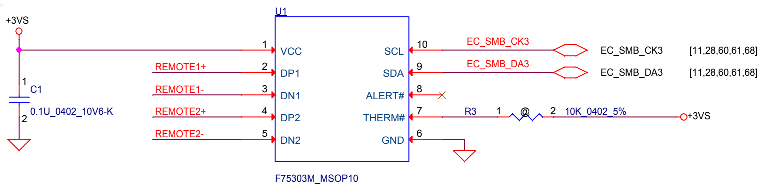
Thermal Sensor

Thermal Sensor
placed near by VRAM



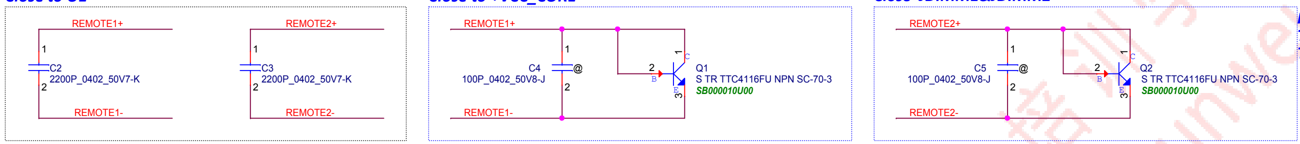
Address 1001_101xb

Internal pull up 1.2K to 1.5V
R for init i d t her md shut do wnt e mp

Close to U1

Close to +VCC_CORE

Close JDIMM1&JDIMM2



REMOTE2+/-:
Trace width/space:10/10 mil
Trace length:<8"

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Title: **Thermal Sensor**

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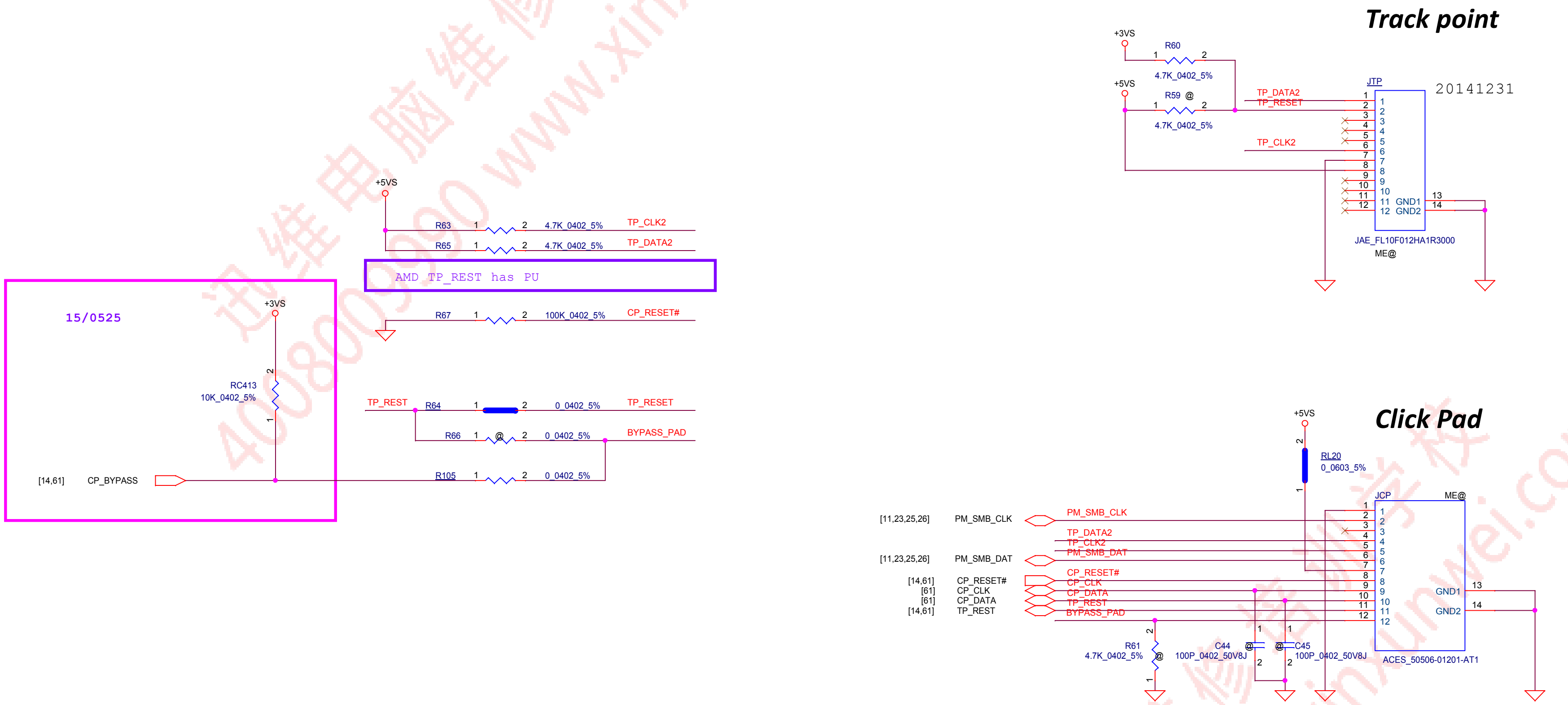

Security Classification	LC Future Center Secret Data		
Issued Date	2013/09/07	Deciphered Date	2014/09/07

Title: CP/TPOINTCONN.

Size: Custom | Document Number: BE560 | Rev: 0.1

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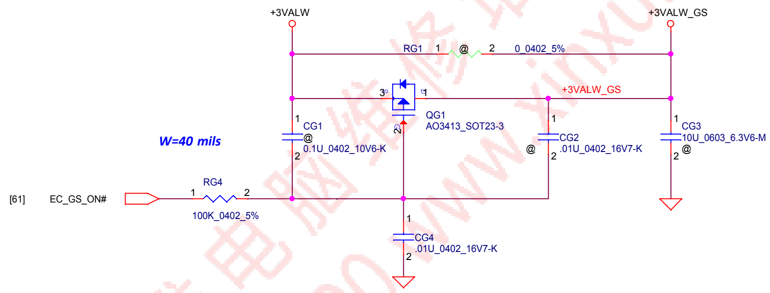
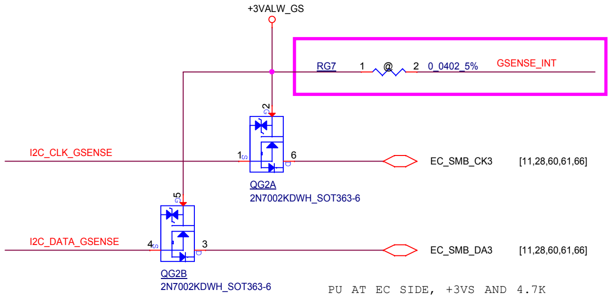


APS G-Sensor

TABLE

P/N	ADDR_SEL	Address
LIS3DH	H L	32h (W) & 33h (R) 30h (W) & 31h (R)
KX023-1025	H L	3Eh (W) & 3Fh (R) 3Ch (W) & 3Dh (R)

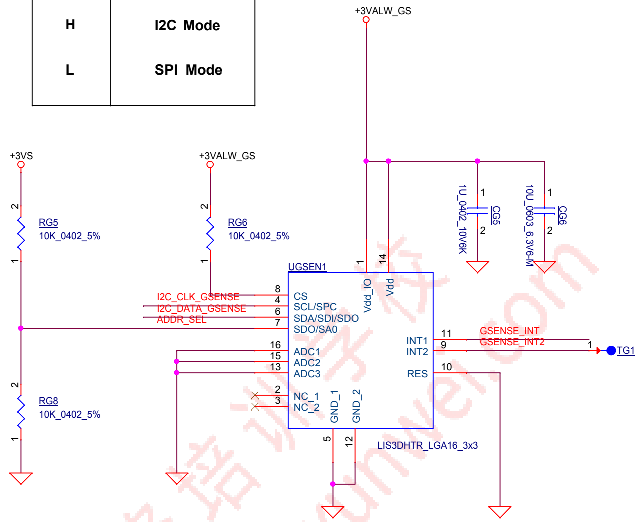


PU AT EC SIDE, +3VS AND 4.7K

20141204

TABLE

P/N	Mode Selection
H	I2C Mode
L	SPI Mode



Security Classification	LC Future Center Secret Data		
Issued Date	2013/09/07	Deciphered Date	2014/09/07

Title: G SENSOR

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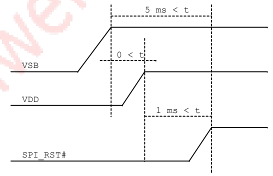
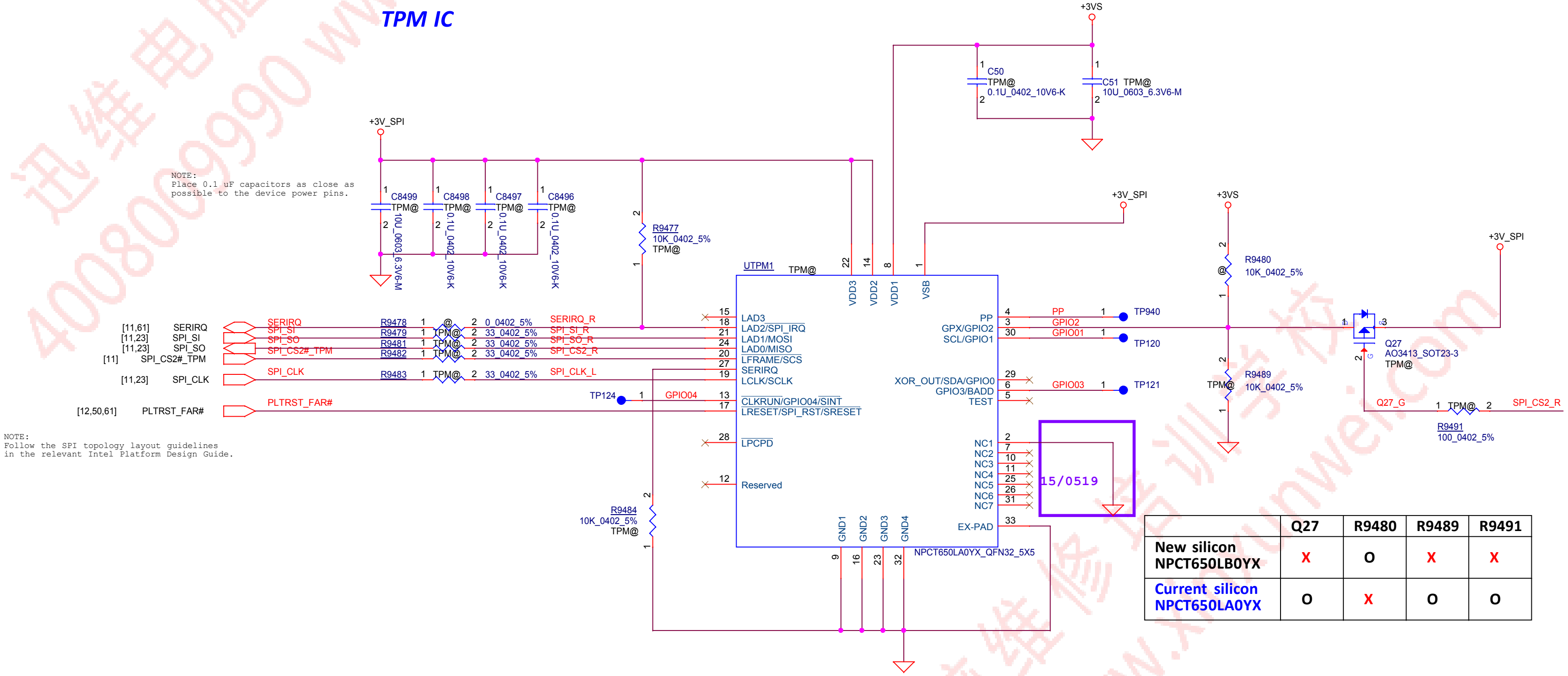


NOTE:
1) It is recommended to connect the TPM to the system's standby voltage to improve performance.
2) SPI_RST# must be asserted for at least 5 msec after VSB power-up.
3) VSB may come up anytime before VDD power-up, but not after VDD power-up.
4) SPI_RST# may be asserted together with VDD power negation, but should not at any point exceed 0.5V above the VDD power level.

TPM IC



	Q27	R9480	R9489	R9491
New silicon NPCT650LB0YX	X	O	X	X
Current silicon NPCT650LA0YX	O	X	O	O

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Issued Date	2013/09/07	Deciphered Date	2014/09/07

Title: TPM

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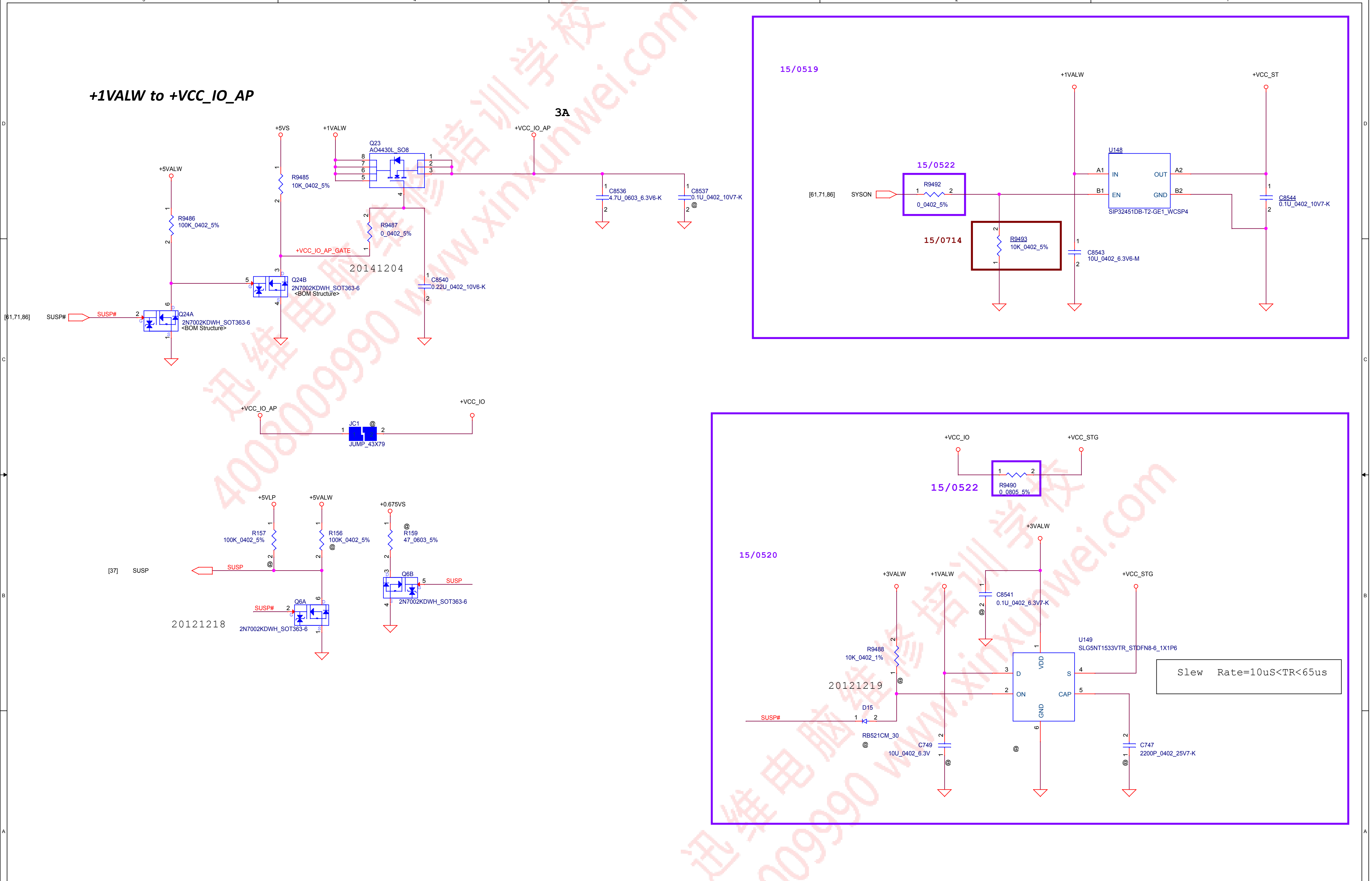
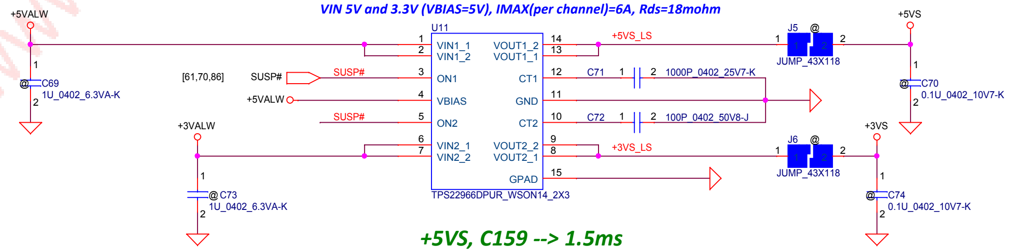


Security Classification	LC Future Center Secret Data		
Issued Date	2013/09/07	Deciphered Date	2014/09/07

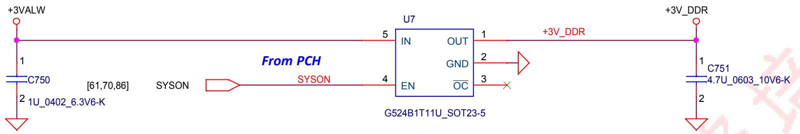
Title: VCC_IO and VCC_ST LSW

Size: Custom | Document Number: BE560 | Rev: 0.1

Load Switch
+5VALW To +5VS
+3VALW To +3VS



+3VALW to +3V_DDR



Security Classification	LC Future Center Secret Data		
Issued Date	2013/09/07	Deciphered Date	2014/09/07

Title: DC V TO VS/ V-PCH/VM

LCFC

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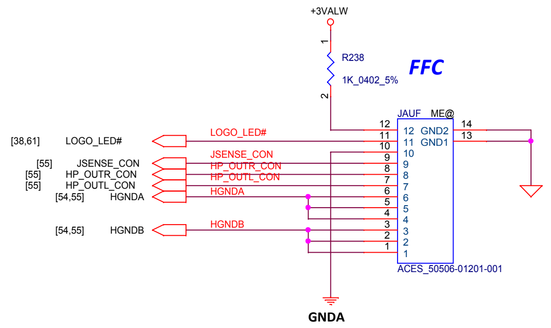
For Edge 15'' Audio board CONNECTOR



Security Classification	LC Future Center Secret Data		
Issued Date	2013/09/07	Deciphered Date	2014/09/07

Title: **SUB-AUDIO JACK CONN.**

LCFC

Size	Document Number	Rev
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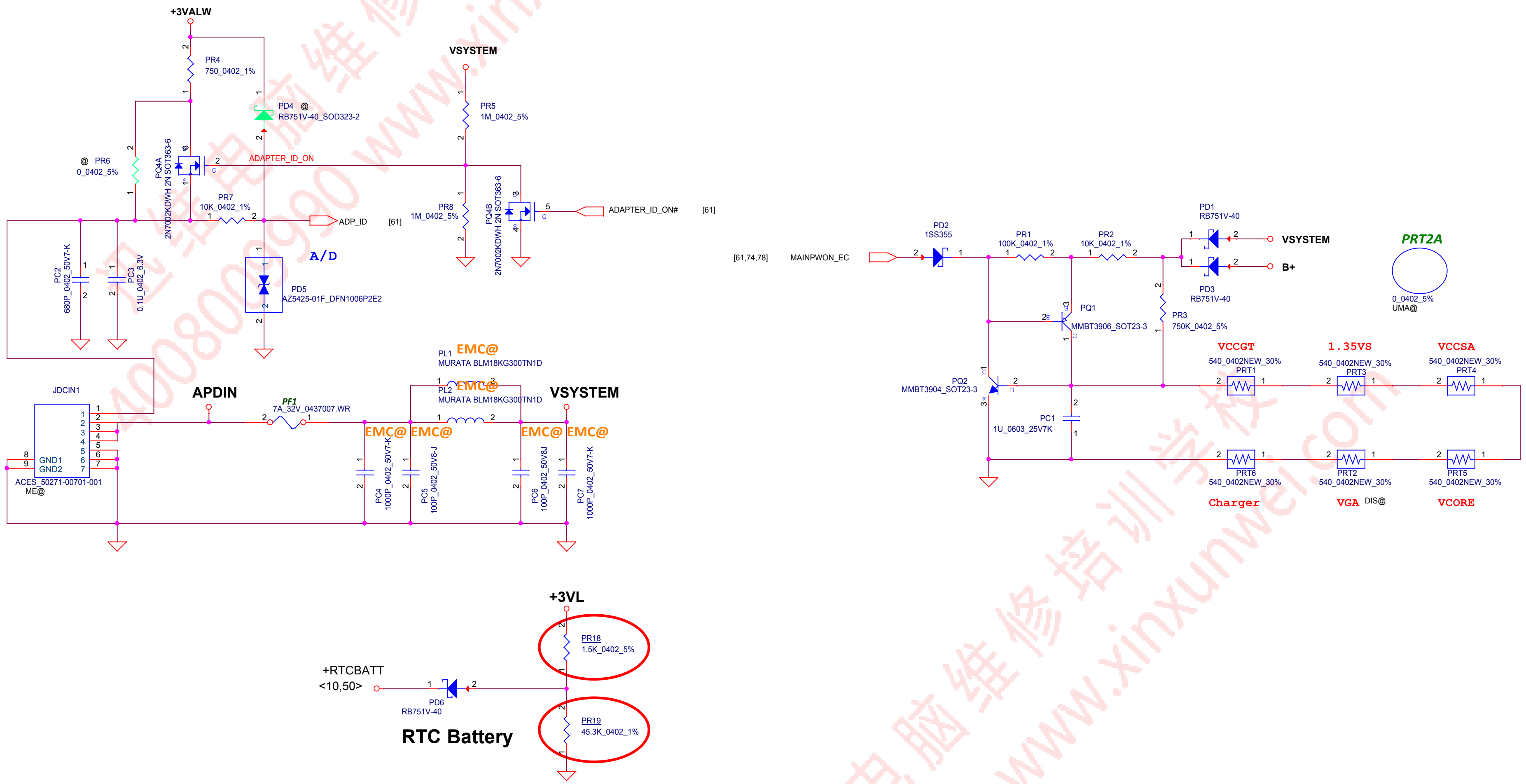

Security Classification	LC Future Center Secret Data		
Issued Date	2013/08/05	Deciphered Date	2014/12/31

Title: DCIN / Vin Detector

Size: Custom | Document Number: AIVL2 NM-A351 | Rev 0.1

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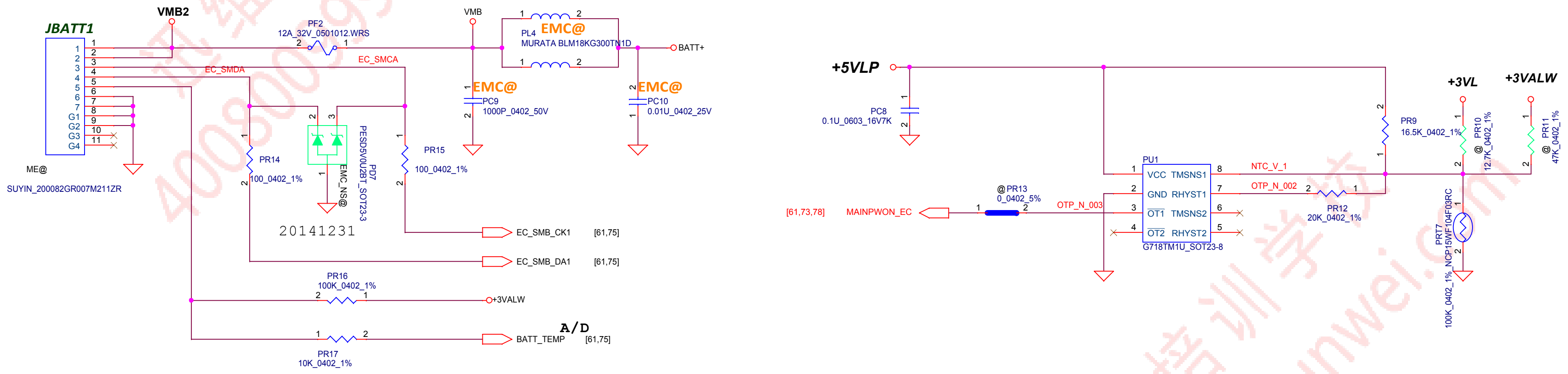
PRT7 under CPU botten side :
CPU thermal protection at 93 +-3 degree C
Recovery at 56 +-3 degree C



Security Classification	LC Future Center Secret Data			Title
Issued Date	2013/08/05	Deciphered Date	2014/12/31	***BATTERY CONN/OTP***

LCFC

Size: Custom | Document Number: AIVL2 NM-A351 | Rev 0.1

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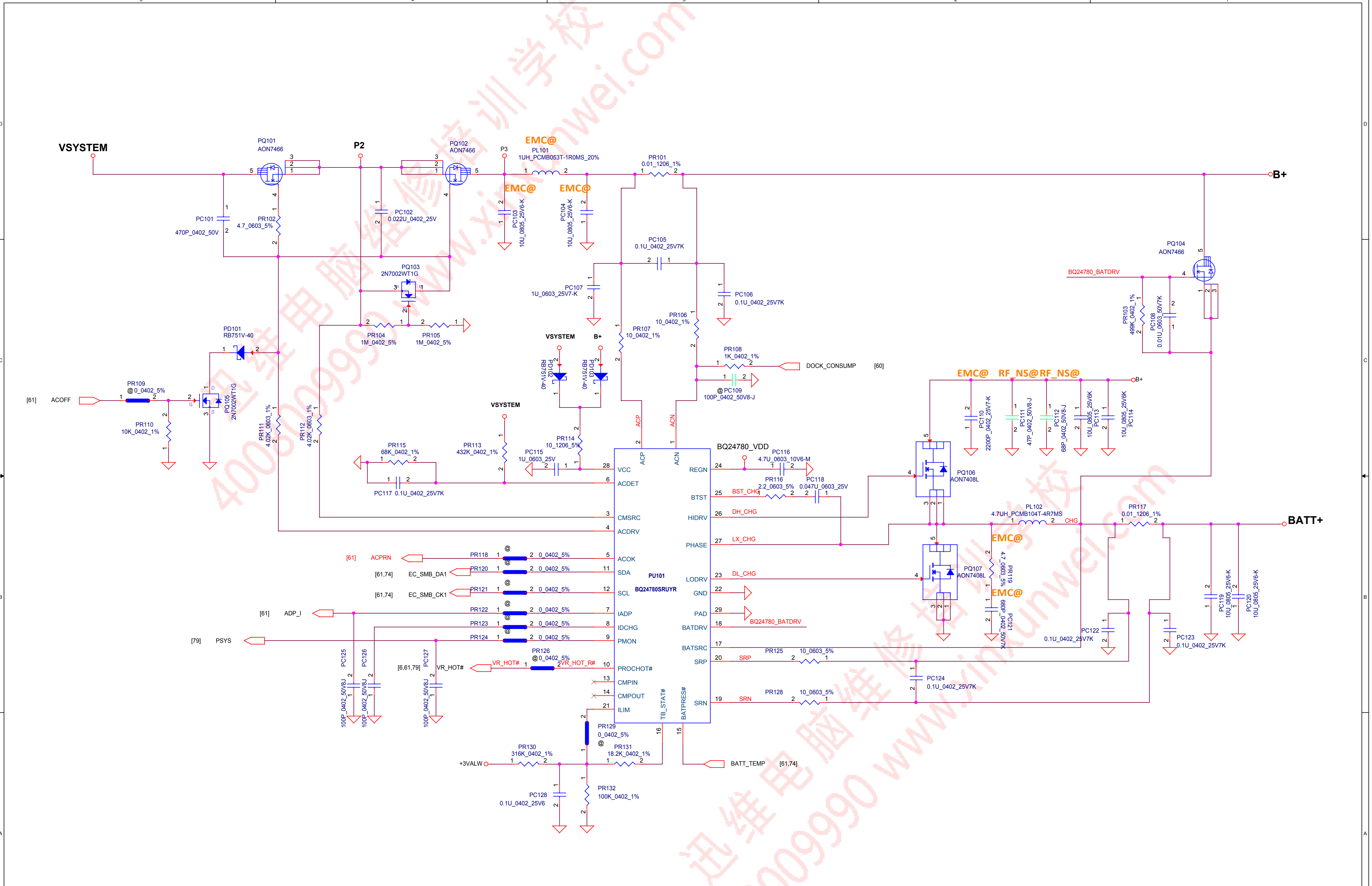
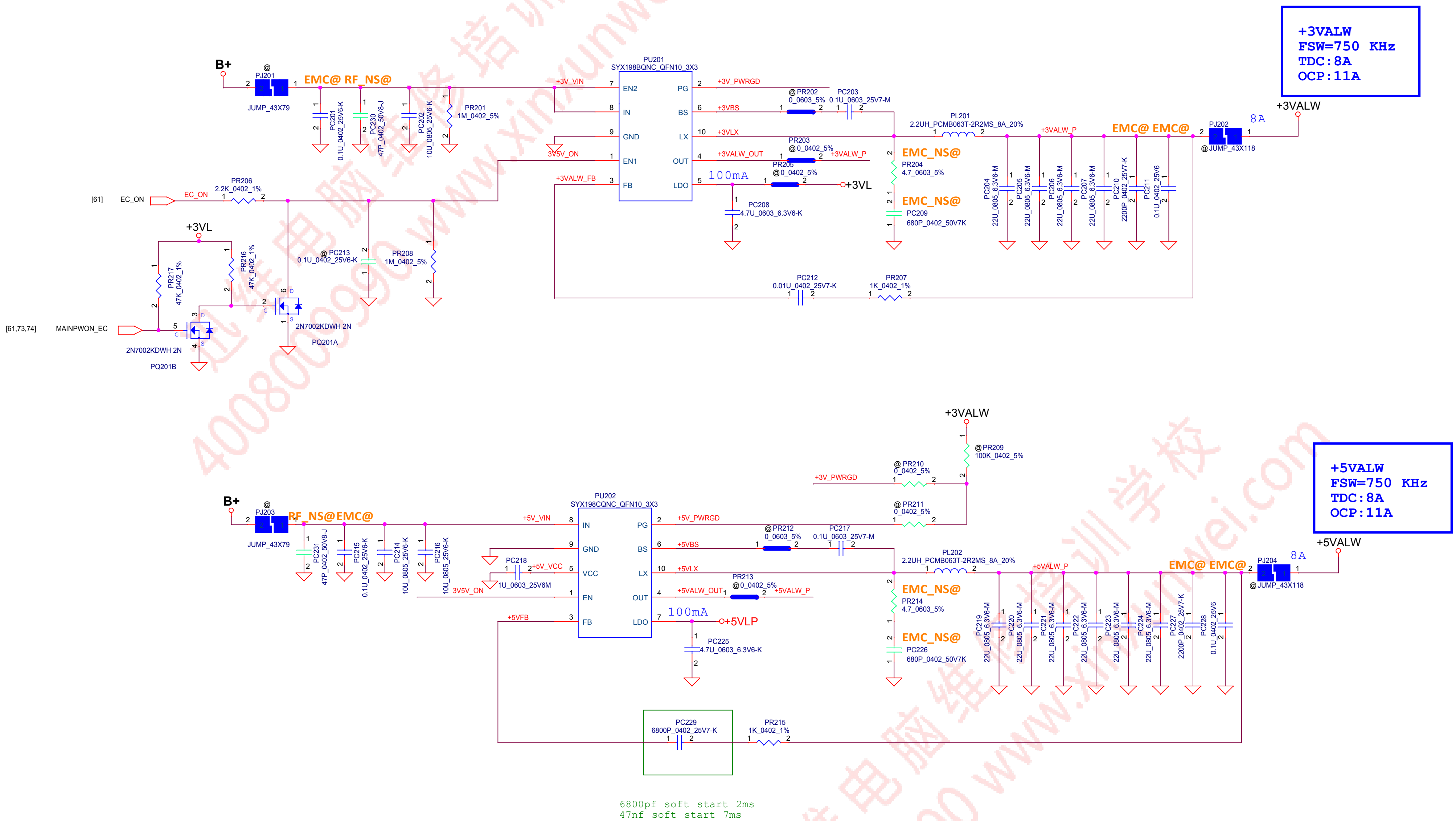


Security Classification	LC Future Center Secret Data		
Issued Date	2013/08/05	Deciphered Date	2014/12/31

Title: CHARGER

LCFC

Size	Document Number	Rev
Custom	AIVL2 NM-A351	0.1



Security Classification	LC Future Center Secret Data		
Issued Date	2013/08/08	Deciphered Date	2013/08/05

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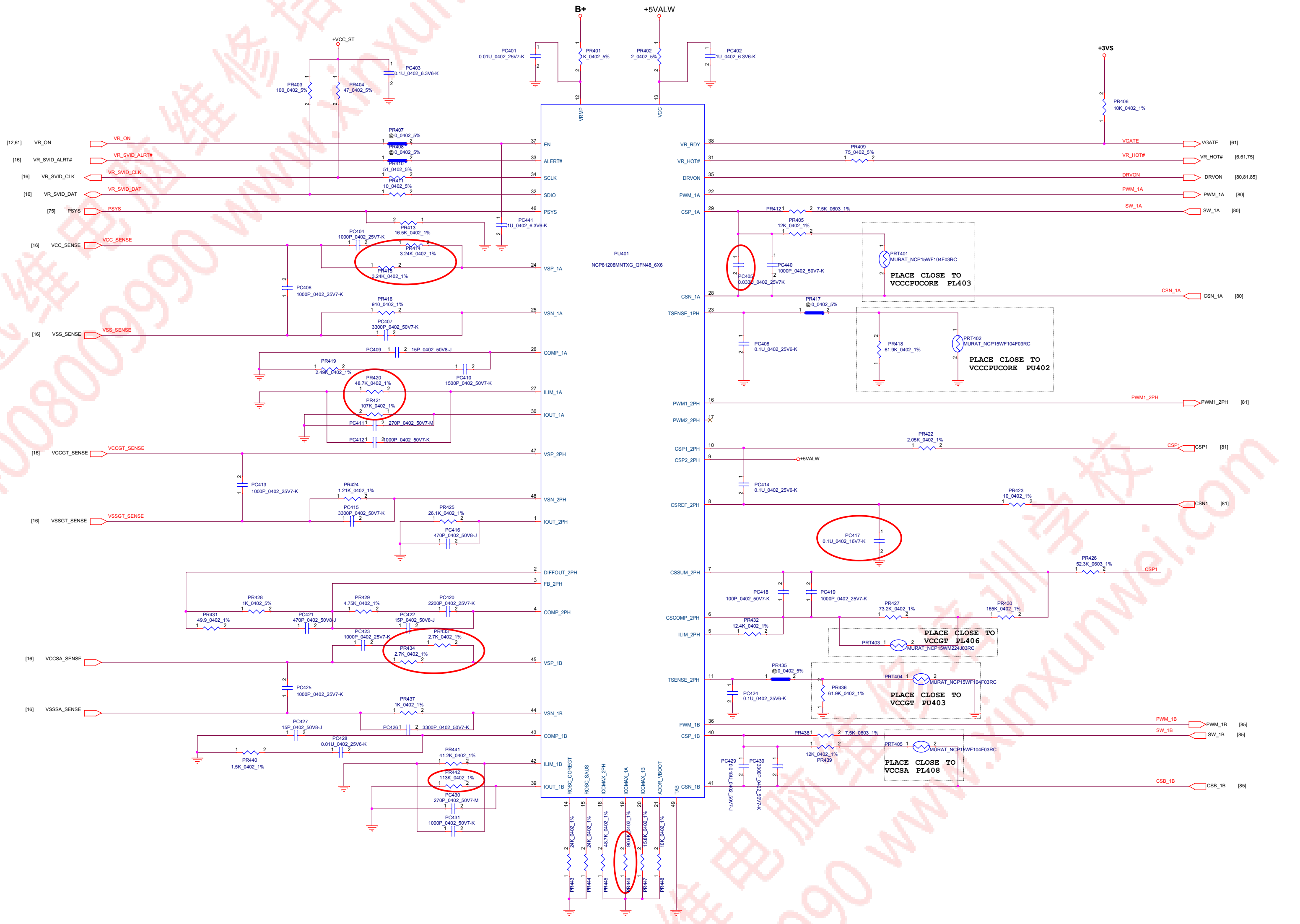
Title: 3VALWP/5VALWP

Size: Custom | Document Number: AIVL2 NM-A351 | Rev: 0.1

Date: Wednesday, September 23, 2015 | Sheet 78 of 99

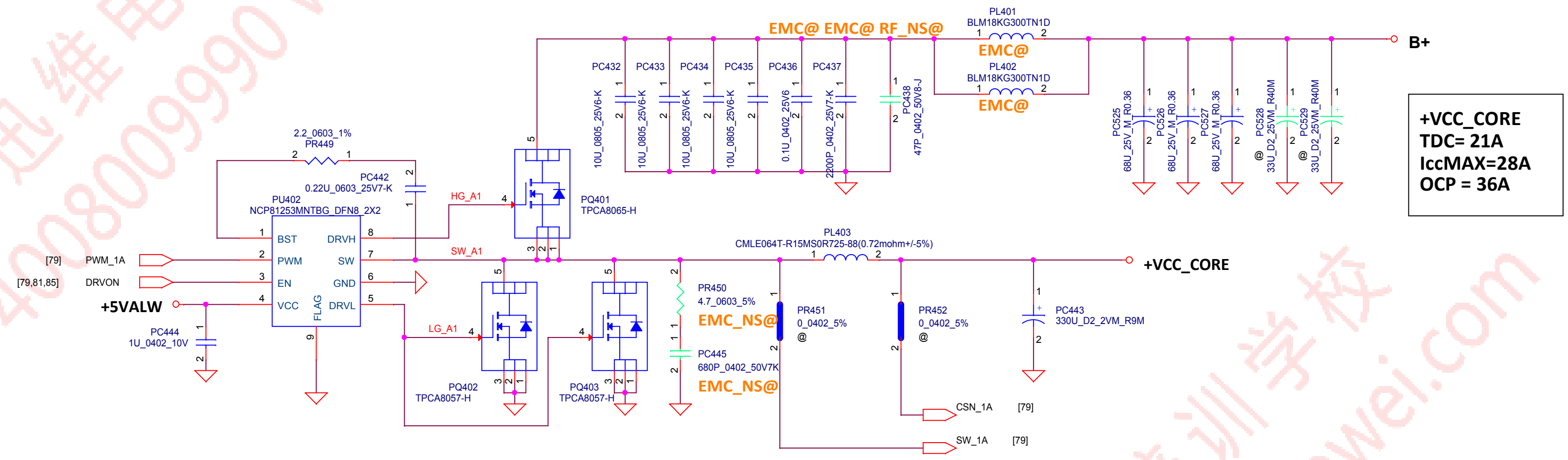


Security Classification	LC Future Center Secret Data		
Issued Date	2013/08/05	Deciphered Date	2014/12/31

Title: IMVP8

Size: A1 | Document Number: AIVL2 NM-A351 | Rev: 0.1

Date: Wednesday, September 23, 2015 | Sheet 79 of 99



Security Classification	LC Future Center Secret Data		
Issued Date	2013/08/05	Deciphered Date	2014/12/31

Title: +VCC_CORE

LCFC

Size	Document Number	Rev
Custom	AIVL2 NM-A351	0.1

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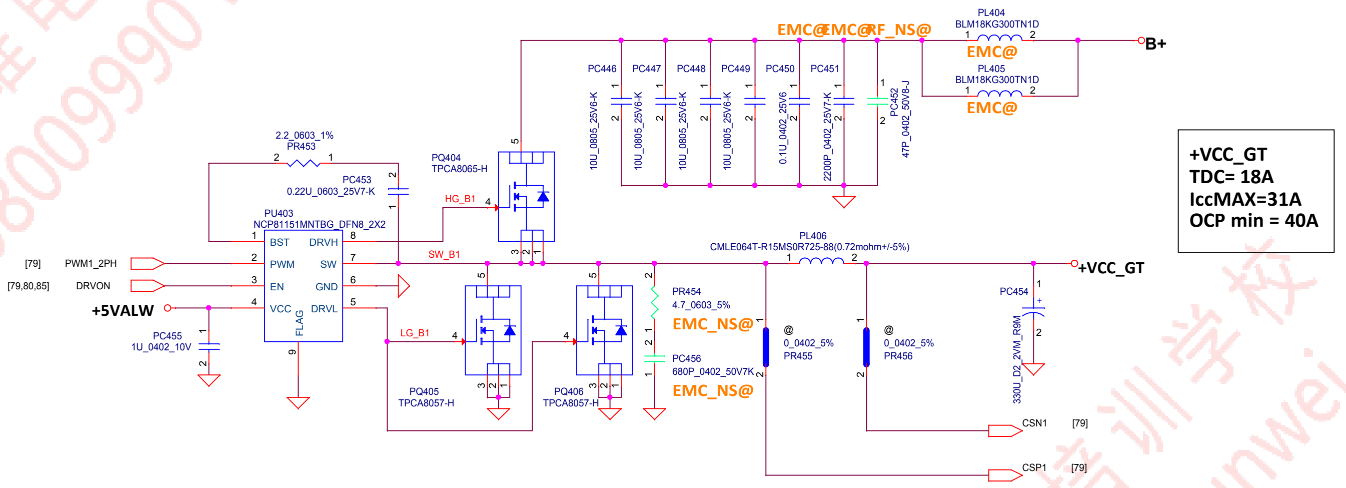


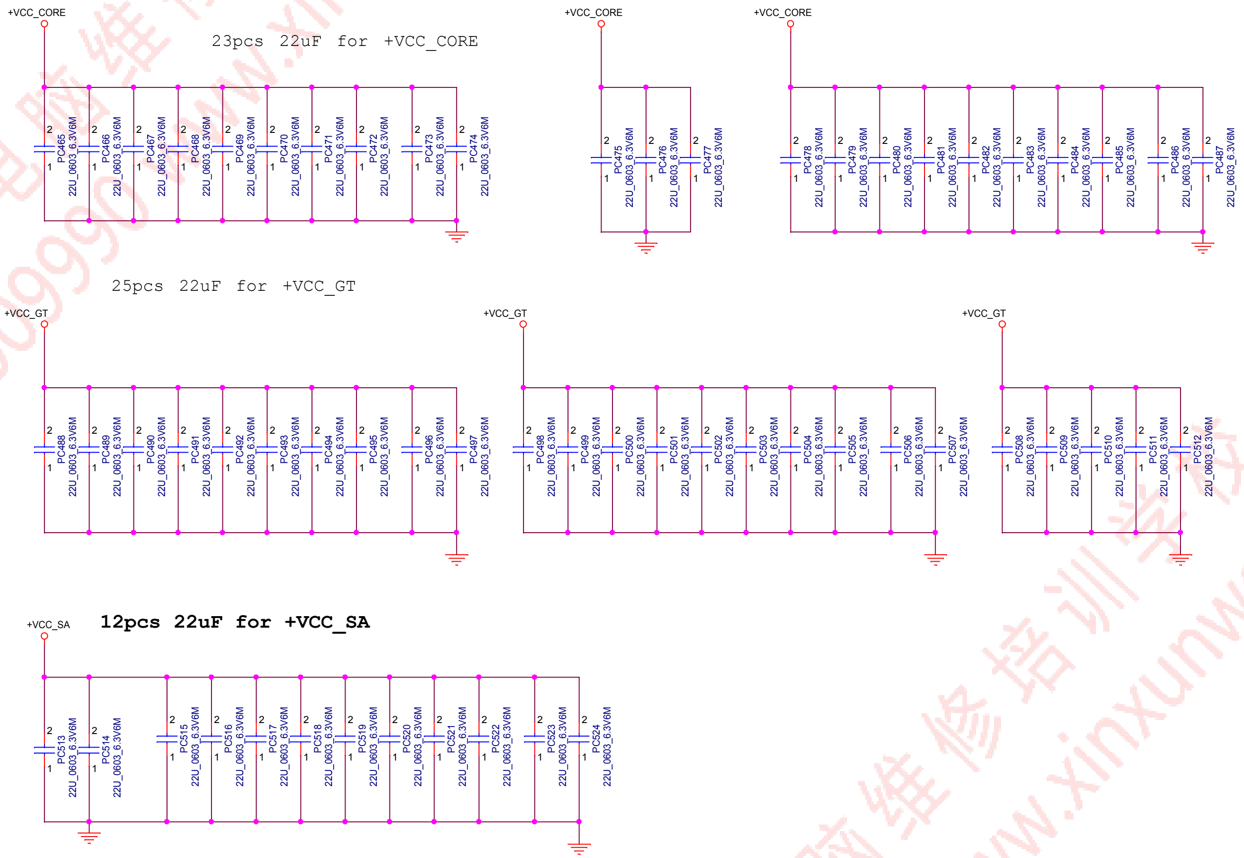
Security Classification	LC Future Center Secret Data		
Issued Date	2013/08/05	Deciphered Date	2014/12/31

Title: +VCC_GT

LCFC

Size: Custom | Document Number: AIVL2 NM-A351 | Rev 0.1

Based on PDDG rev 0.7 Table 5-1.



Security Classification	LC Future Center Secret Data		
Issued Date	2013/08/05	Deciphered Date	2014/12/31

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Title: **PROCESSOR DECOUPLING**

Size	Document Number	Rev
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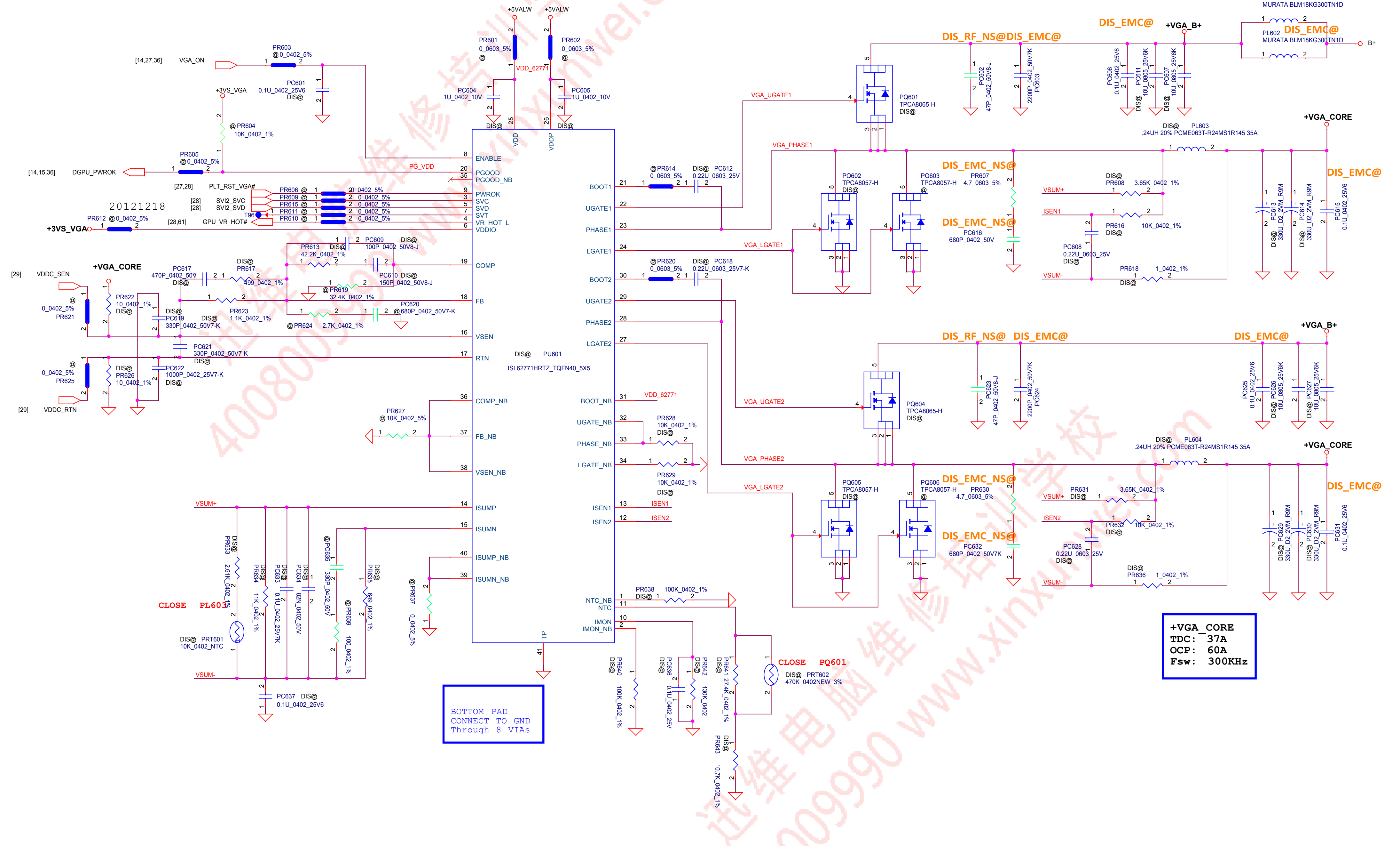
ISL62771 Schematic for FT3 solution



Security Classification	LC Future Center Secret Data		
Issued Date	2013/08/05	Deciphered Date	2014/12/31

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Title: +VGA_CORE

LCFC

Size	Document Number	Rev
Custom	AIVL2 NM-A351	0.1

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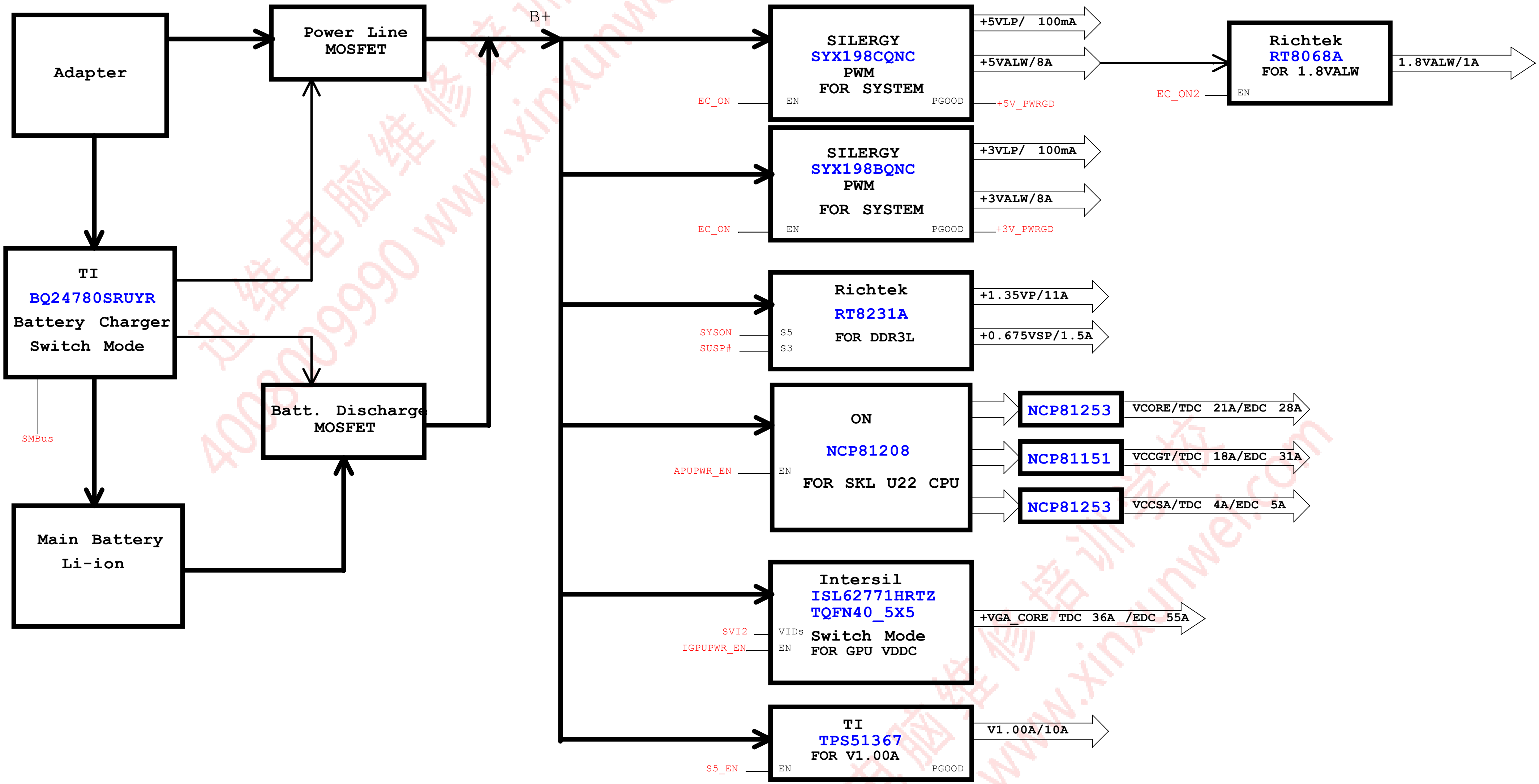

Security Classification	LC Future Center Secret Data		
Issued Date		Deciphered Date	

Title	Block Diagram	
Size: Custom	Document Number: AIVL2_NM-A351	Rev: 0.1
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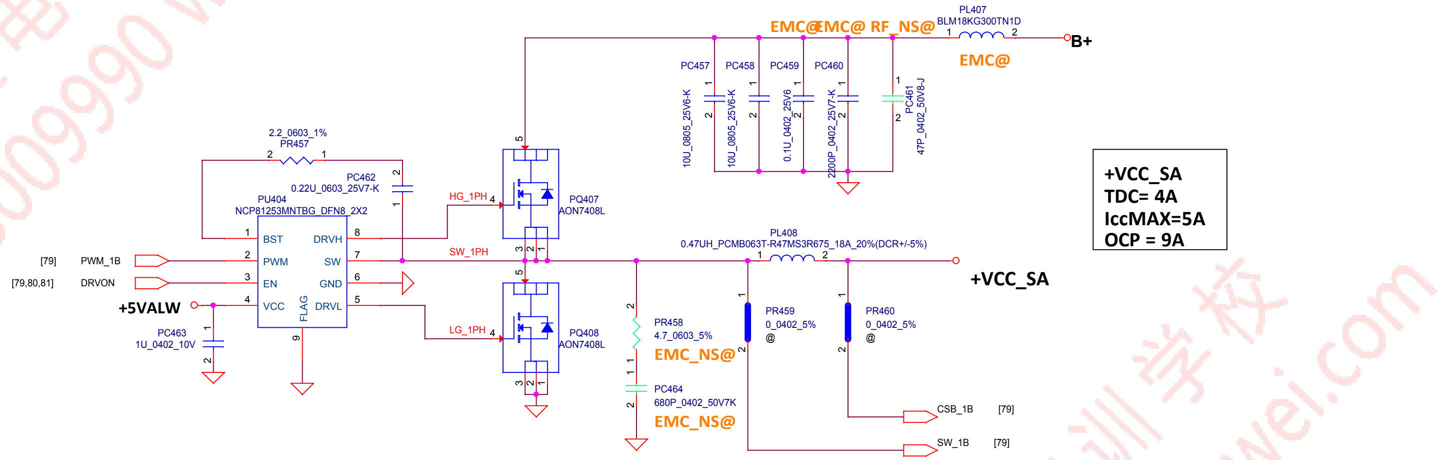

Security Classification	LC Future Center Secret Data		
Issued Date	2013/08/05	Deciphered Date	2014/12/31

Title: VCCSA

LCFC

Size	Document Number	Rev
Custom	AIVL2 NM-A351	0.1

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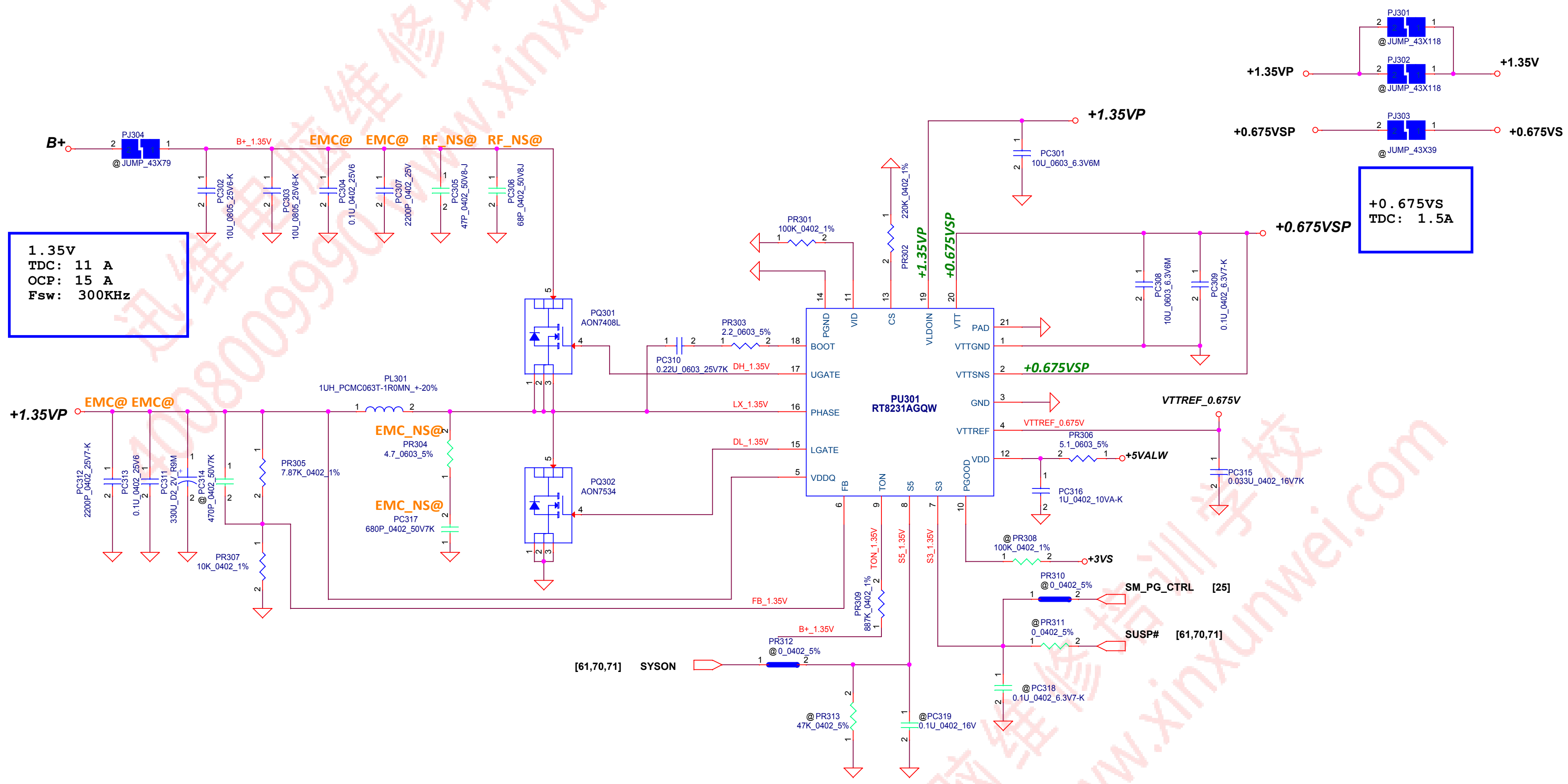

Security Classification	LC Future Center Secret Data			Title
Issued Date	2013/08/05	Deciphered Date	2014/12/31	+1.35V/+0.675VS

Size	Document Number	Rev
Custom	AIVL2 NM-A351	0.1

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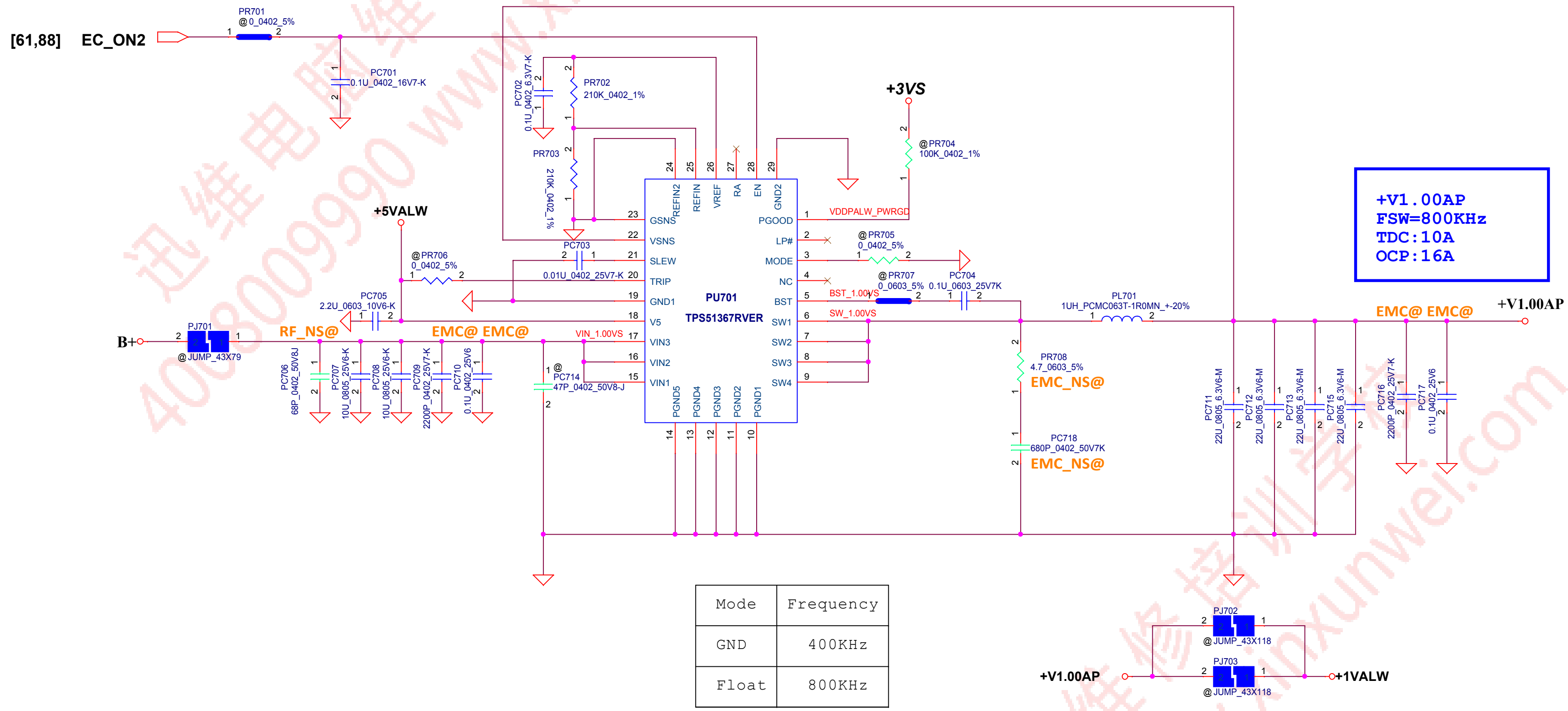

Mode	Frequency
GND	400KHz
Float	800KHz

Security Classification	LC Future Center Secret Data		
Issued Date	2013/08/01	Deciphered Date	2014/08/01

Title: +V1.00A

Size: Custom | Document Number: AIVL2 NM-A351 | Rev: 0.1

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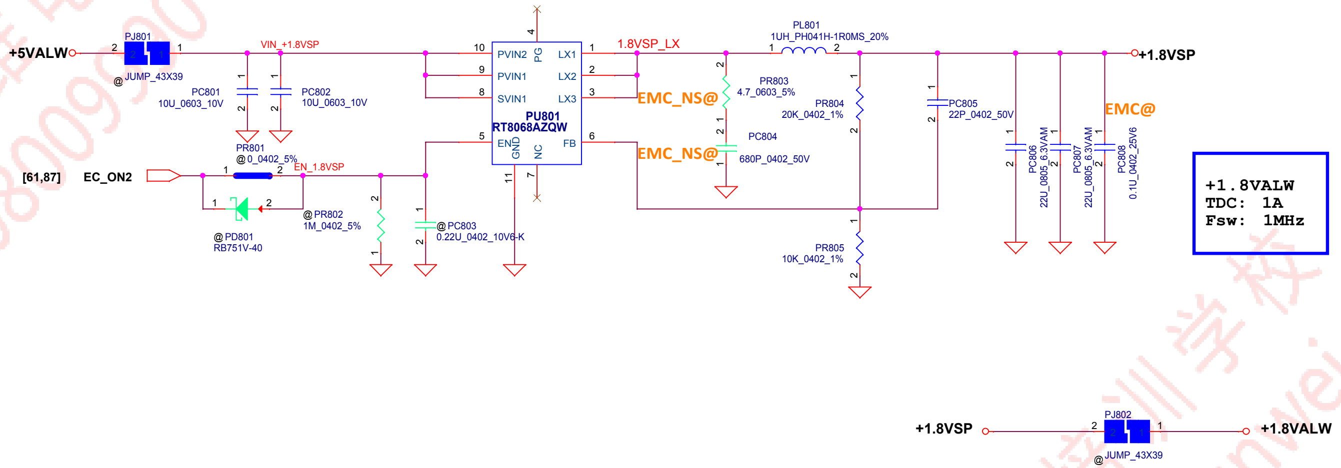

Security Classification	LC Future Center Secret Data		
Issued Date	2013/08/05	Deciphered Date	2014/12/31

Title: +1.8VS_VGA

Size	Document Number	Rev
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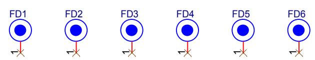
Screw Hole

Center Zero

PCB Fedical Mark PAD



Security Classification	LC Future Center Secret Data			Title XXXX
Issued Date	2013/09/07	Deciphered Date	2014/09/07	

Size	Document Number	Rev
Custom	BE560	0.1

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