

CS10G VGA
N10X_40nm_DDR3
CS2 Build

2009/7/16

INVENTEC			
TITLE <i>Nvidia_VGA_X01</i>			
SIZE A3	CODE CS	DOC. NUMBER	REV
SHEET		1	OF 14

CHANGE by CS10G 16-Jul-2009

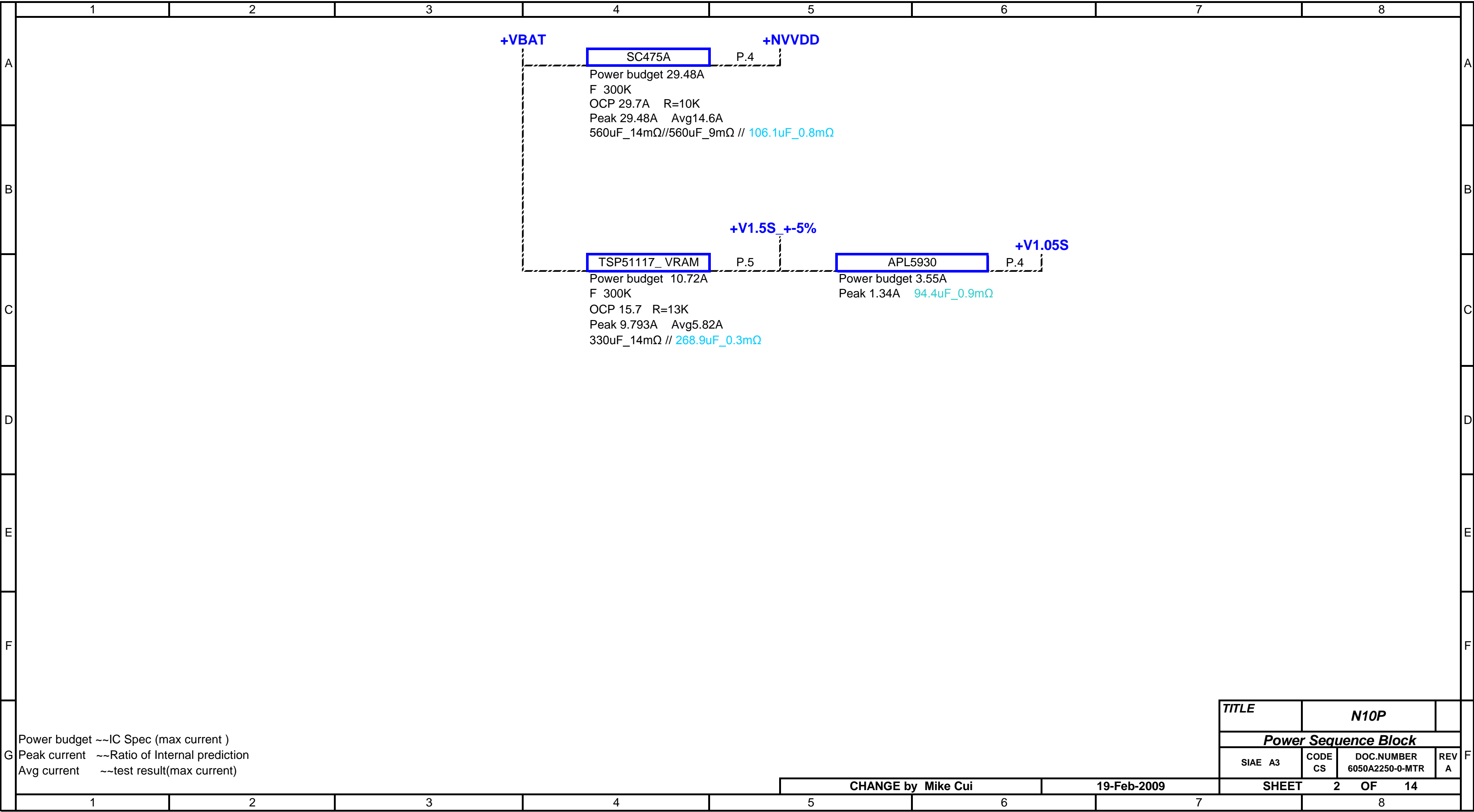
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A									A
B									B
C									C
D									D
E									E
F									F

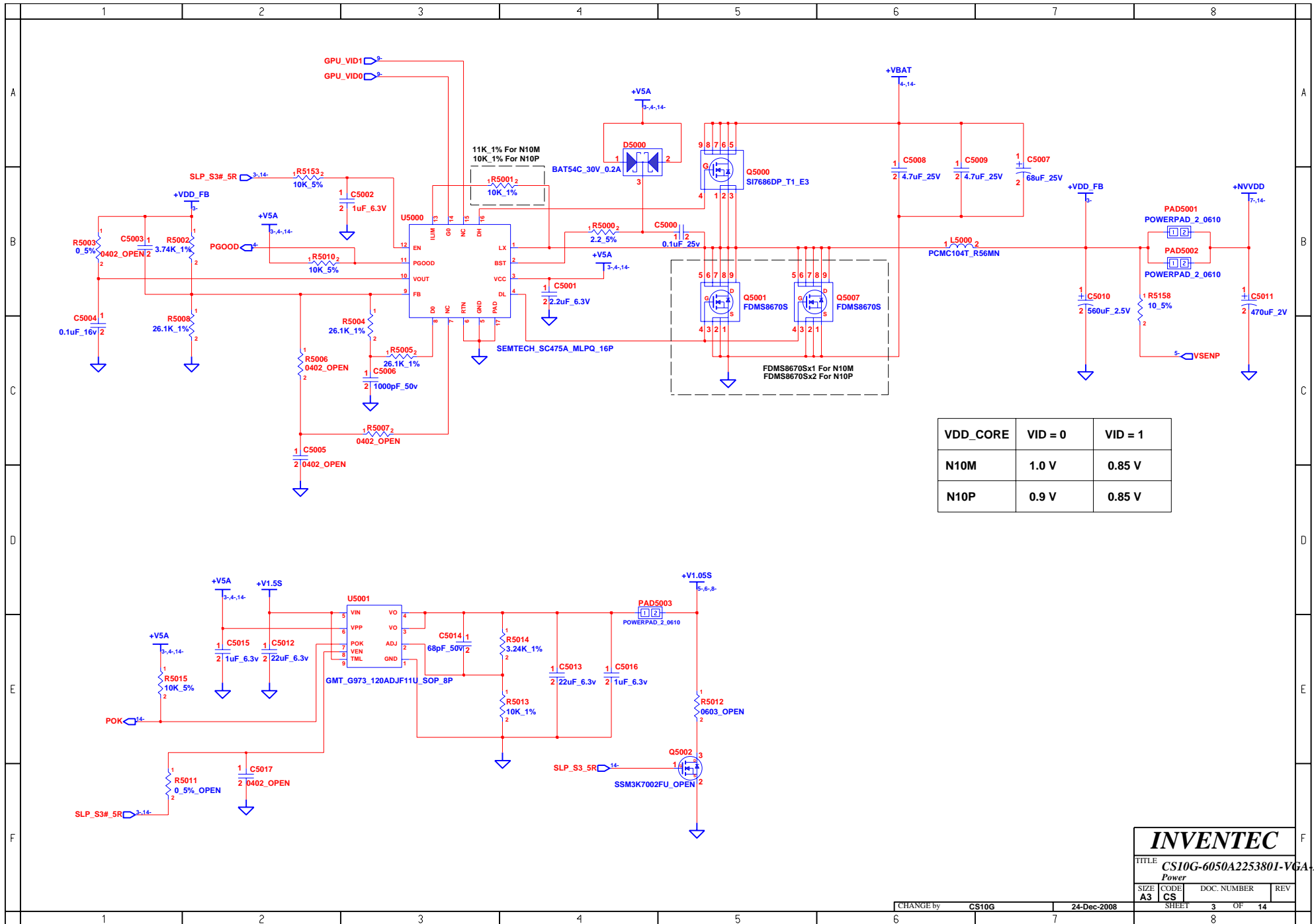
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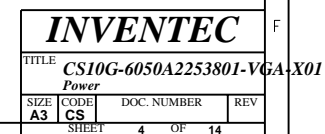
PAGE
Power
Power
GPU
GPU
GPU
GPU
GPU
GPU
VRAM DDR3
VRAM DDR3
VRAM DDR3
VRAM DDR3
MXM

INVENTEC			
TITLE <i>Nvidia_VGA_X01</i>			
SIZE A3	CODE CS	DOC. NUMBER	REV
CHANGE by CS10G		25-Dec-2008	SHEET 2 OF 14

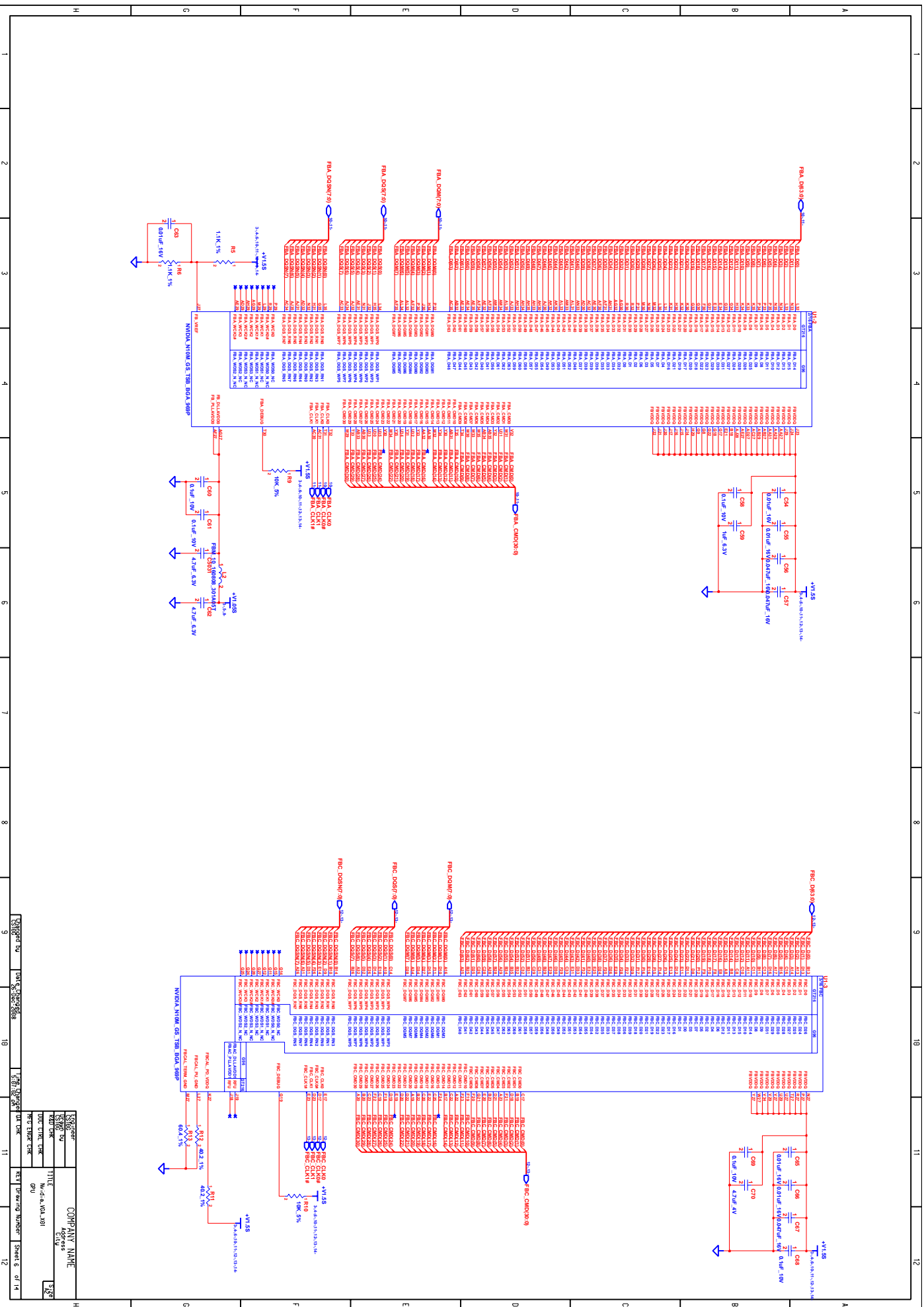
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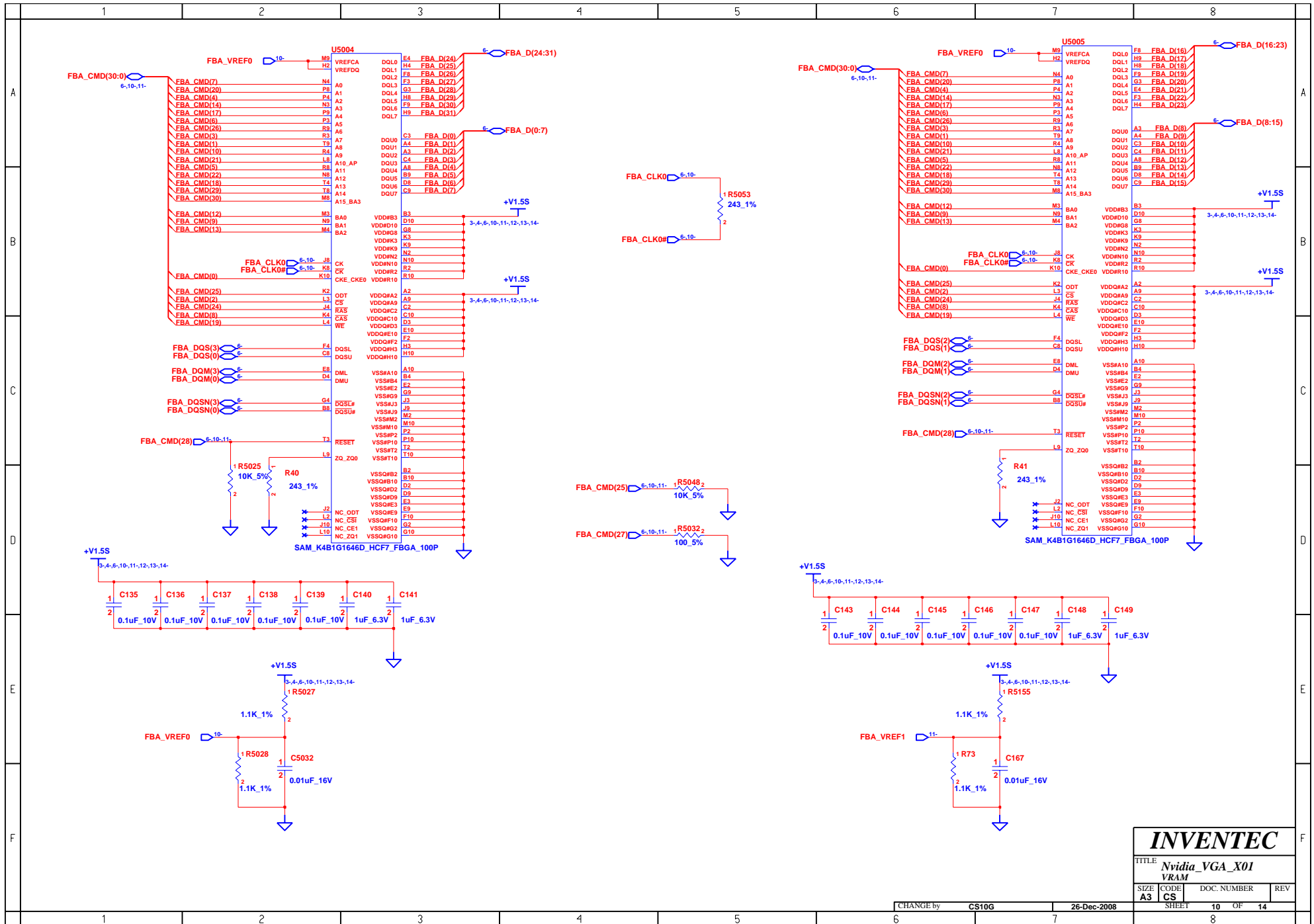


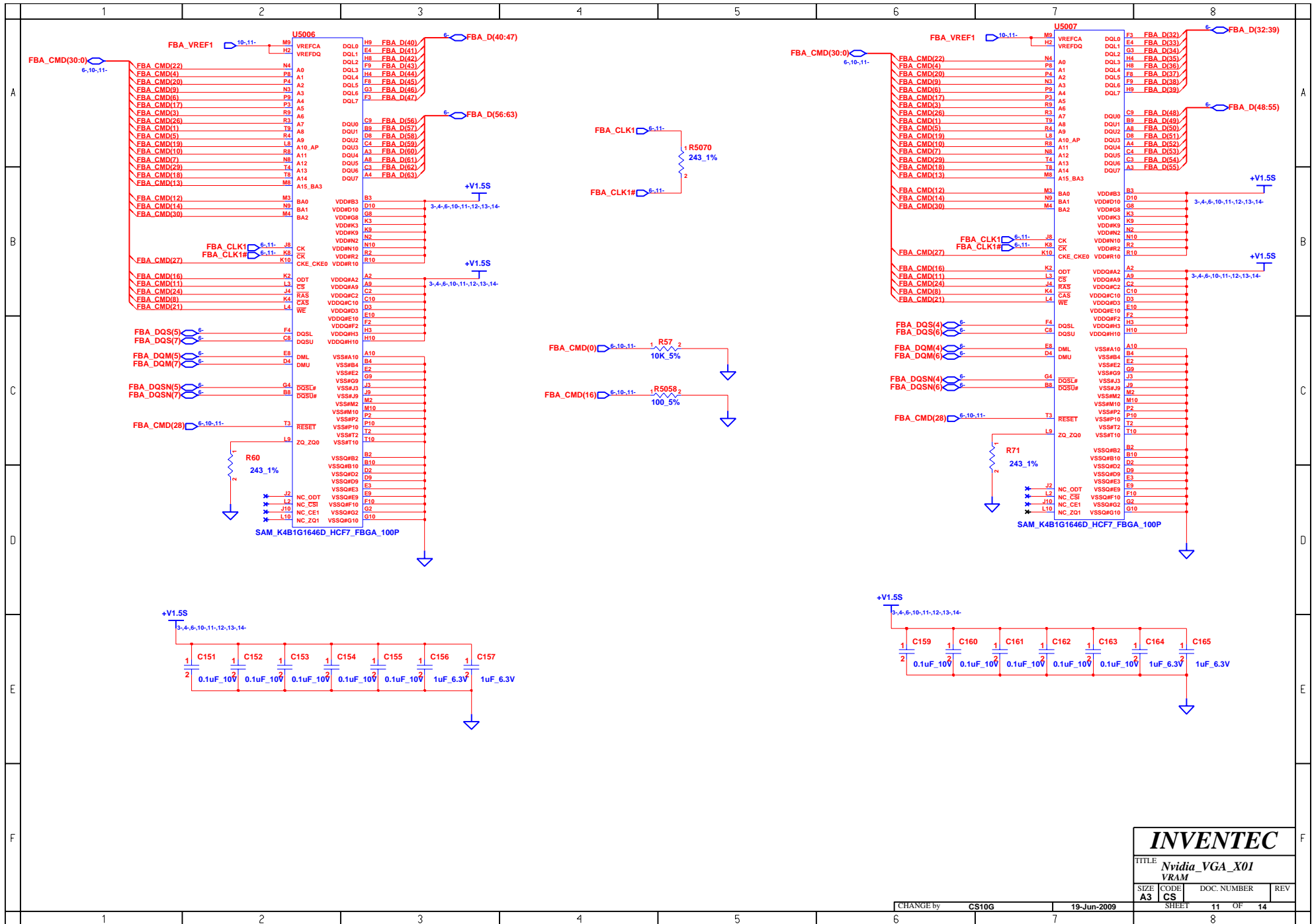












INVENTEC

TITLE: **NvidiA_VGA_X01**
VRAM

SIZE	CODE	DOC. NUMBER	REV
A3	CS		

CHANGE by: **CS10G** 19-Jun-2009

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