

2009.07.21

DRAWER		EE	DATE	POWER	DATE	<div style="text-align: center;"> <h1 style="margin: 0;">INVENTEC</h1> <p style="margin: 0;">TITLE CALPELLA</p> </div>
DESIGN						
CHECK						
RESPONSIBLE						
SIZE 3		VER :				SIZE A3 CODE CS DOC. NUMBER <i>Model No</i> REV <i>000</i>
FILE NAME : XXXX.XXXXXX-XX						
P/N 1.XXXXXXXX		SHEET 1 OF 75				

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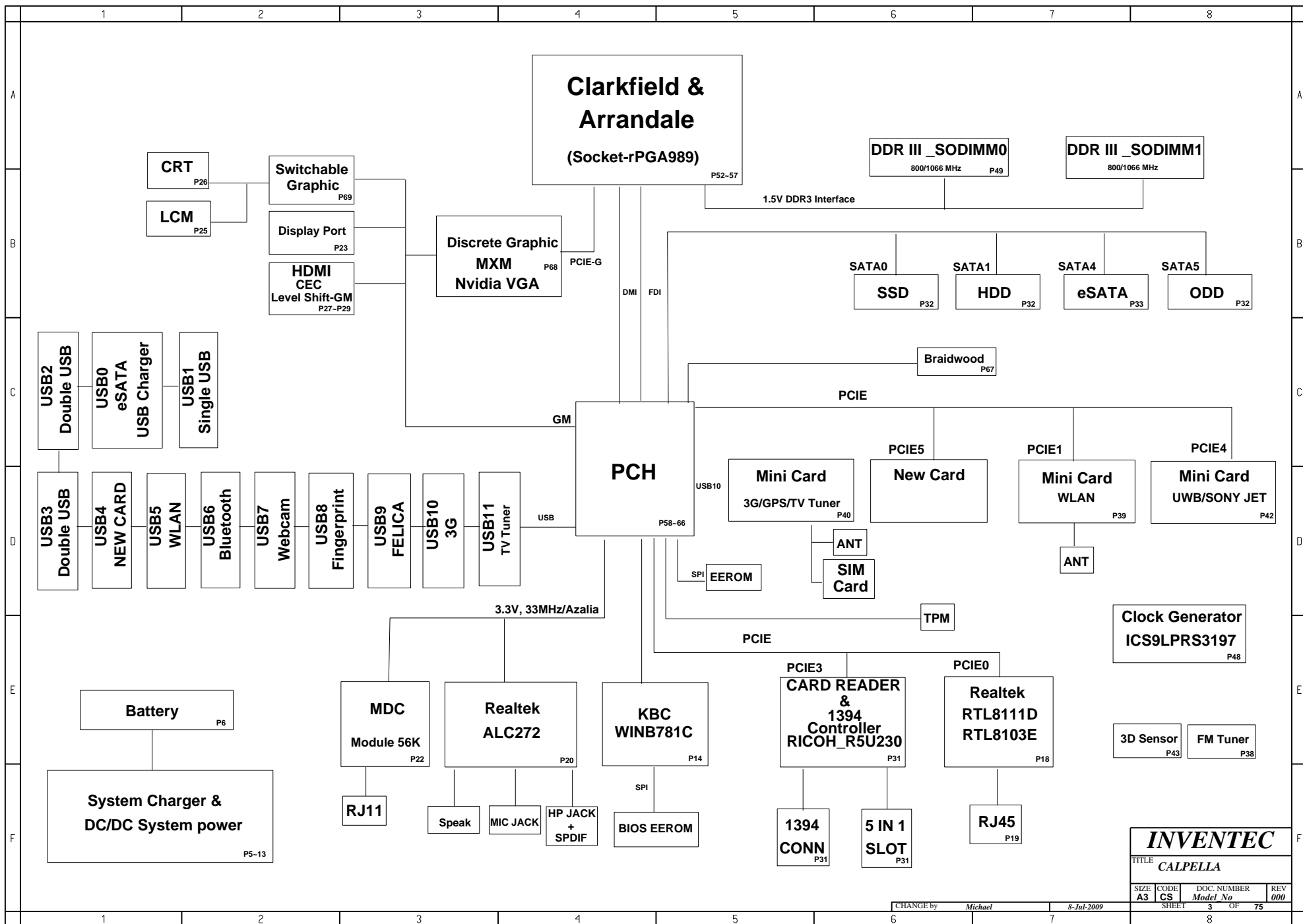
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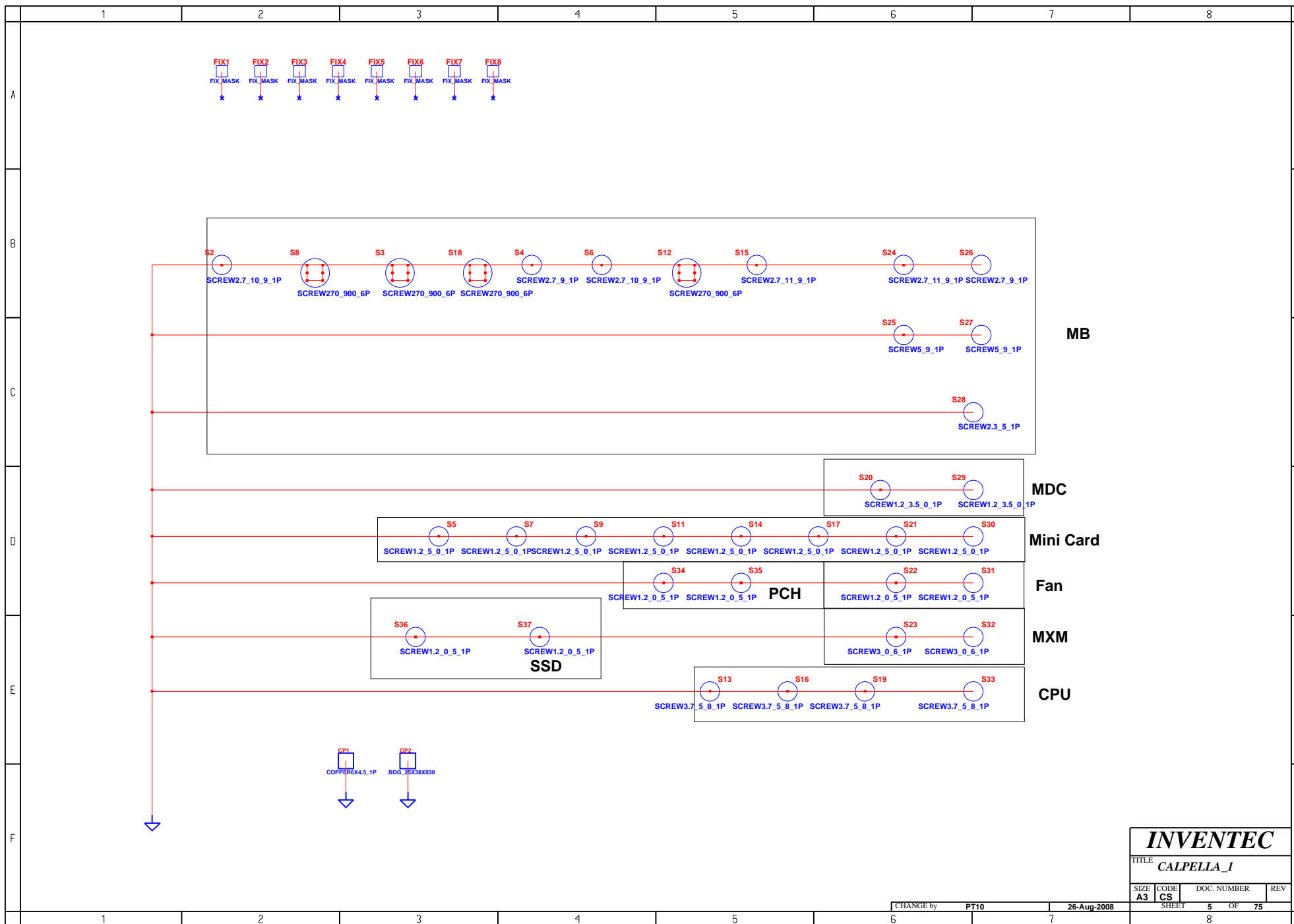
INVENTEC

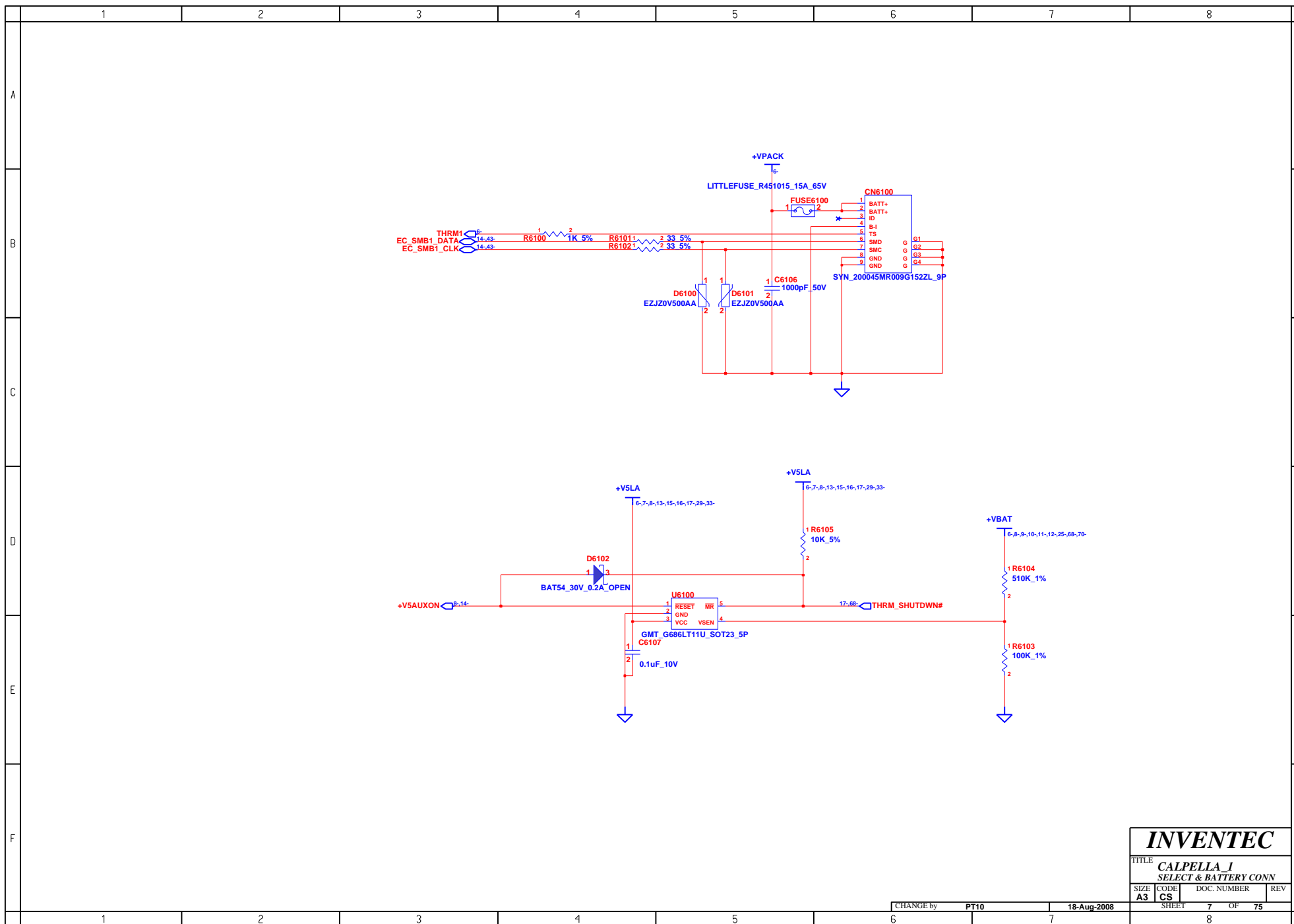
TITLE **CALPELLA_I**

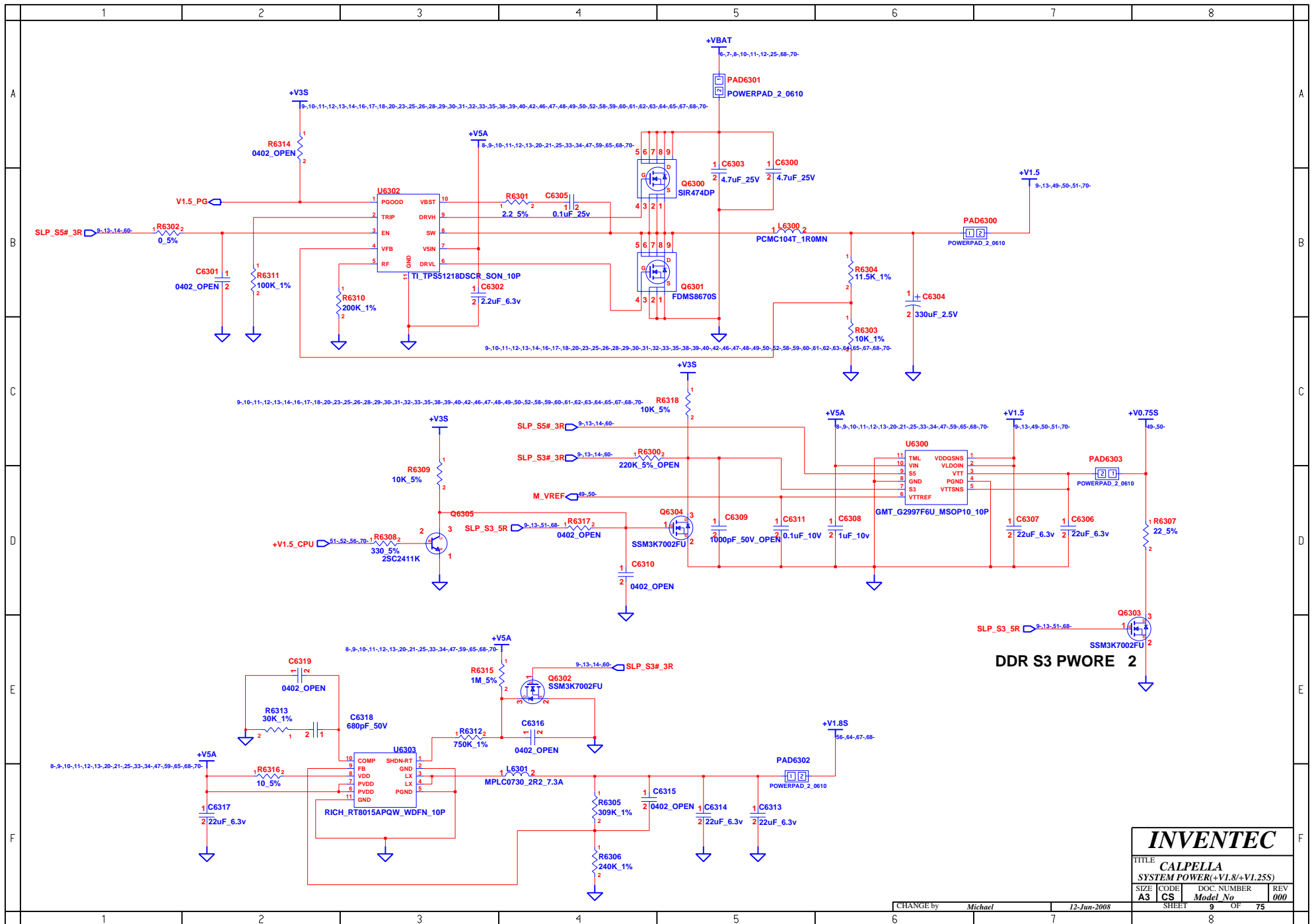
SIZE A3	CODE CS	DOC. NUMBER	REV
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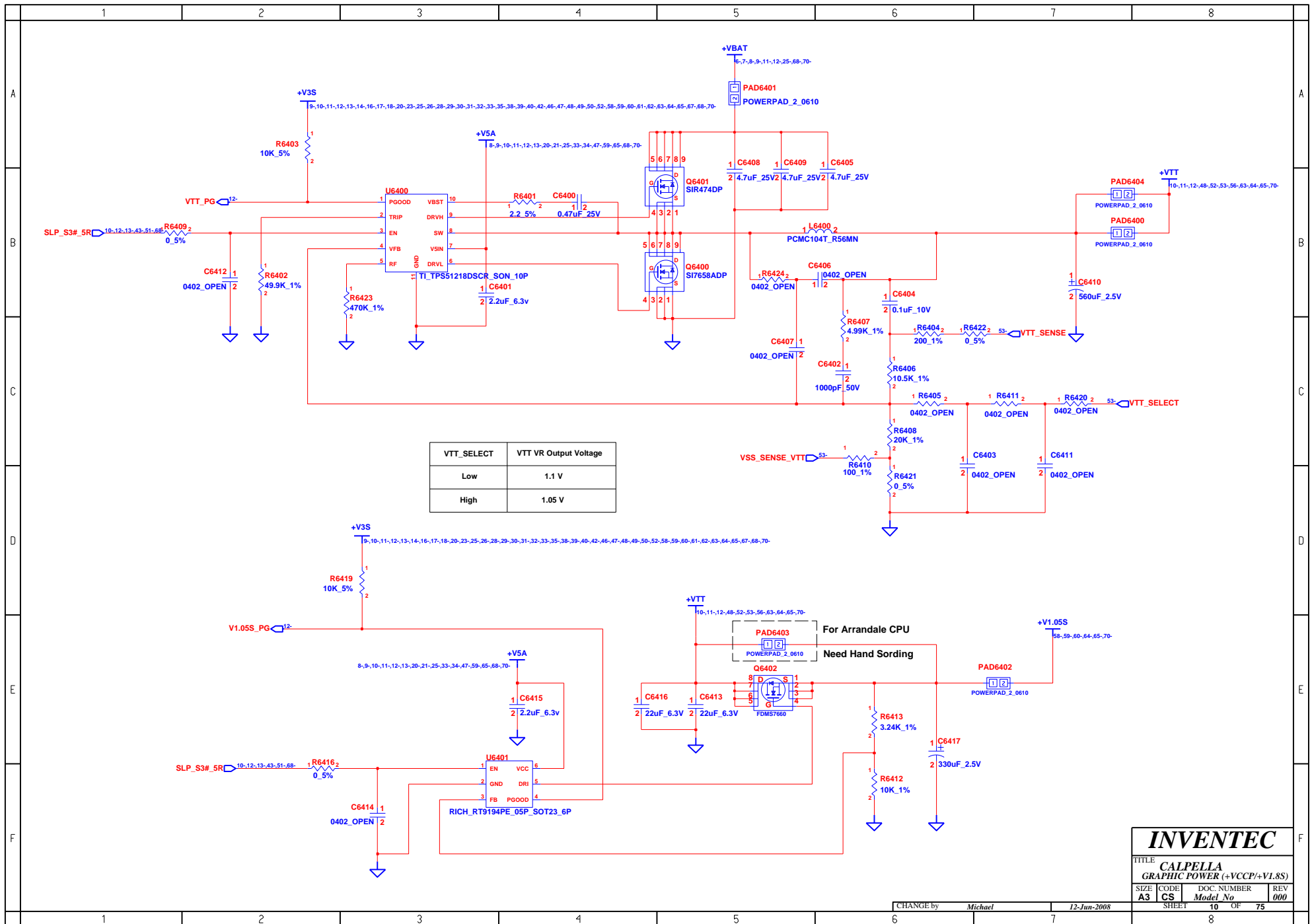
CHANGE by **PT10** 14-Jul-2009







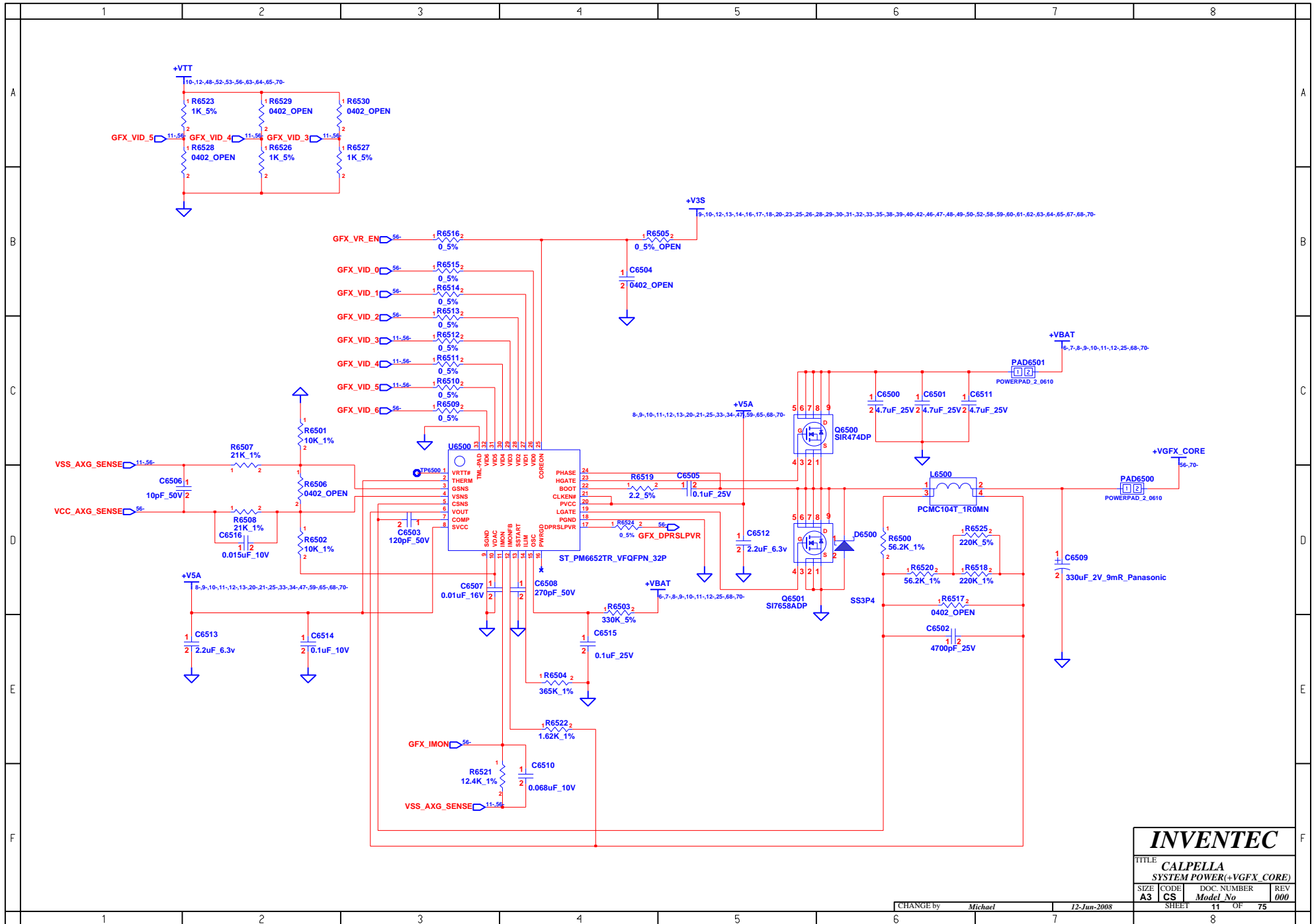


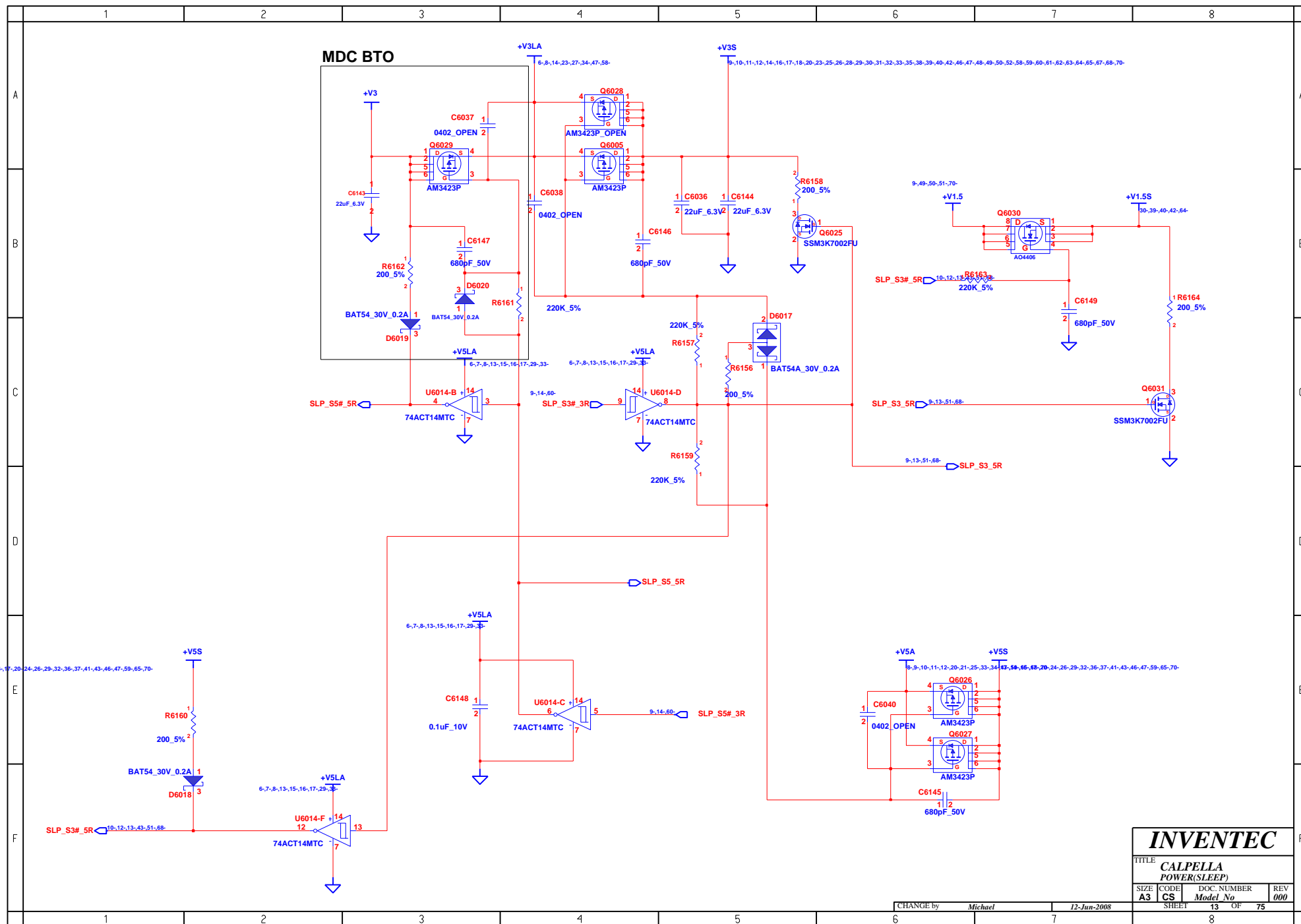


INVENTEC			
TITLE: CALPELLA GRAPHIC POWER (+VCCP/+V1.8S)			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	000

CHANGE by Michael 12-Jun-2008

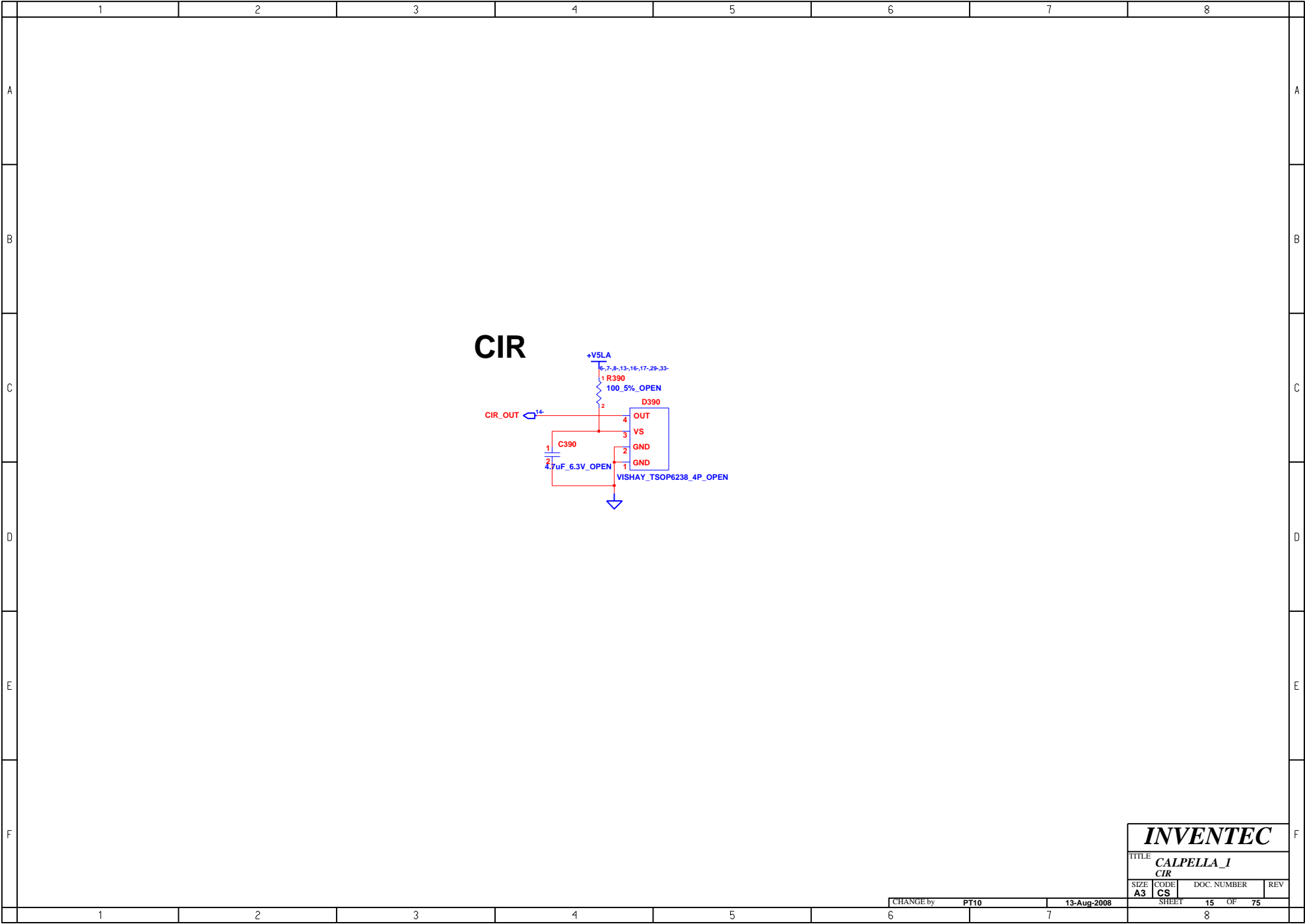
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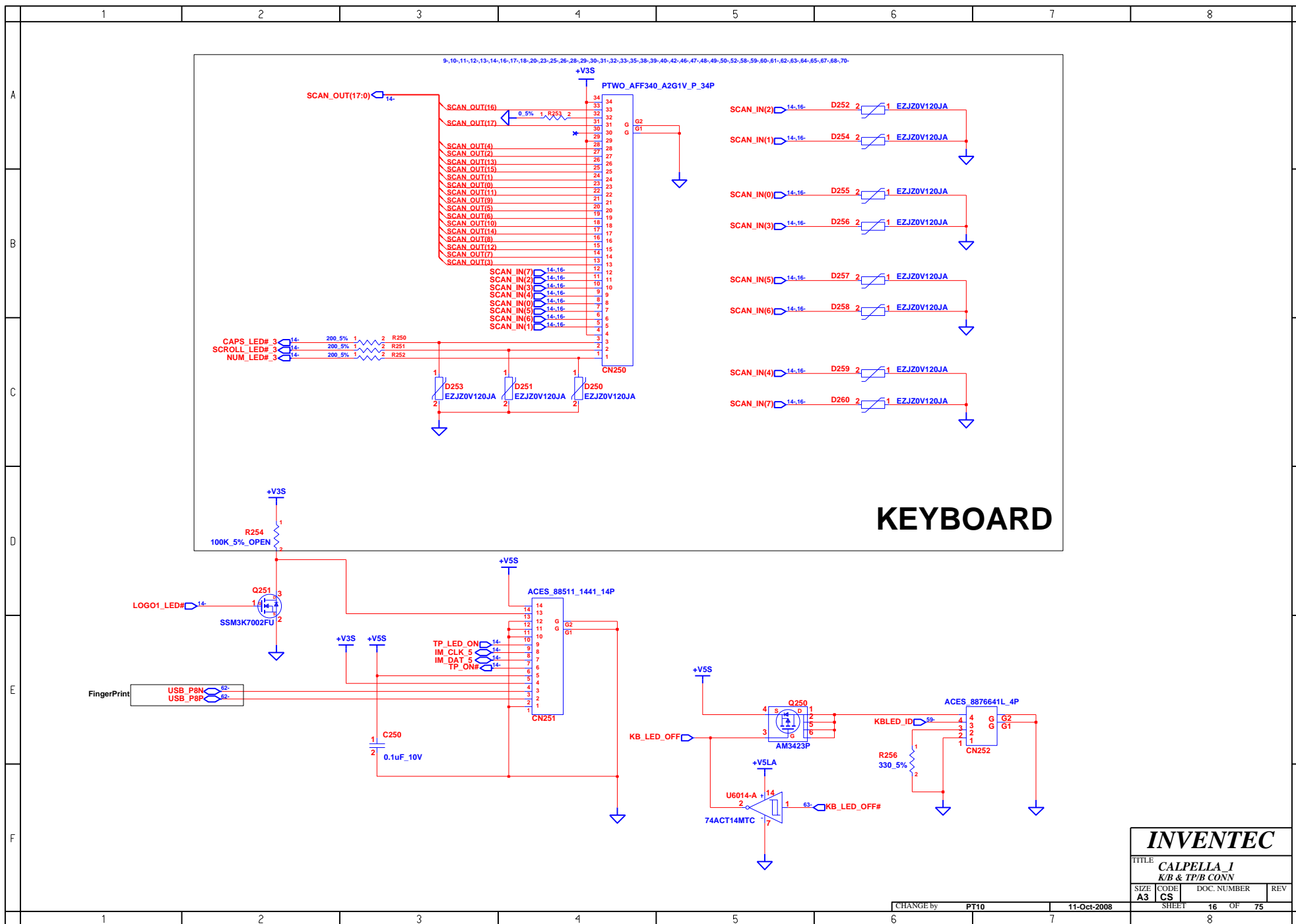


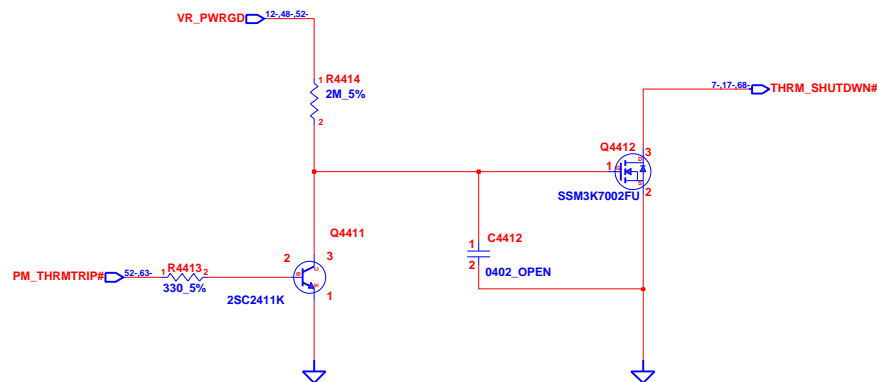
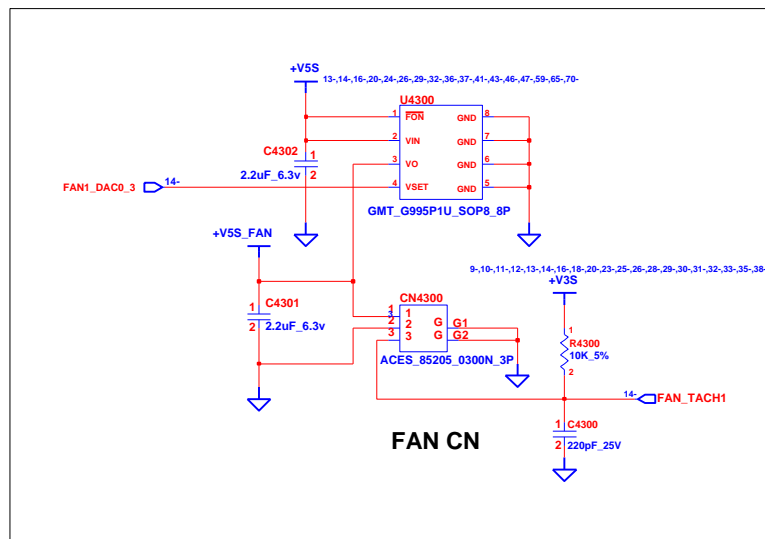
INVENTEC			
TITLE CALPELLA POWER(SLEEP)			
SIZE A3	CODE CS	DOC. NUMBER	REV 000
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CHANGE by Michael 12-Jun-2008



INVENTEC			
TITLE CALPELLA_I			
CIR			
SIZE A3	CODE CS	DOC. NUMBER	REV
CHANGE by PT10		13-Aug-2008	SHEET 15 OF 75





GM Thermal shutdown at 80.8°C +/-3°C from 60°C to 100°C
 PM Thermal shutdown at 86°C +/-3°C from 60°C to 100°C
 $RSET=0.0012 \cdot T^2 - 0.9308 \cdot T + 96.147$
 Hysteresis is 30C

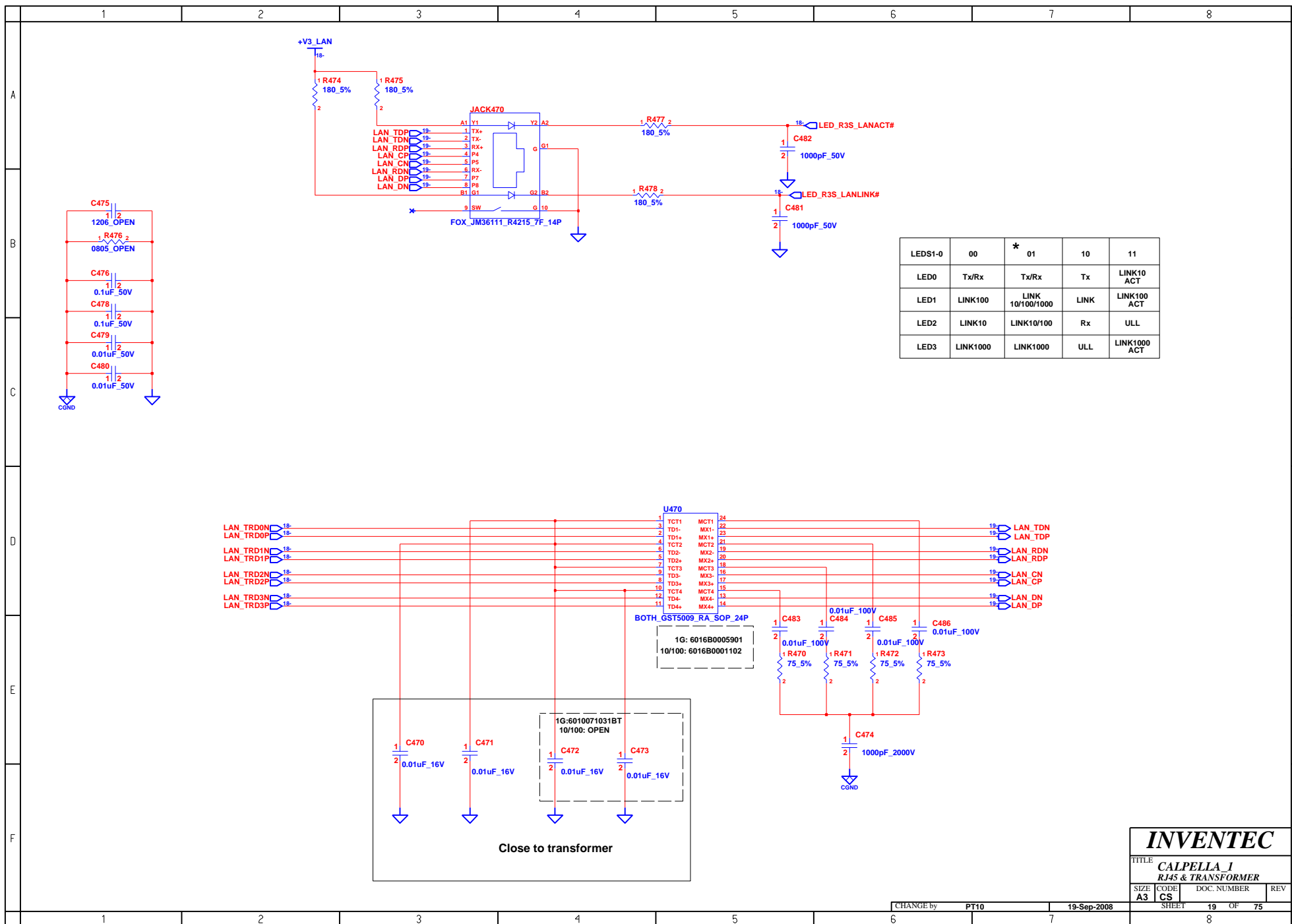
INVENTEC

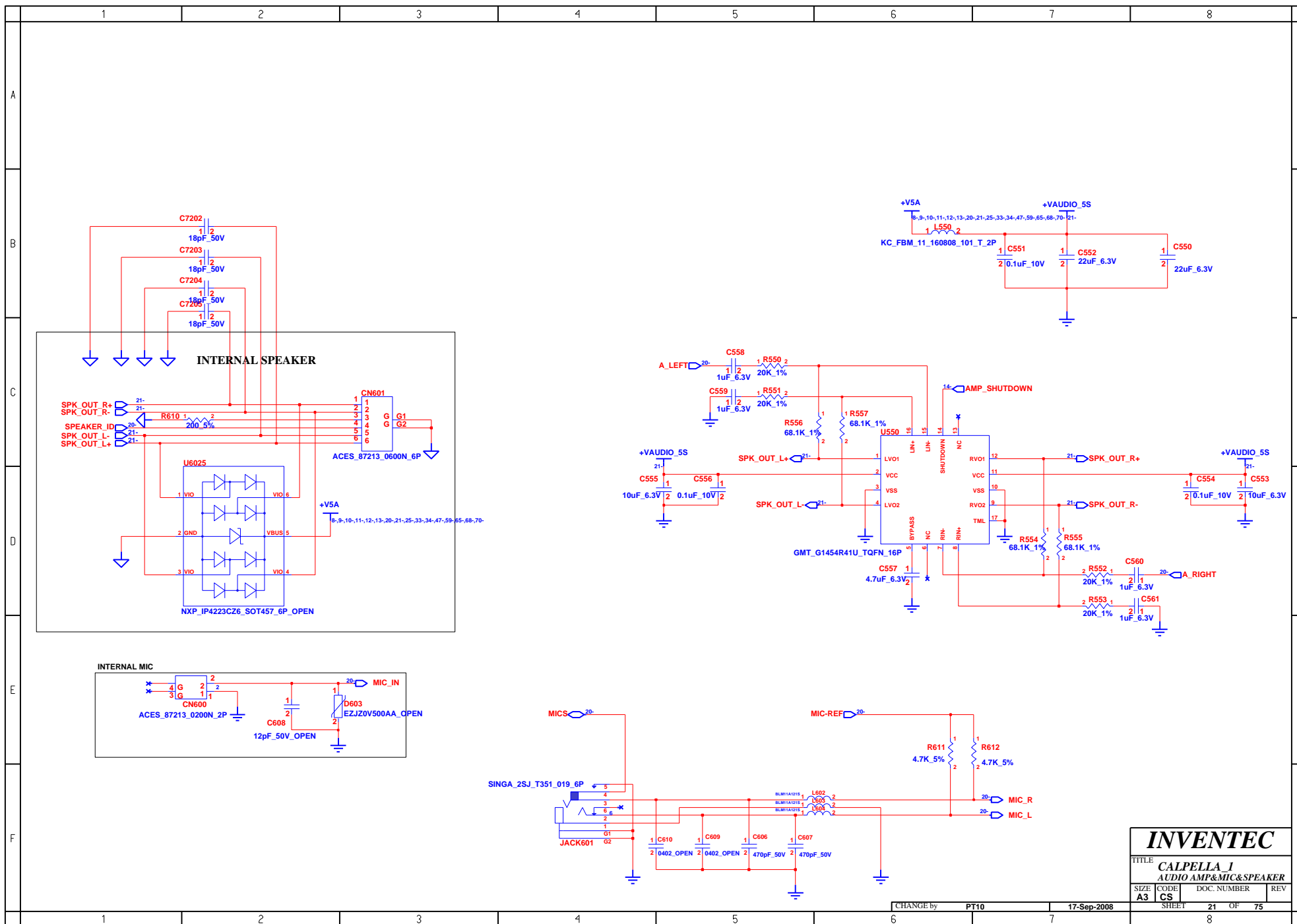
TITLE
CALPELLA_1
 FAN, Thermal controller

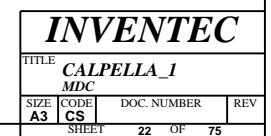
SIZE CODE DOC. NUMBER REV

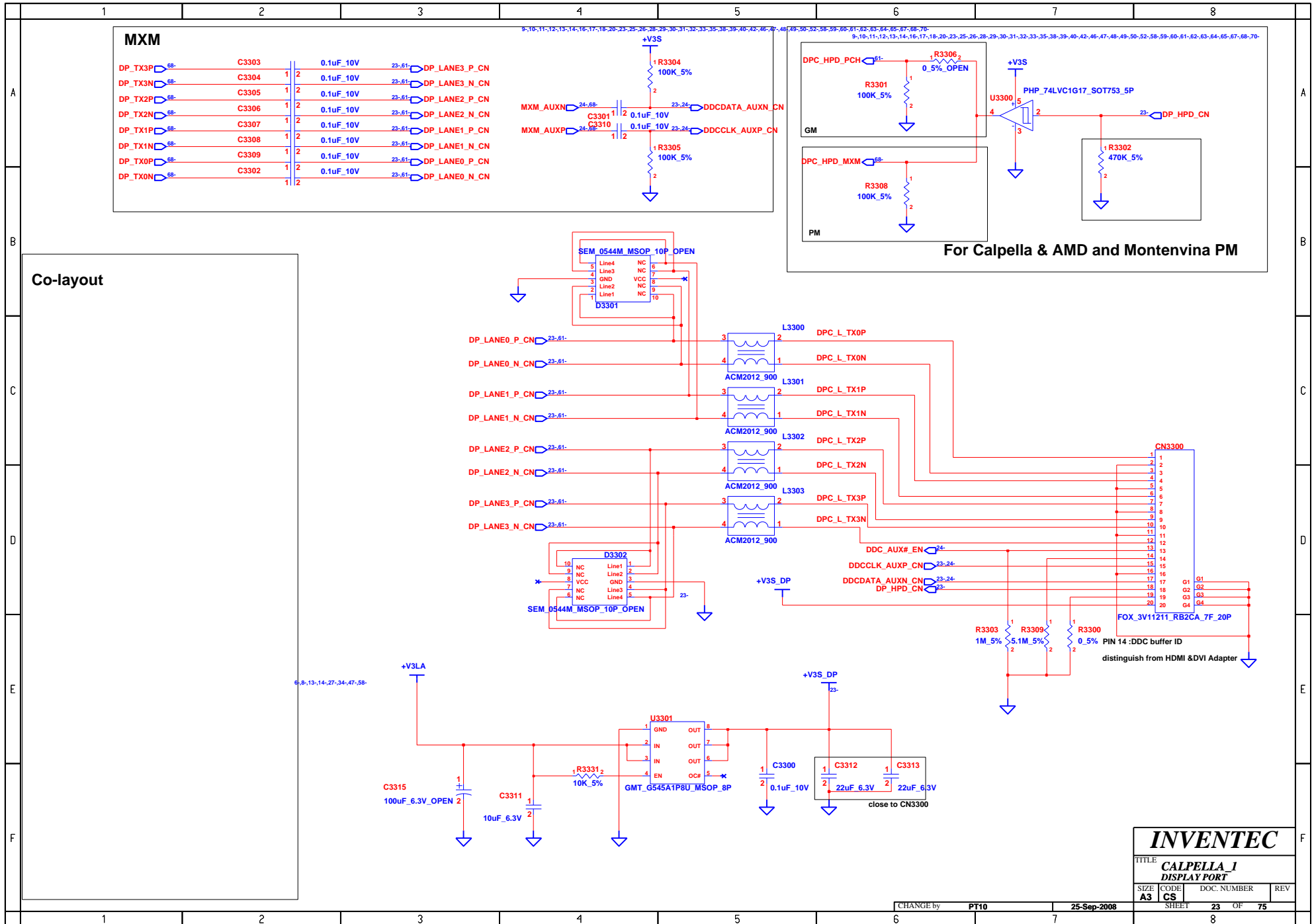
A3 CS 17 OF 75

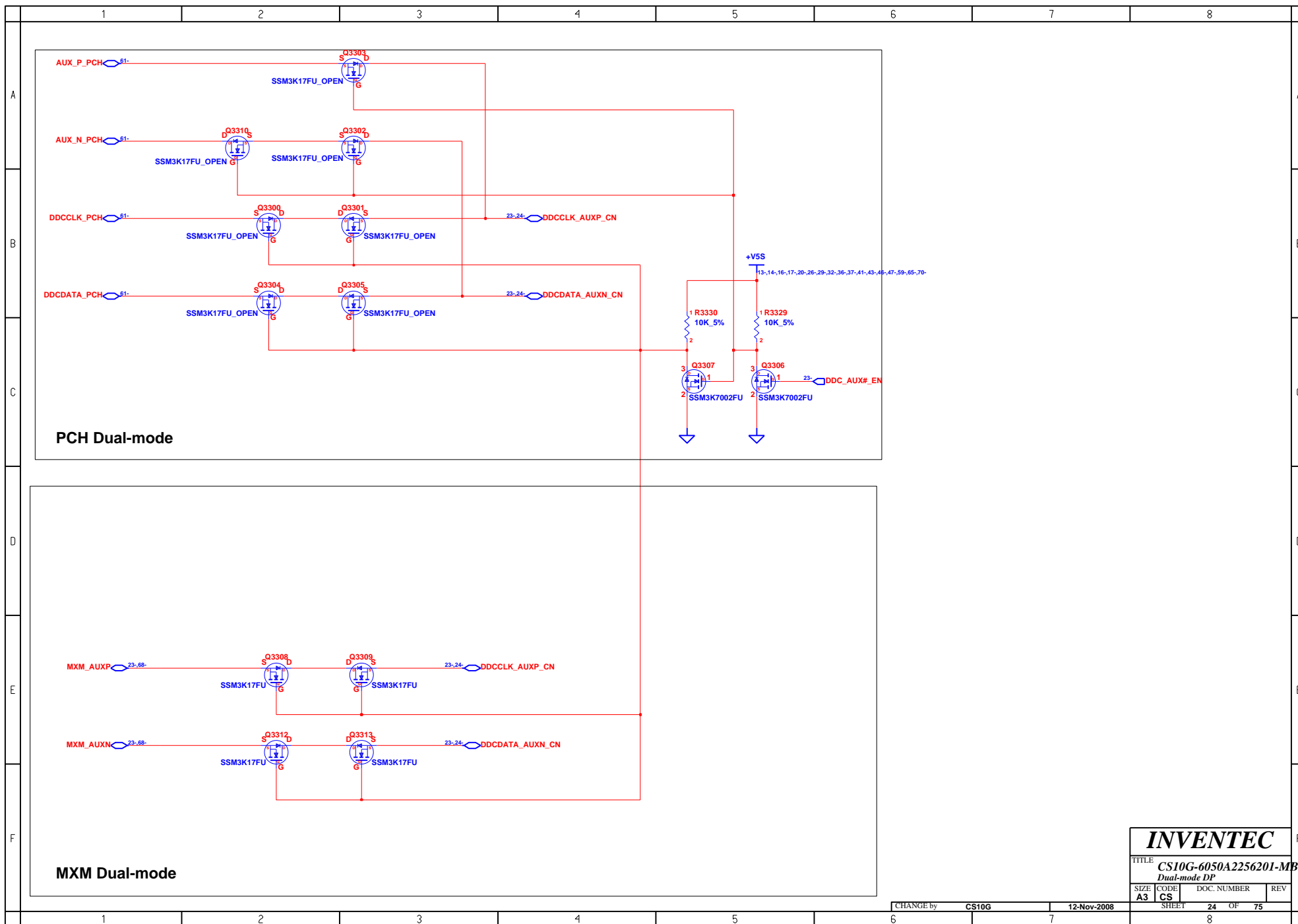
CHANGE by TESTBU2 25-Sep-2008

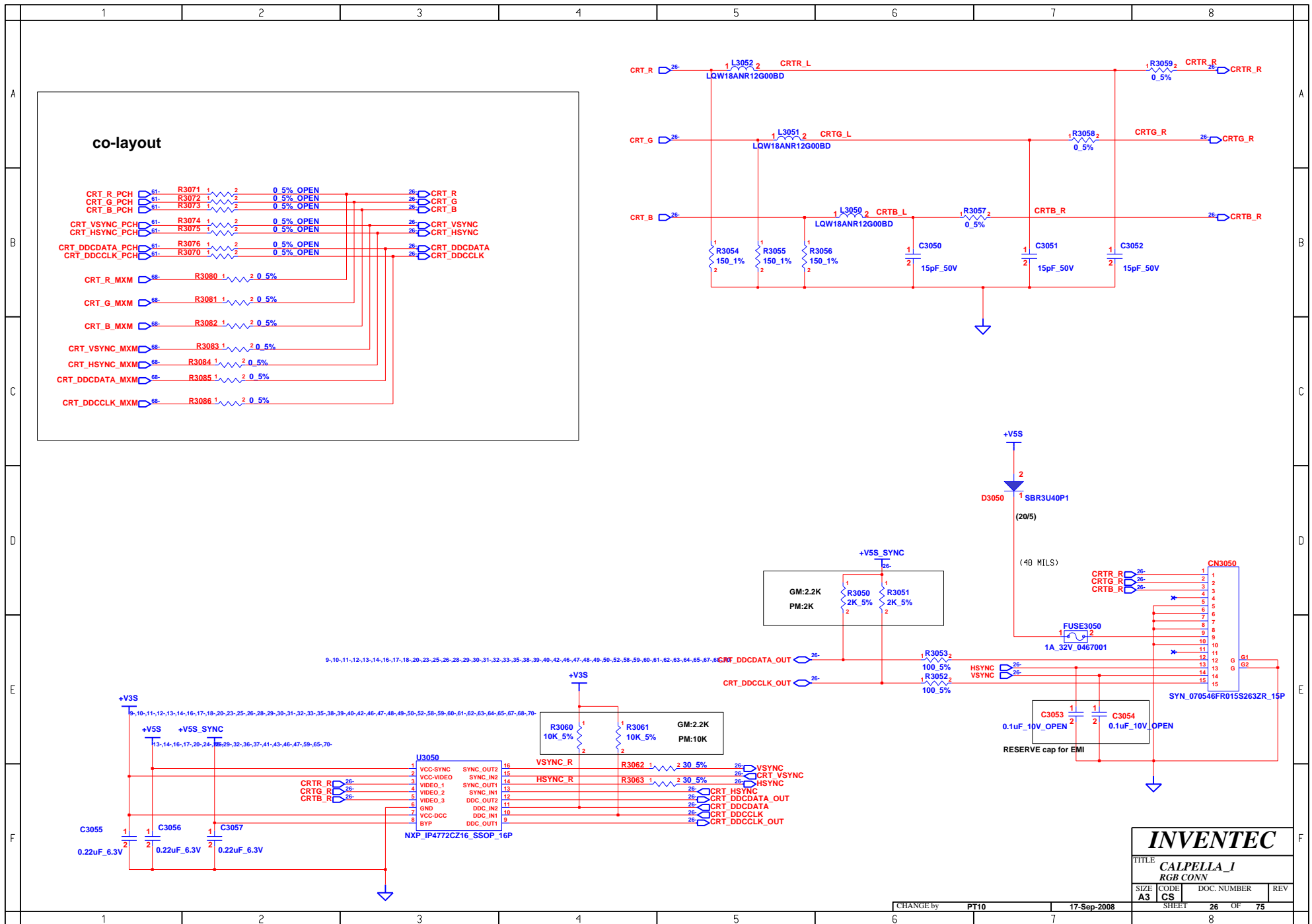


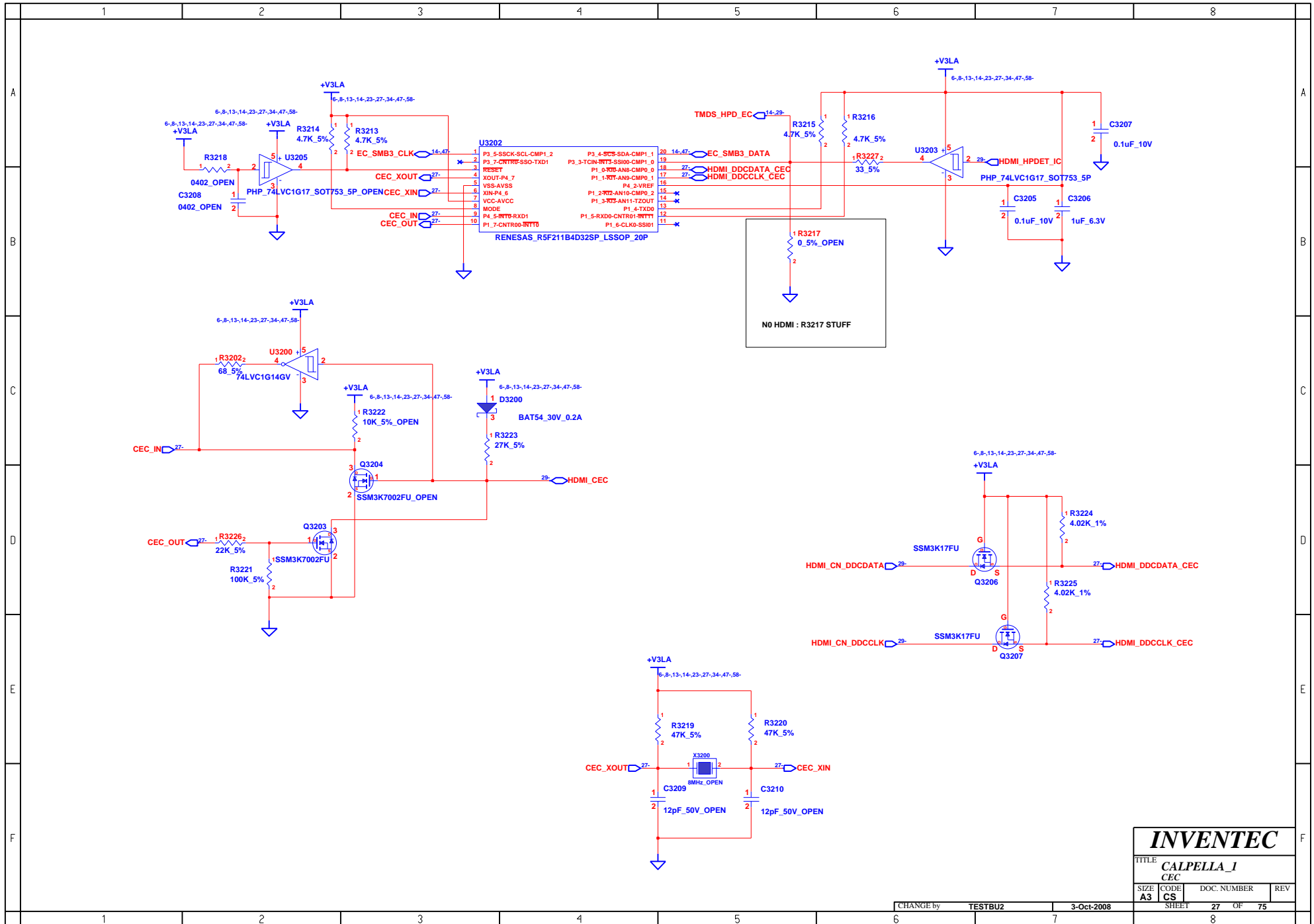




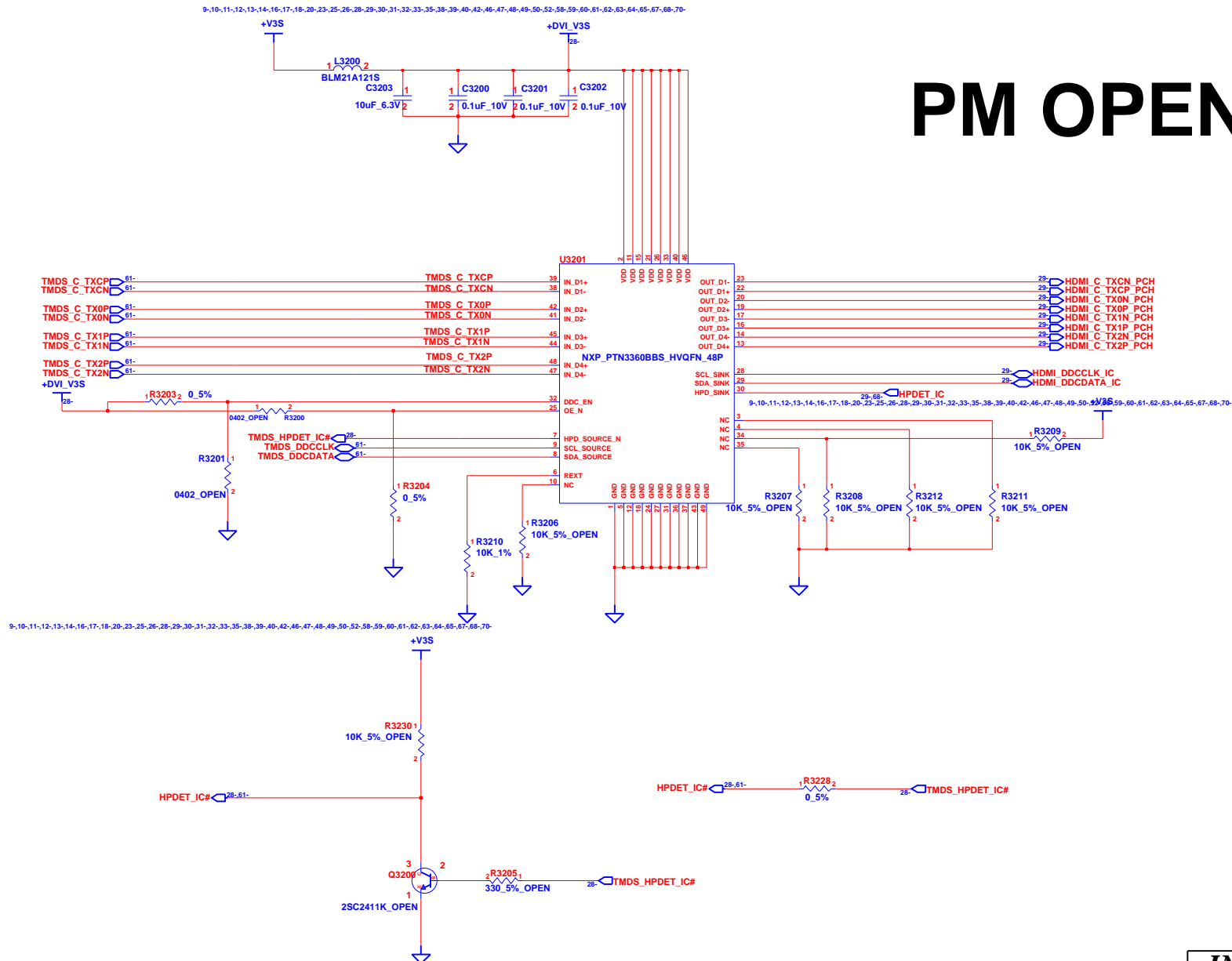






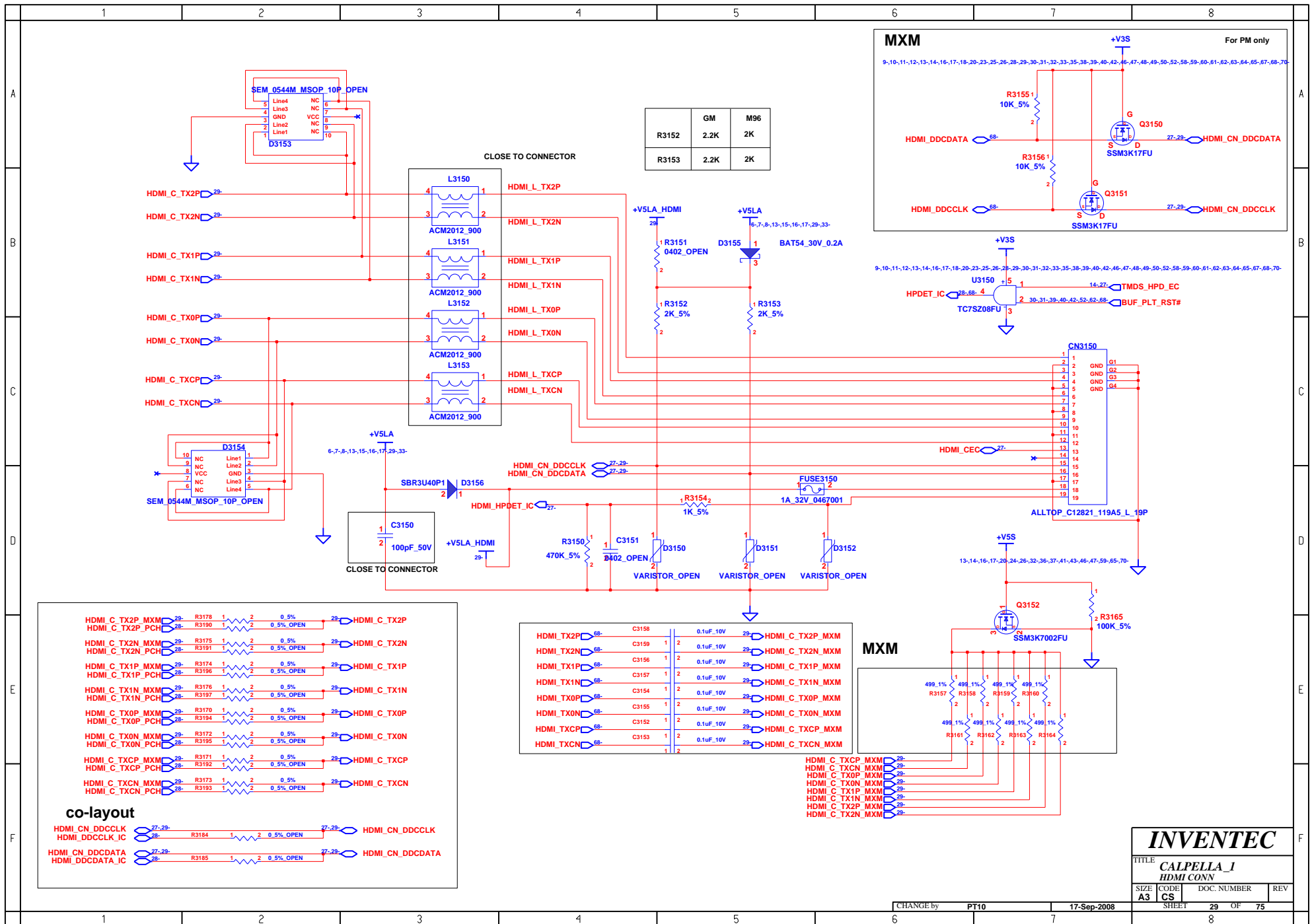


PM OPEN

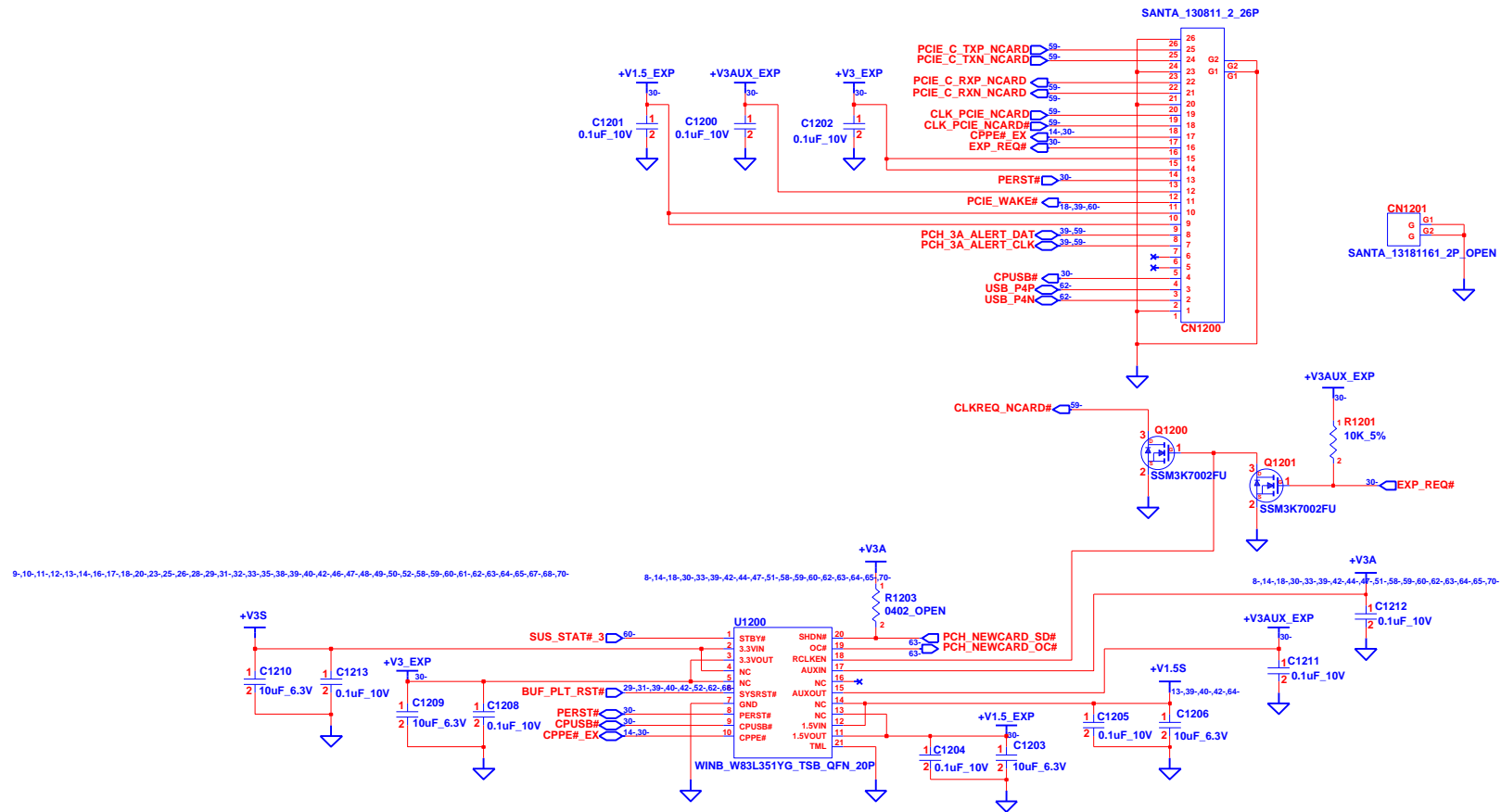


INVENTEC			
TITLE CALPELLA_1 HDMI Level Shifter			
SIZE A3	CODE CS	DOC. NUMBER	REV
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CHANGE by PT10 21-Oct-2008



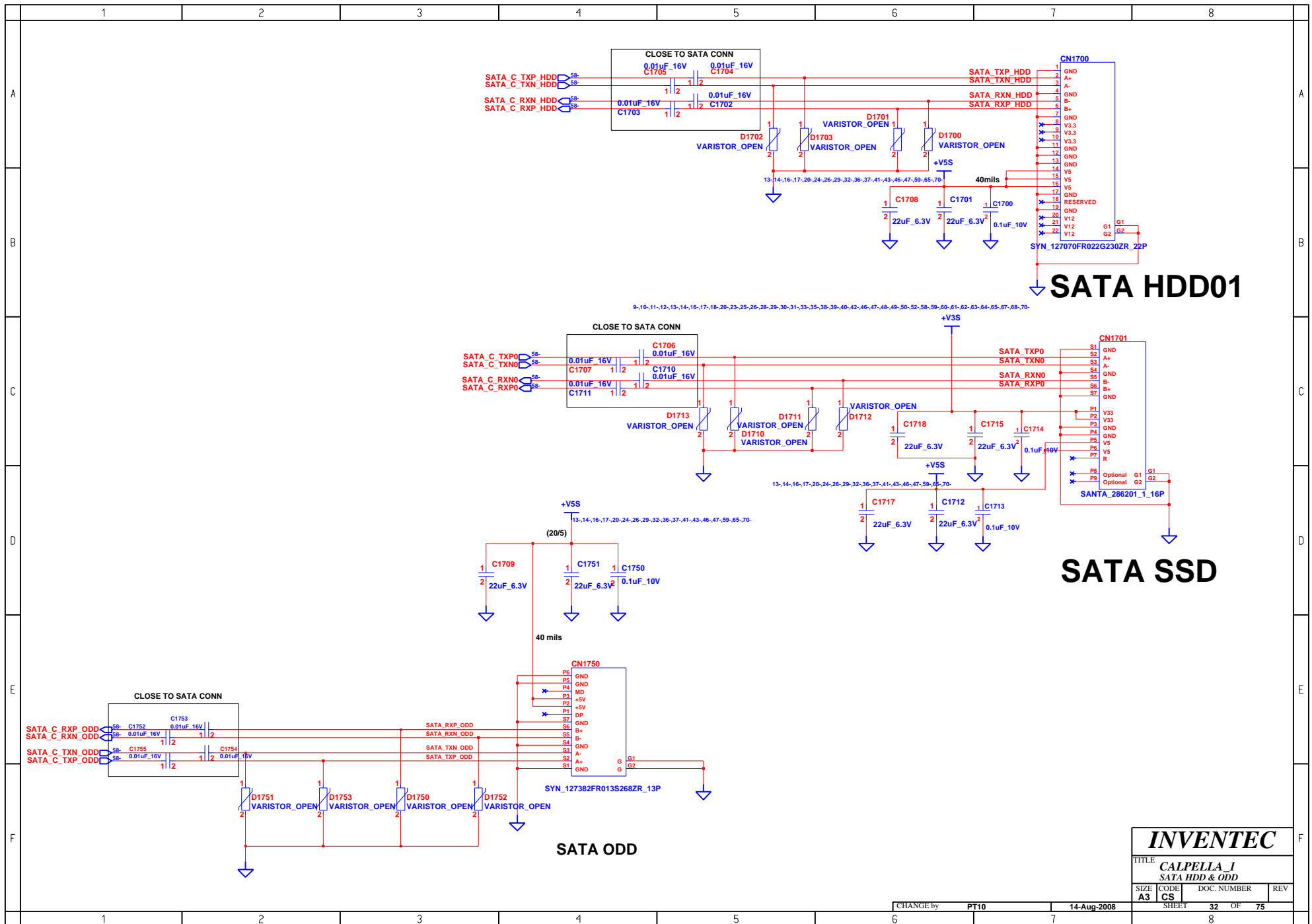
Express Card

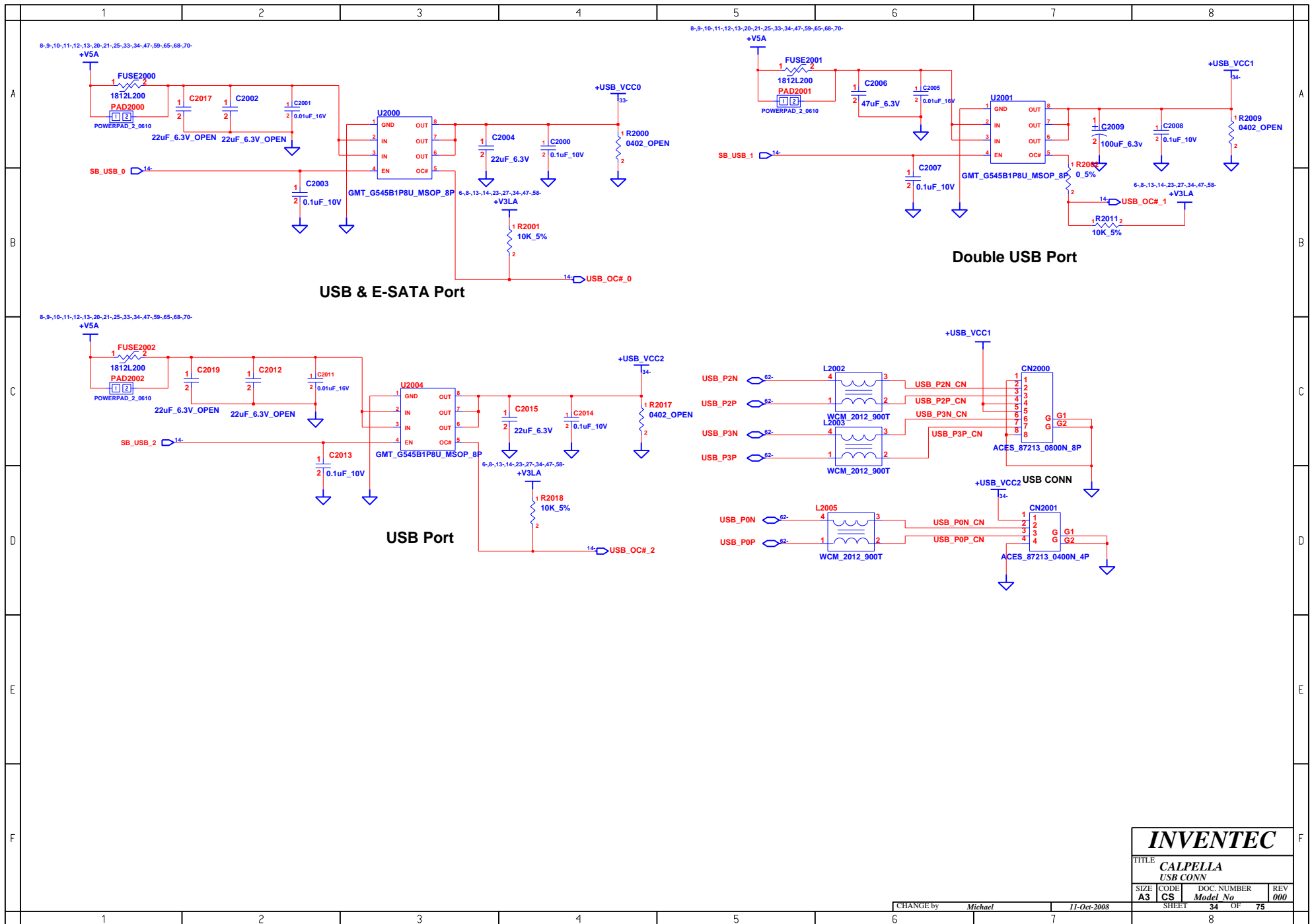


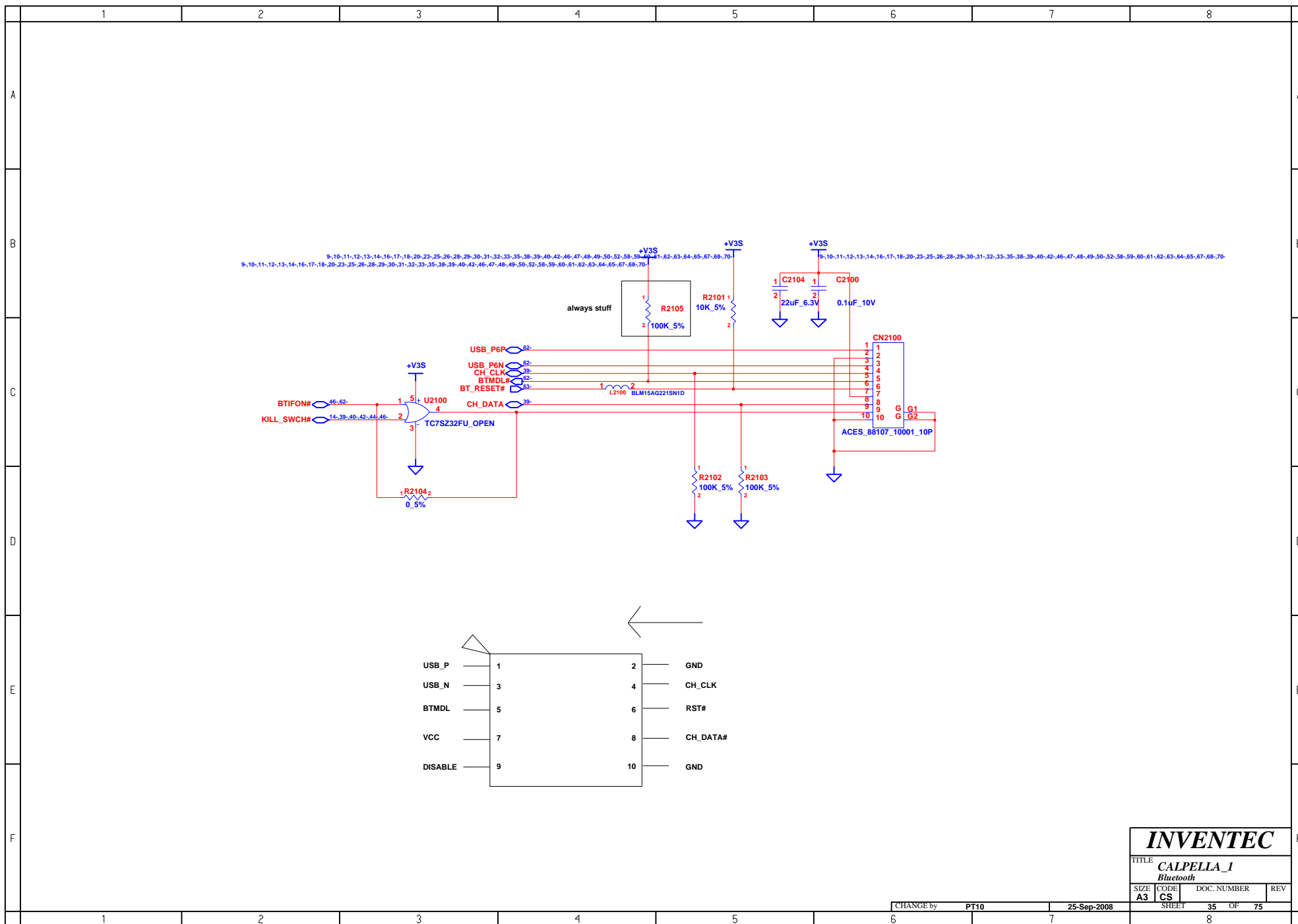
INVENTEC

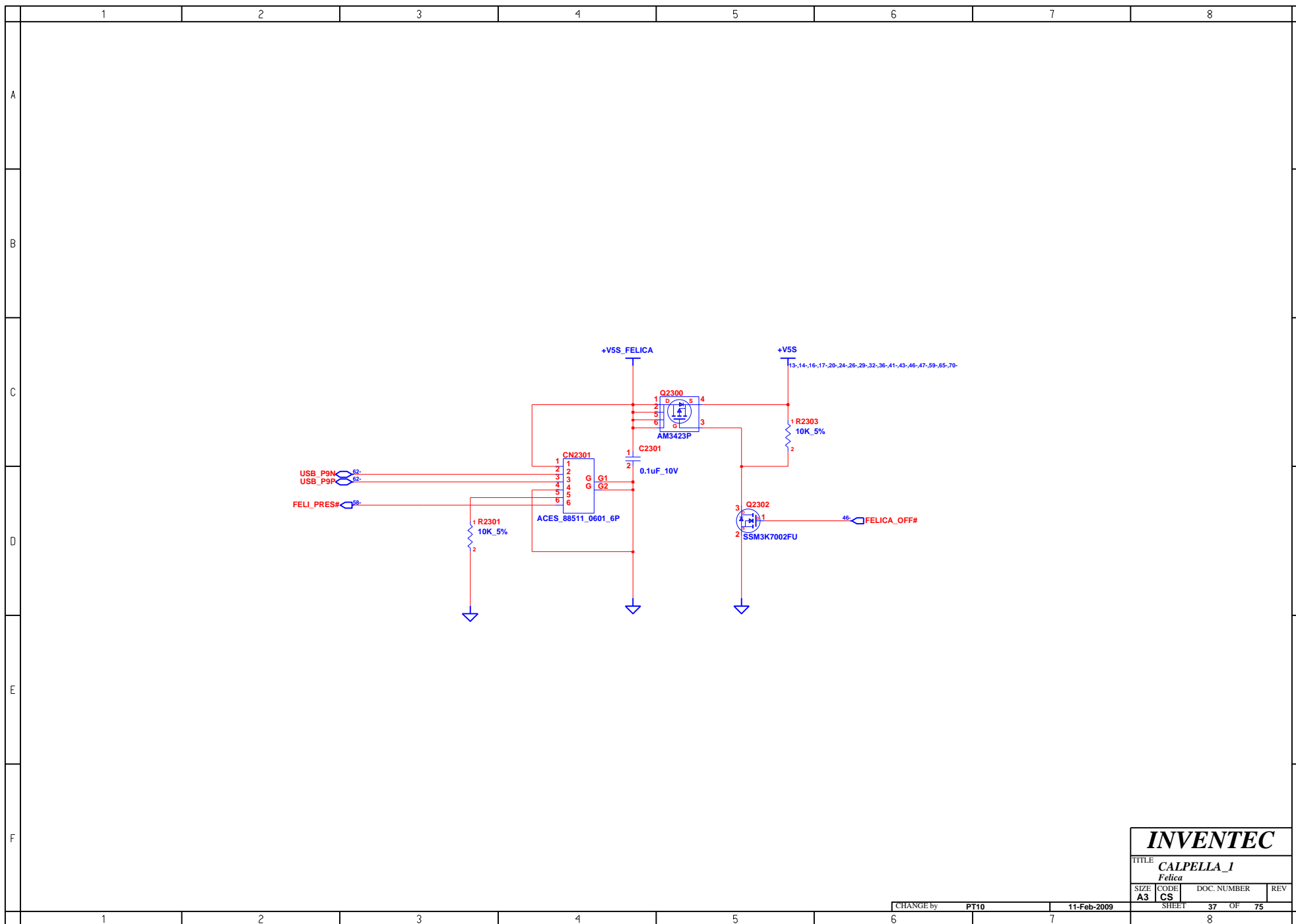
TITLE		
CALPELLA_1		
EXPRESS CARD		
SIZE	CODE	DOC. NUMBER
A3	CS	
REV		
SHEET 30 OF 75		

CHANGE by PT10 17-Sep-2008



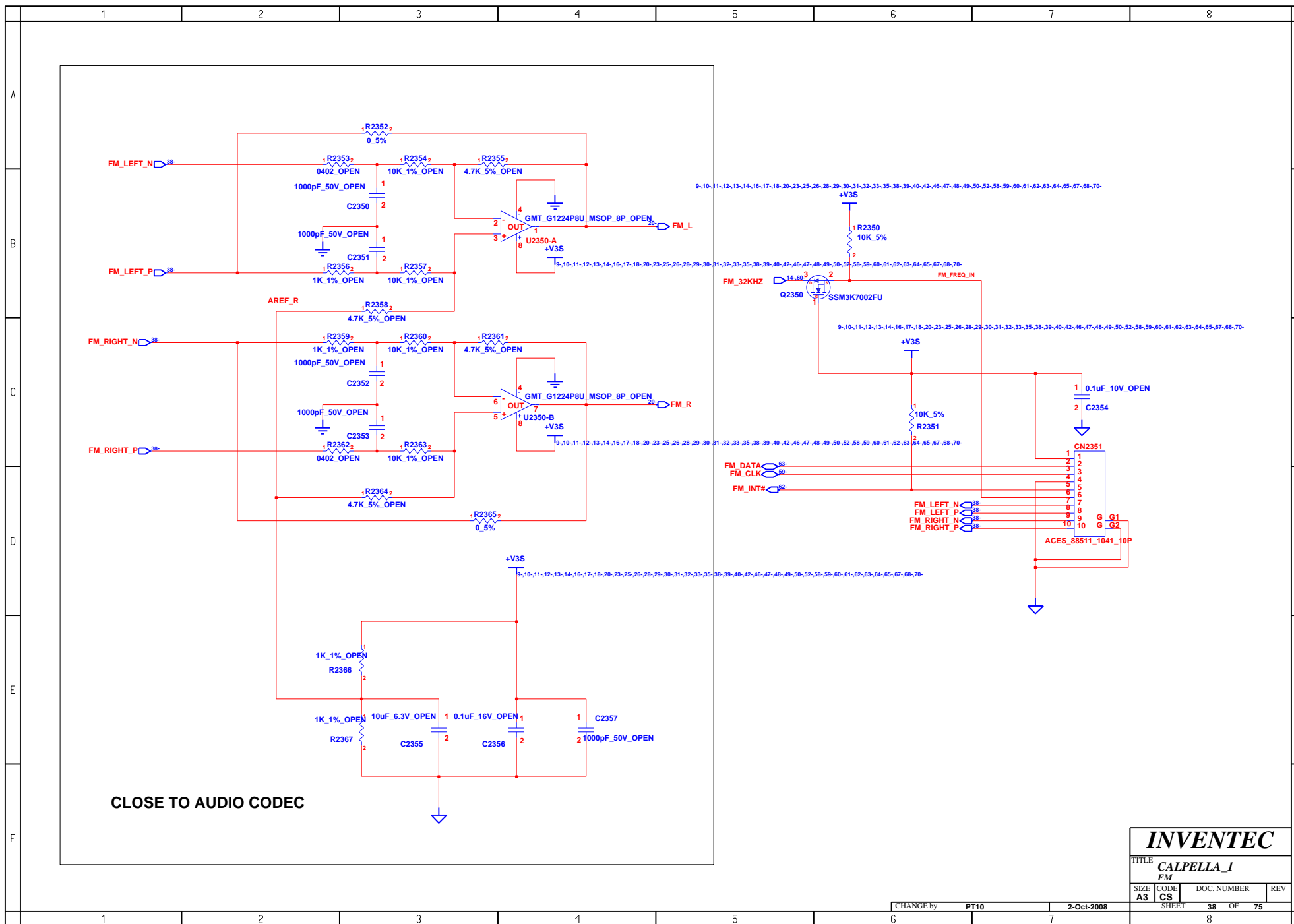






INVENTEC			
TITLE <i>CALPELLA_I</i>			
FELICA			
SIZE A3	CODE CS	DOC. NUMBER	REV
SHEET		37	OF 75
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CHANGE by **PT10** 11-Feb-2009



[illegible]

TITLE			
CALPELLA_1 WLAN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS		
SHEET 39 OF 75			

CHANGE by	PT10	16-Oct-2008
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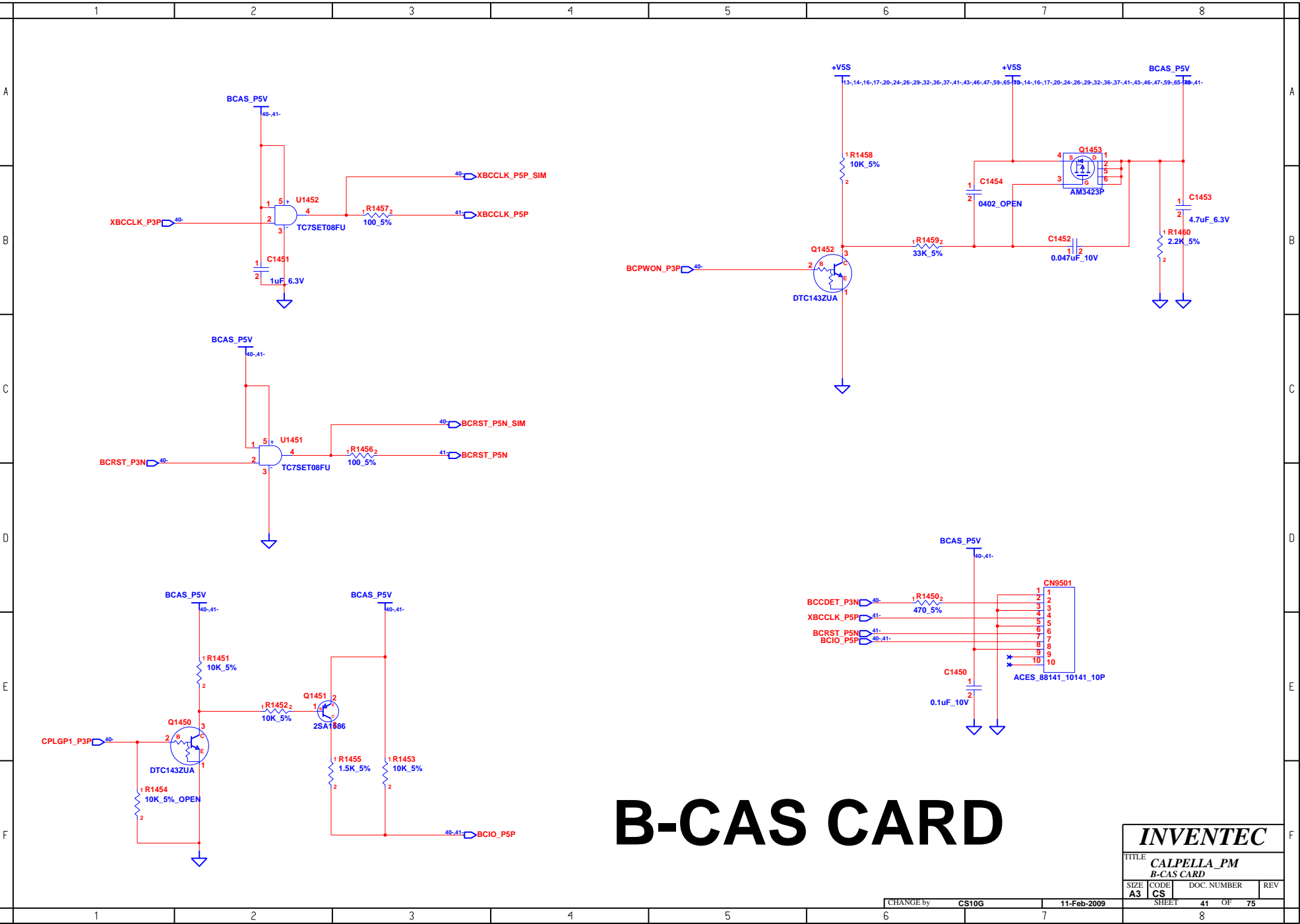
A

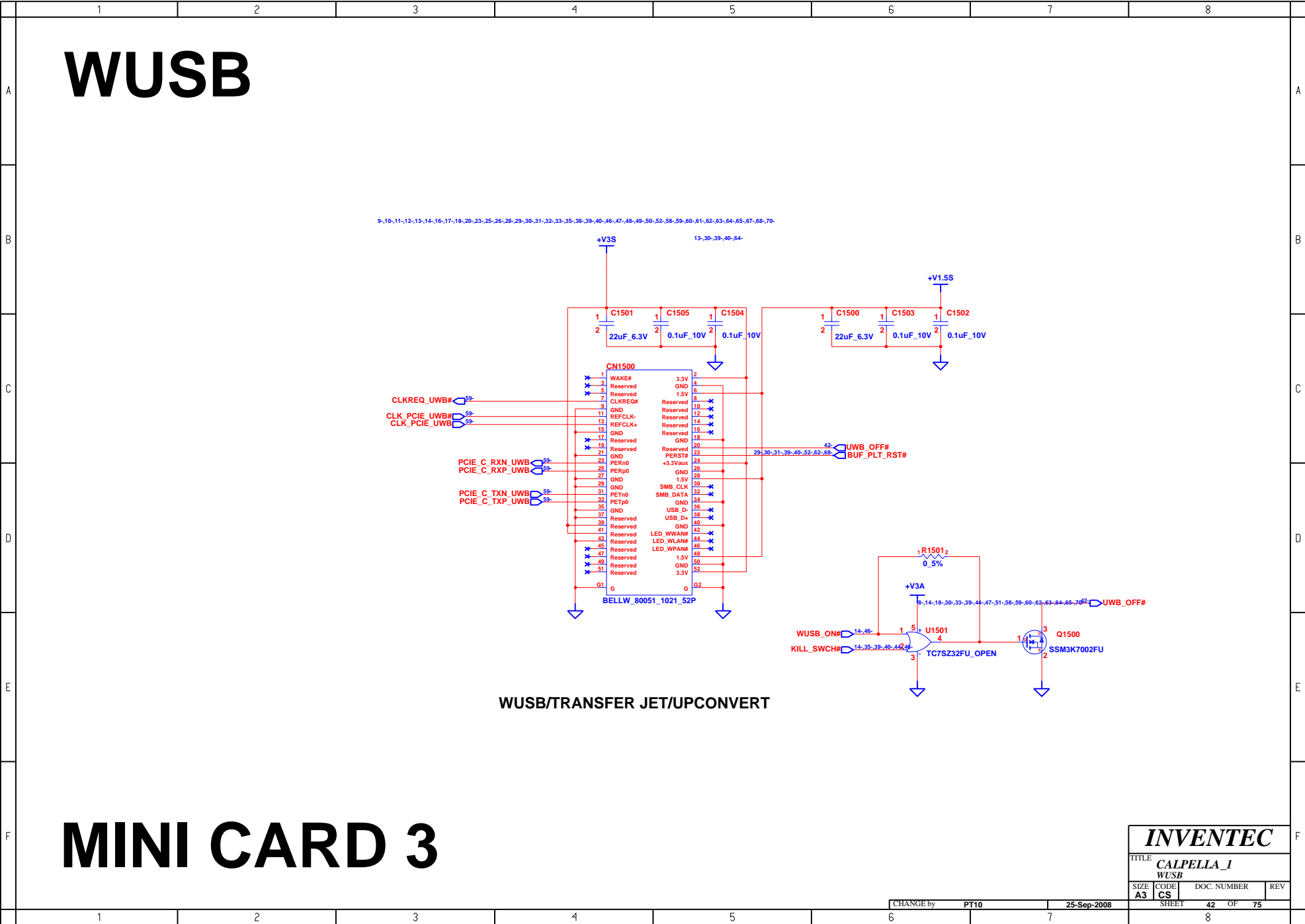


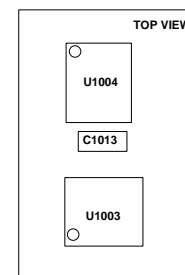
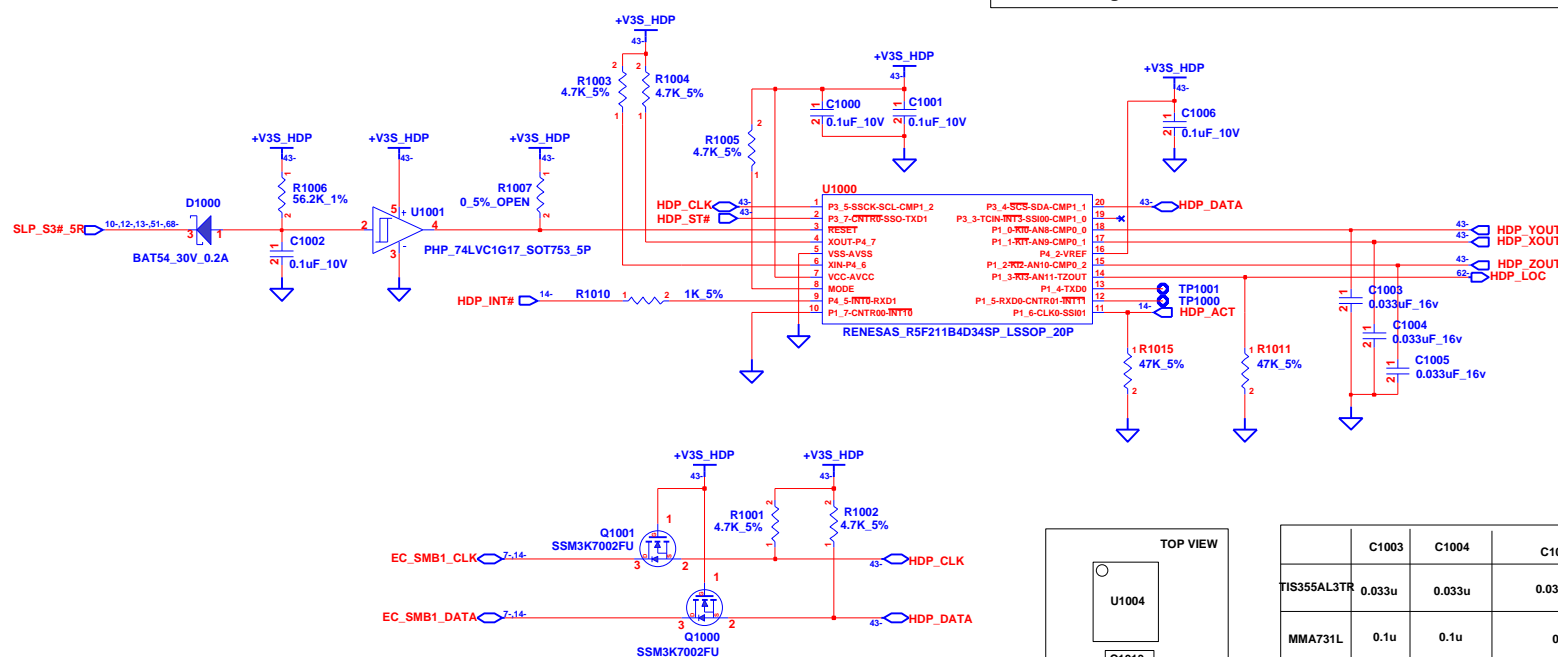
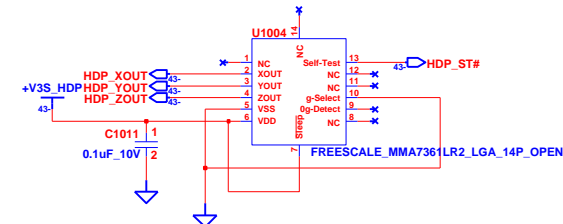
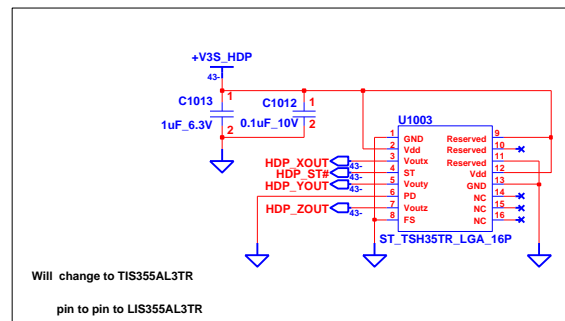
3



3



[illegible][illegible][illegible][illegible][illegible][illegible][illegible][illegible]



	C1003	C1004	C1005
TIS355AL3TR	0.033u	0.033u	0.033u
MMA731L	0.1u	0.1u	0.1u

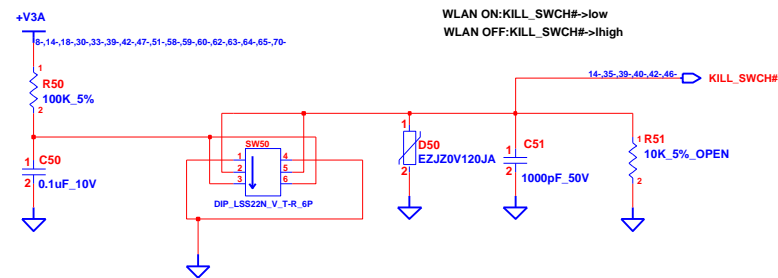
INVENTEC

TITLE	<i>CALPELLA_1</i> <i>3D sensor</i>
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SIZE A3	CODE CS	DOC. NUMBER	REV
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CHANGE by	TESTBU2	11-Oct-2008
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KILL SWITCH



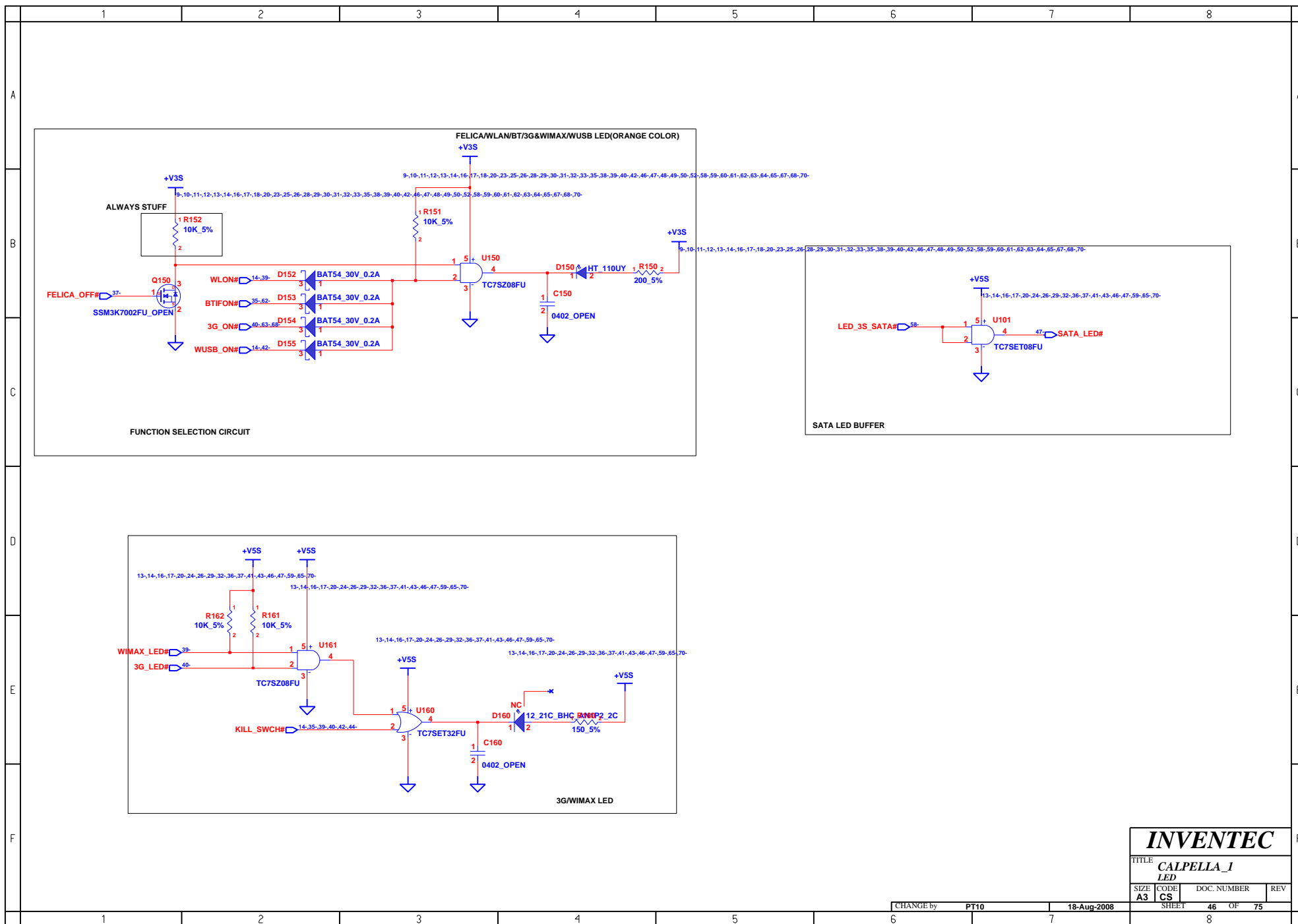
INVENTEC			
TITLE: CALPELLA_I			
KILL SW			
SIZE: A3	CODE: CS	DOC. NUMBER	REV
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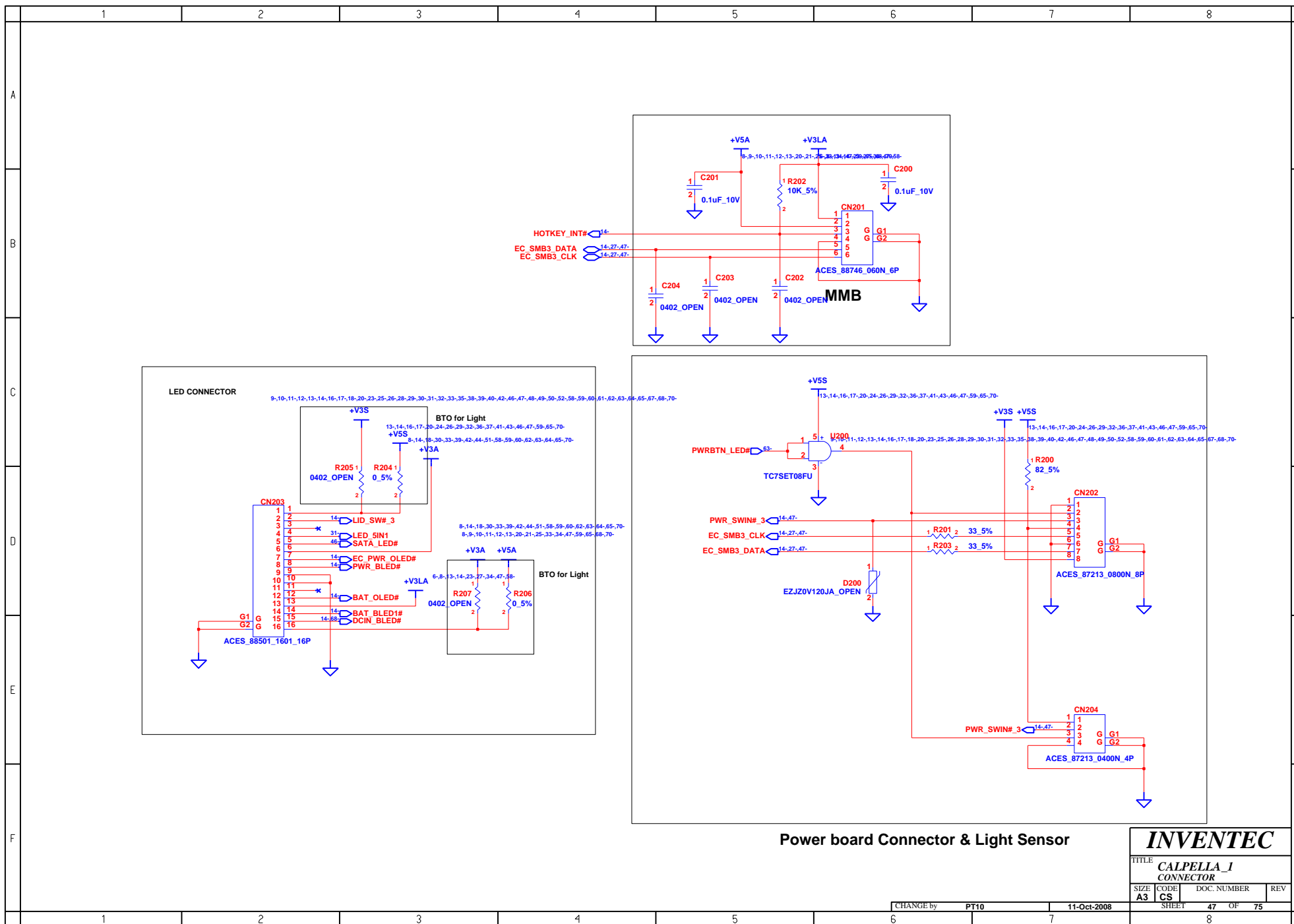
CHANGE by PT10 18-Aug-2008

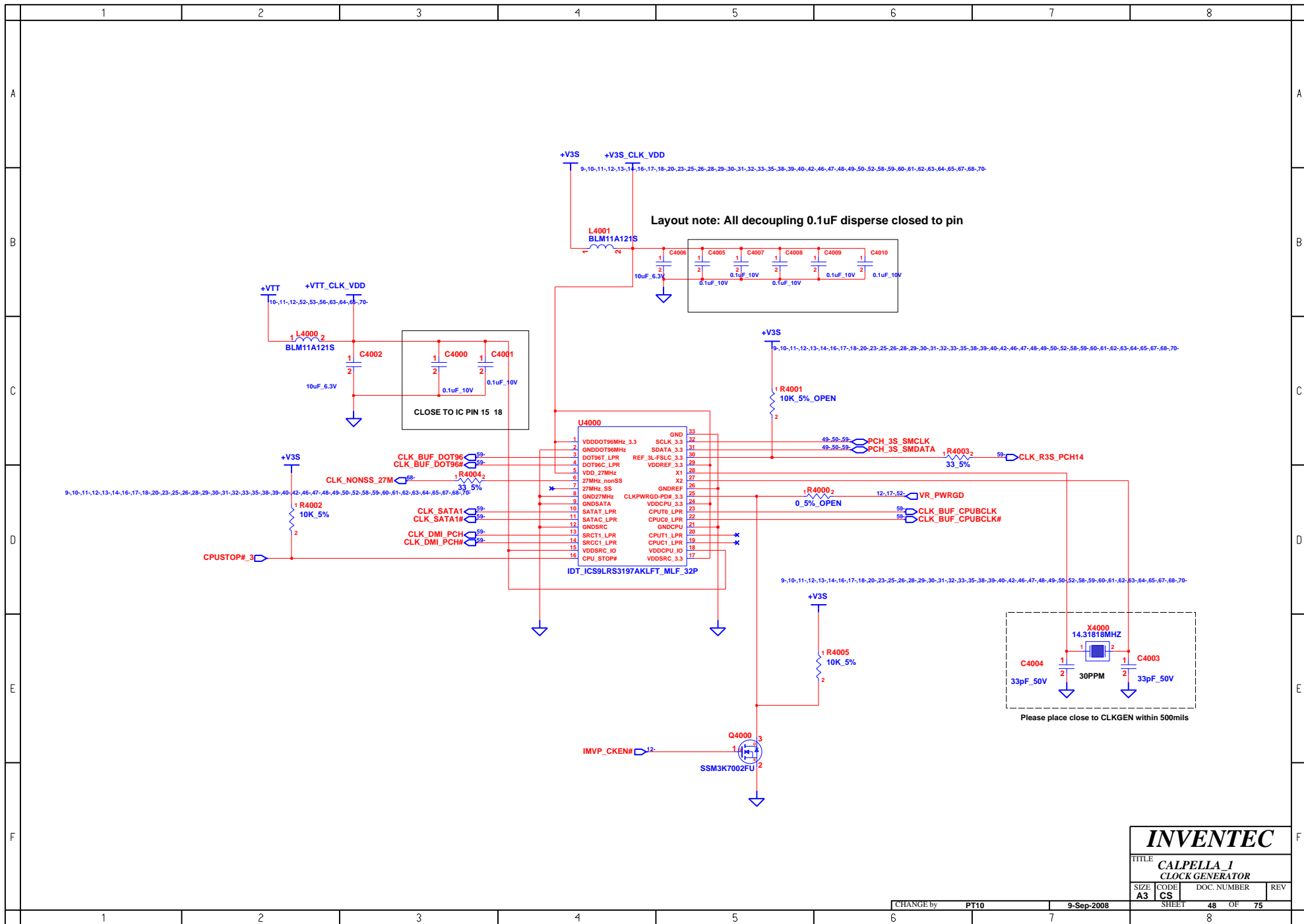
	1	2	3	4	5	6	7	8	
A	<div>BLANK</div>								A
B									B
C									C
D									D
E									E
F									F
	1	2	3	4	5	6	7	8	

INVENTEC			
TITLE CALPELLA_I			
LID SW			
SIZE	CODE	DOC. NUMBER	REV
A3	CS		
SHEET		45	OF 75

CHANGE by PT10 11-Feb-2009







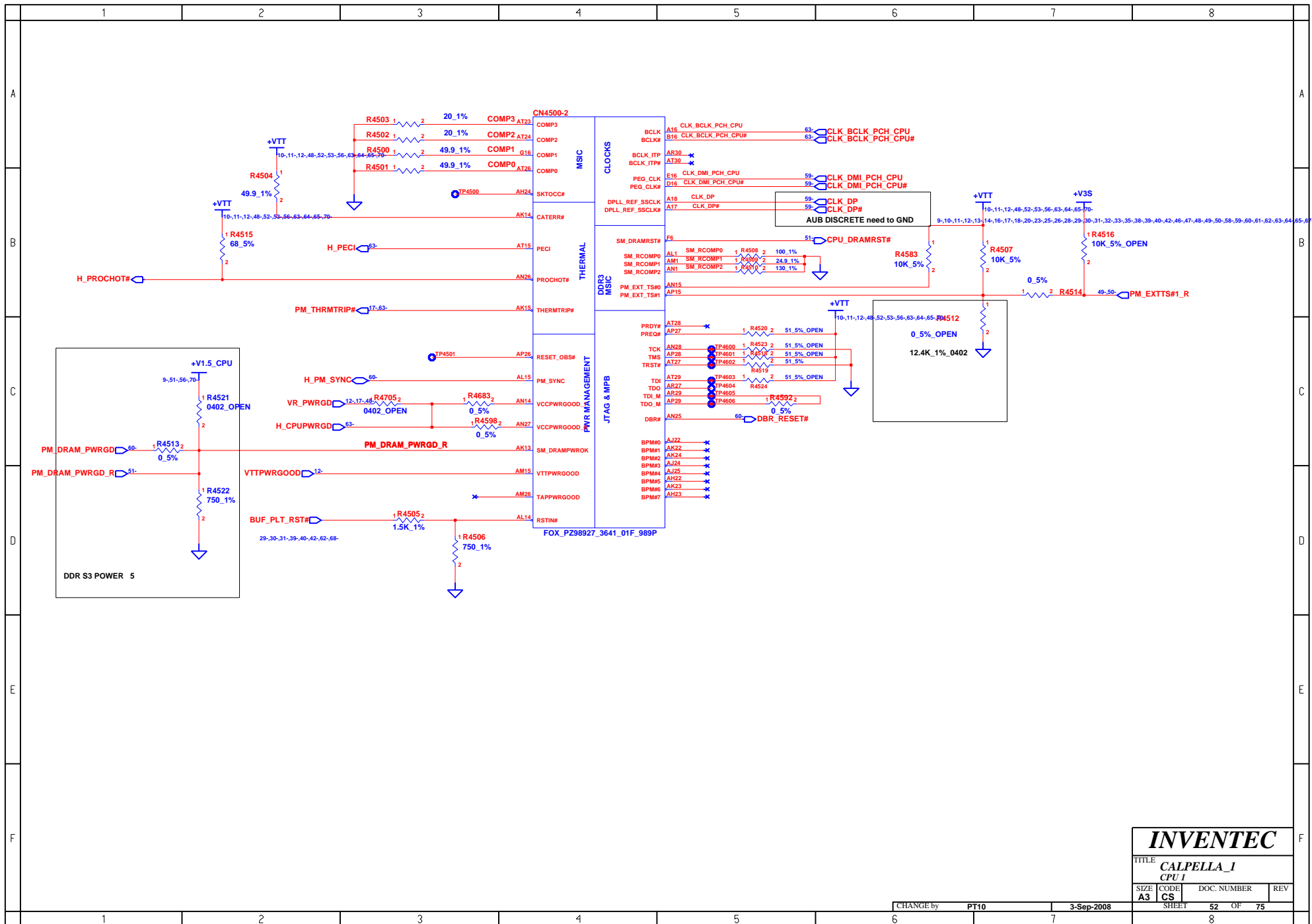
INVENTEC

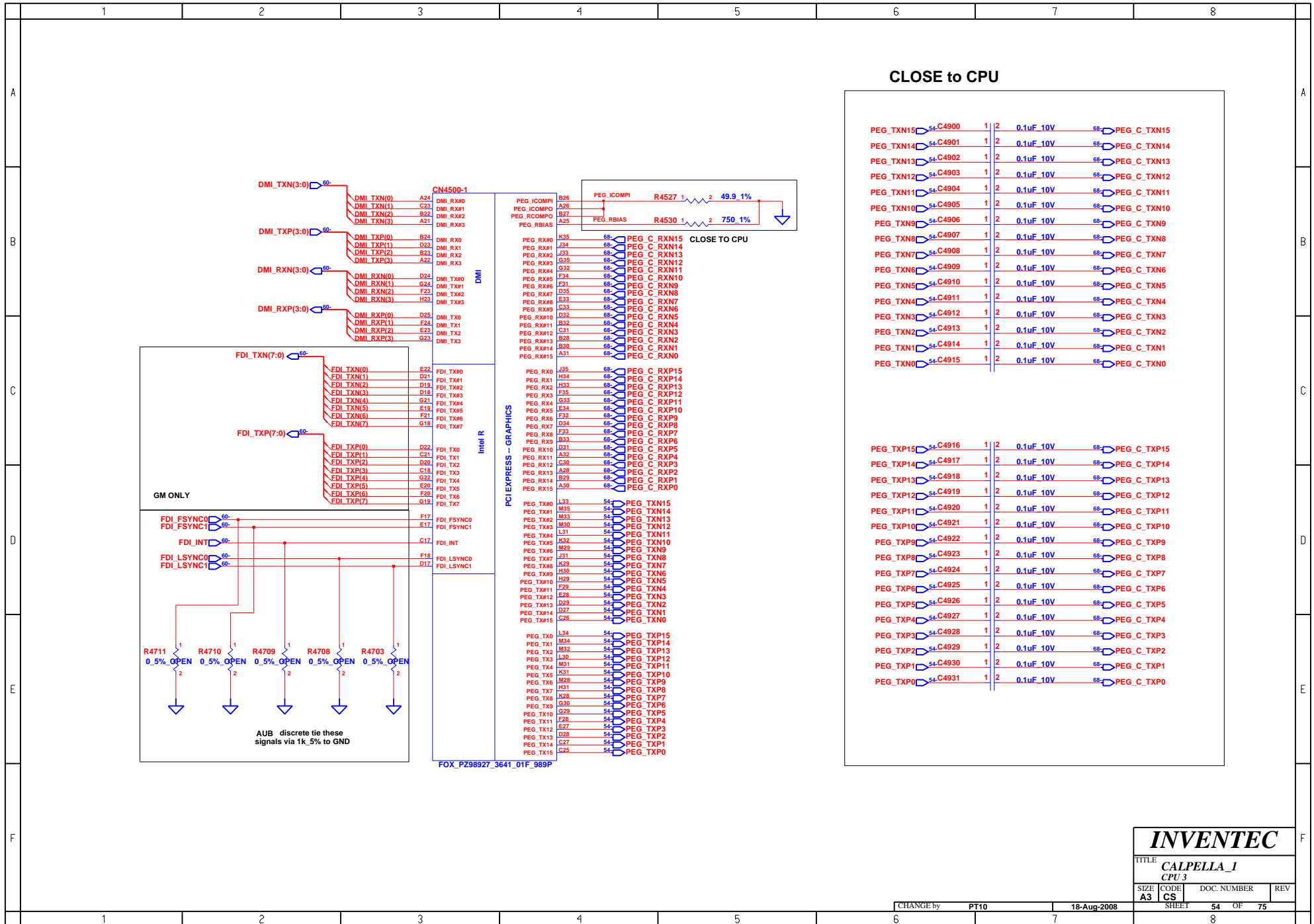
TITLE: **CALPELLA_I**
CLOCK GENERATOR

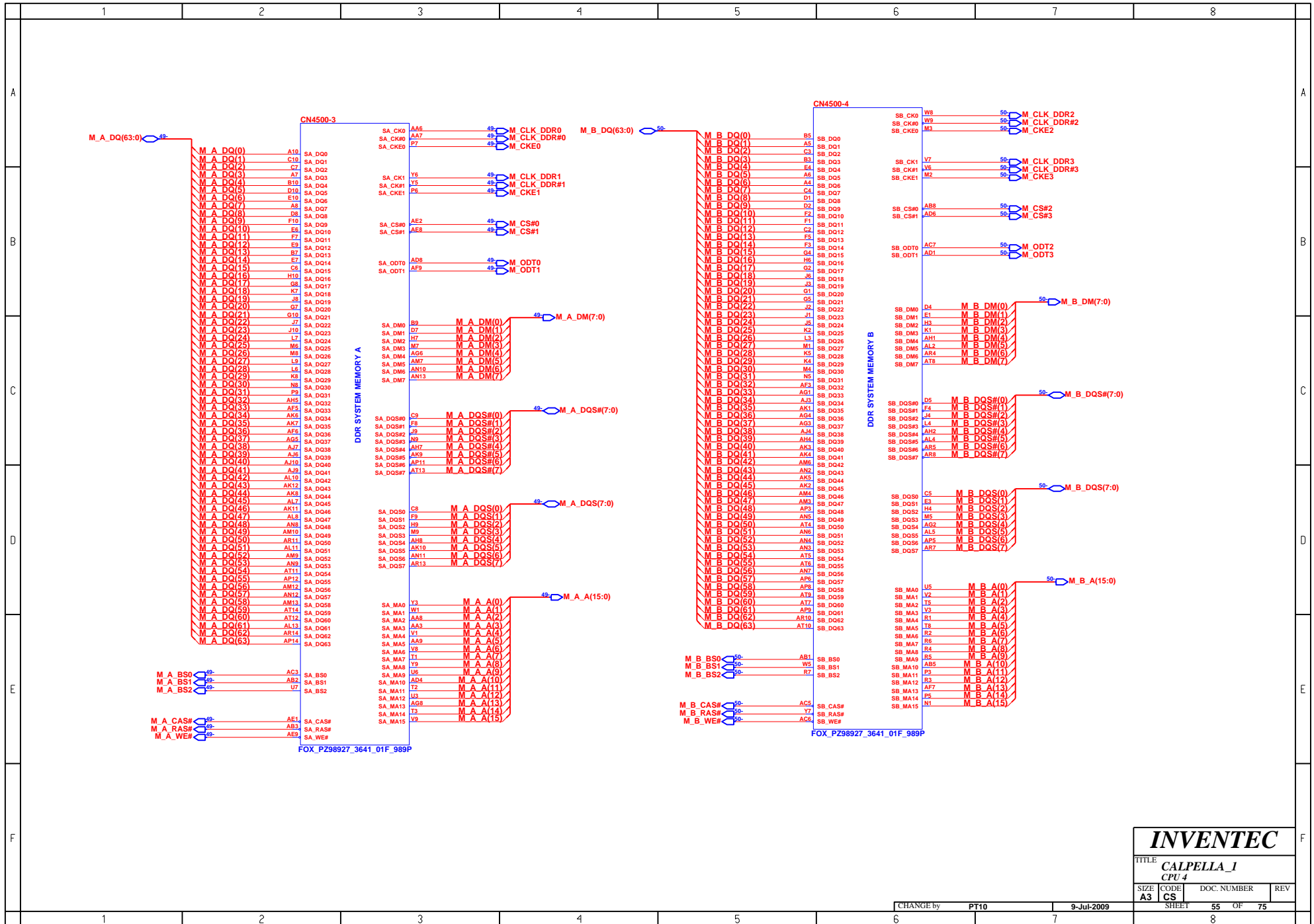
SIZE: A3	CODE: CS	DOC. NUMBER:	REV:
-----------------	-----------------	--------------	------

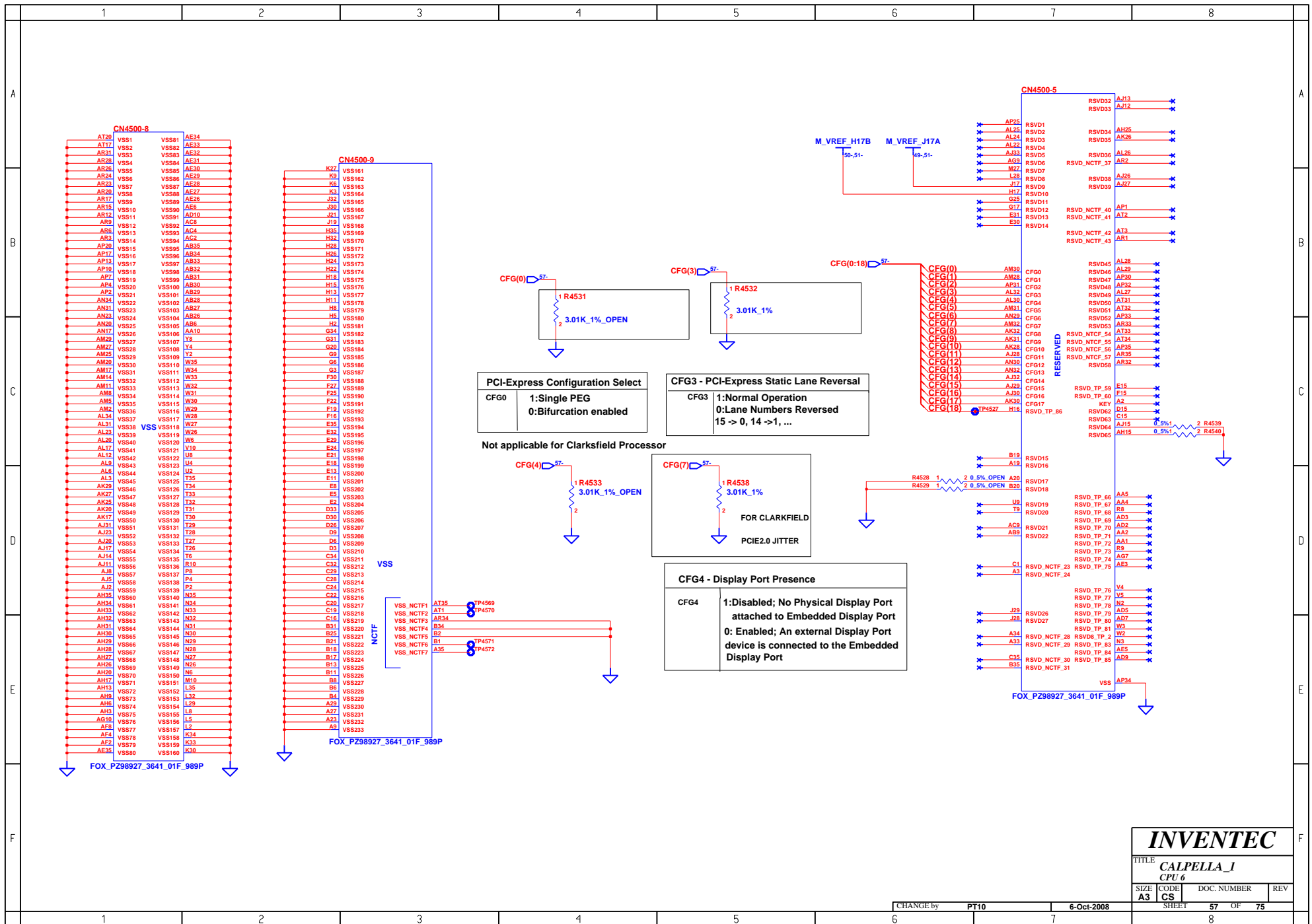
CHANGE by: **PT10** 9-Sep-2008

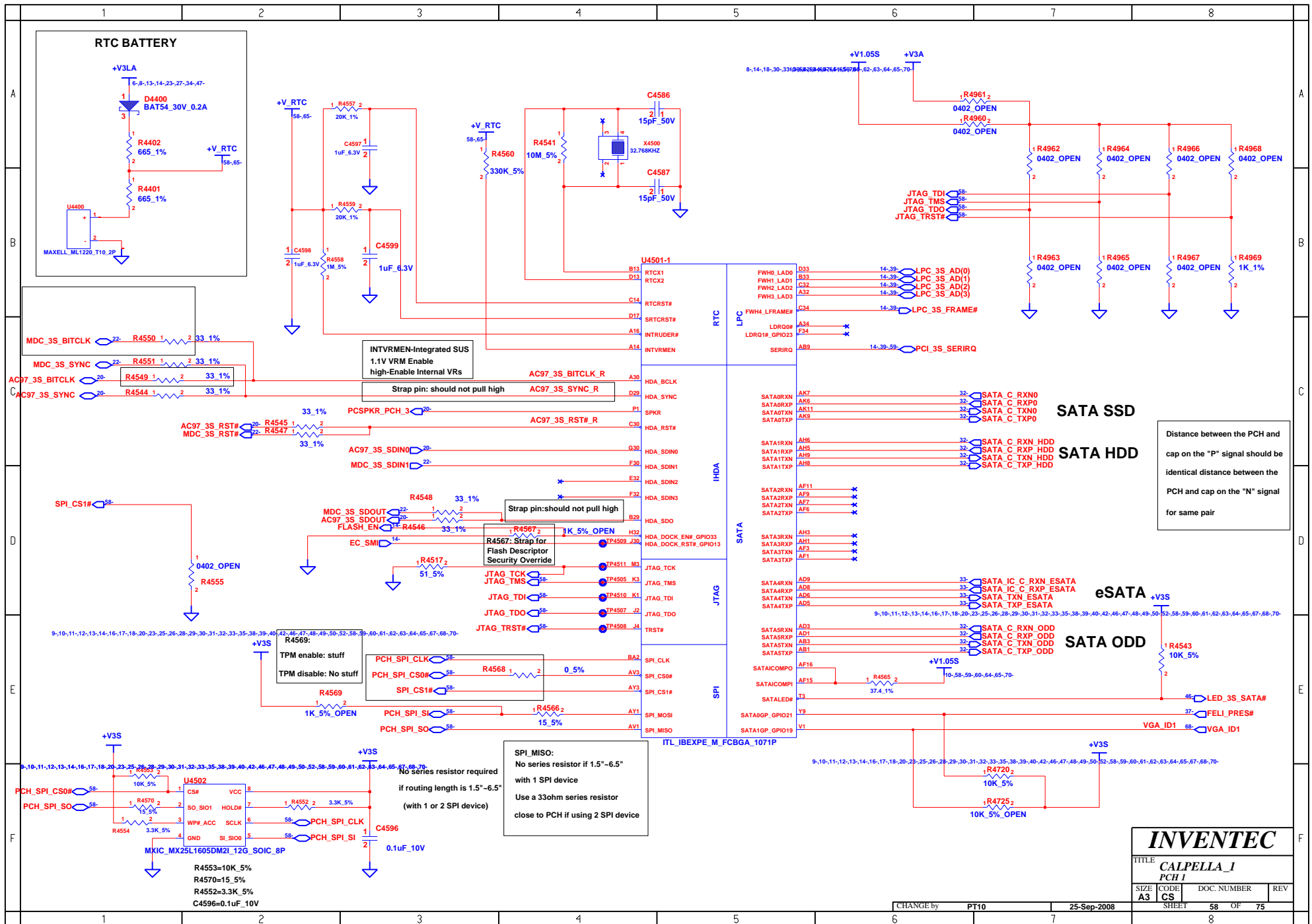
SHEET: **48** OF **75**

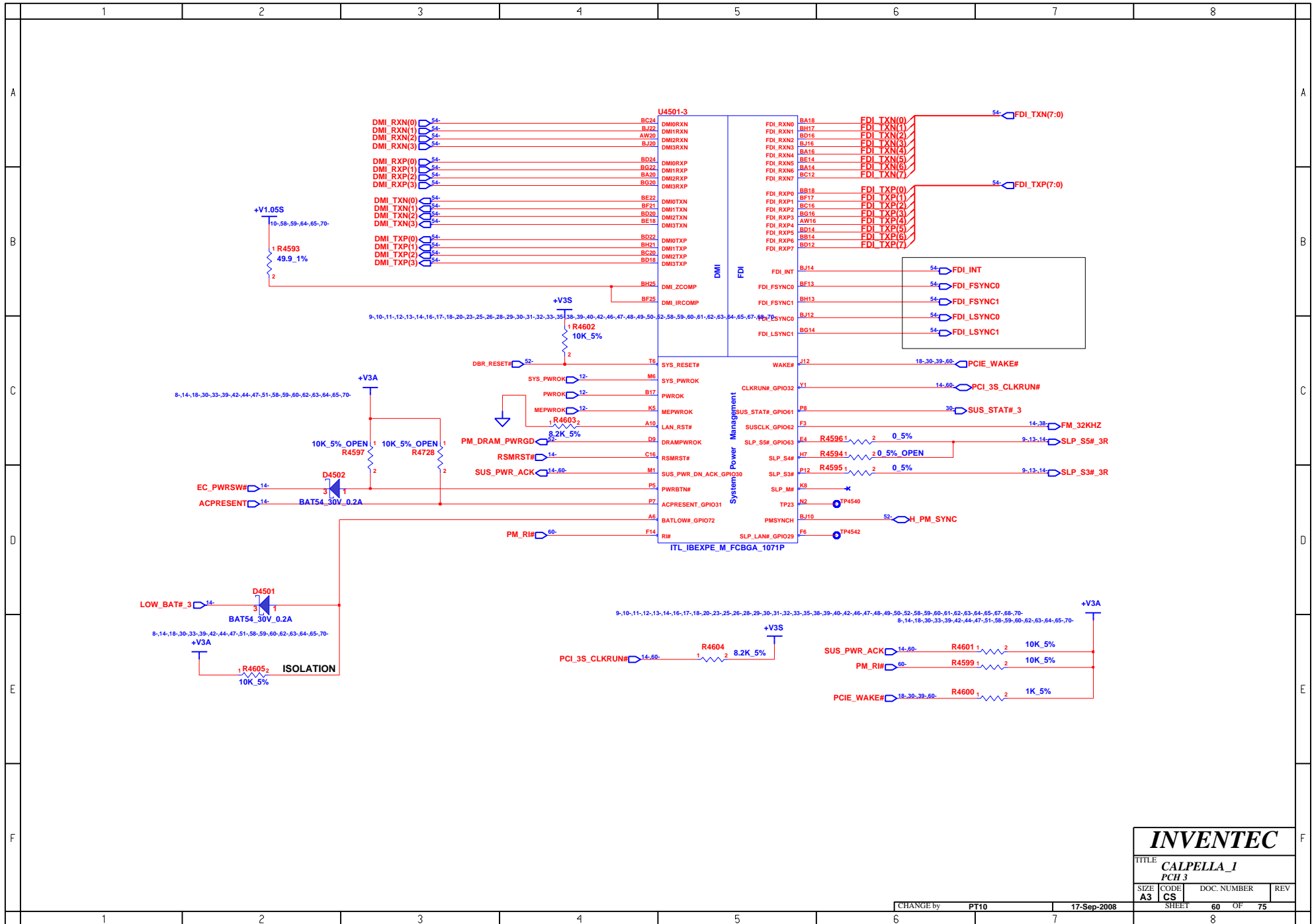












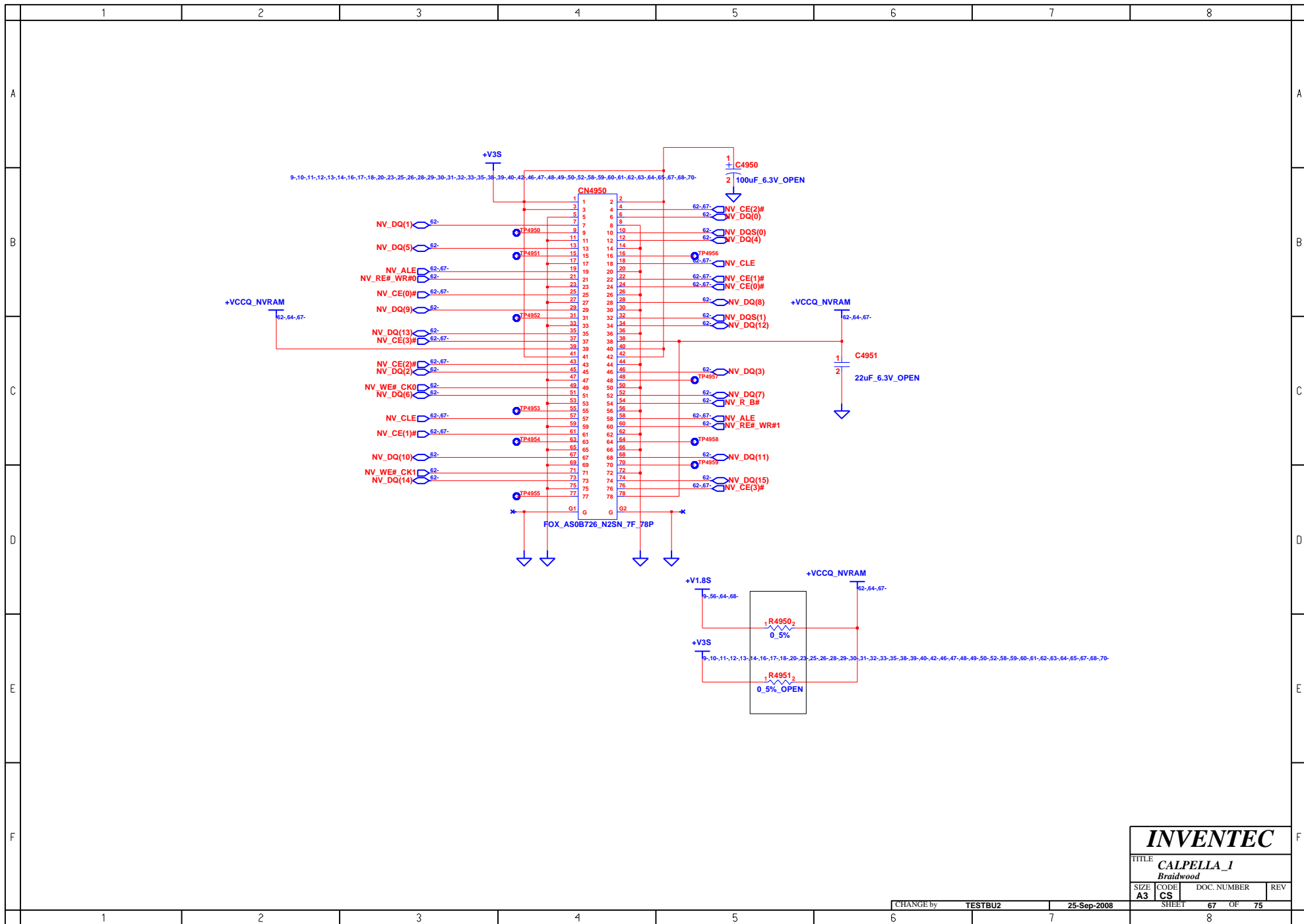
INVENTEC

TITLE
CALPELLA_I
PCH 3

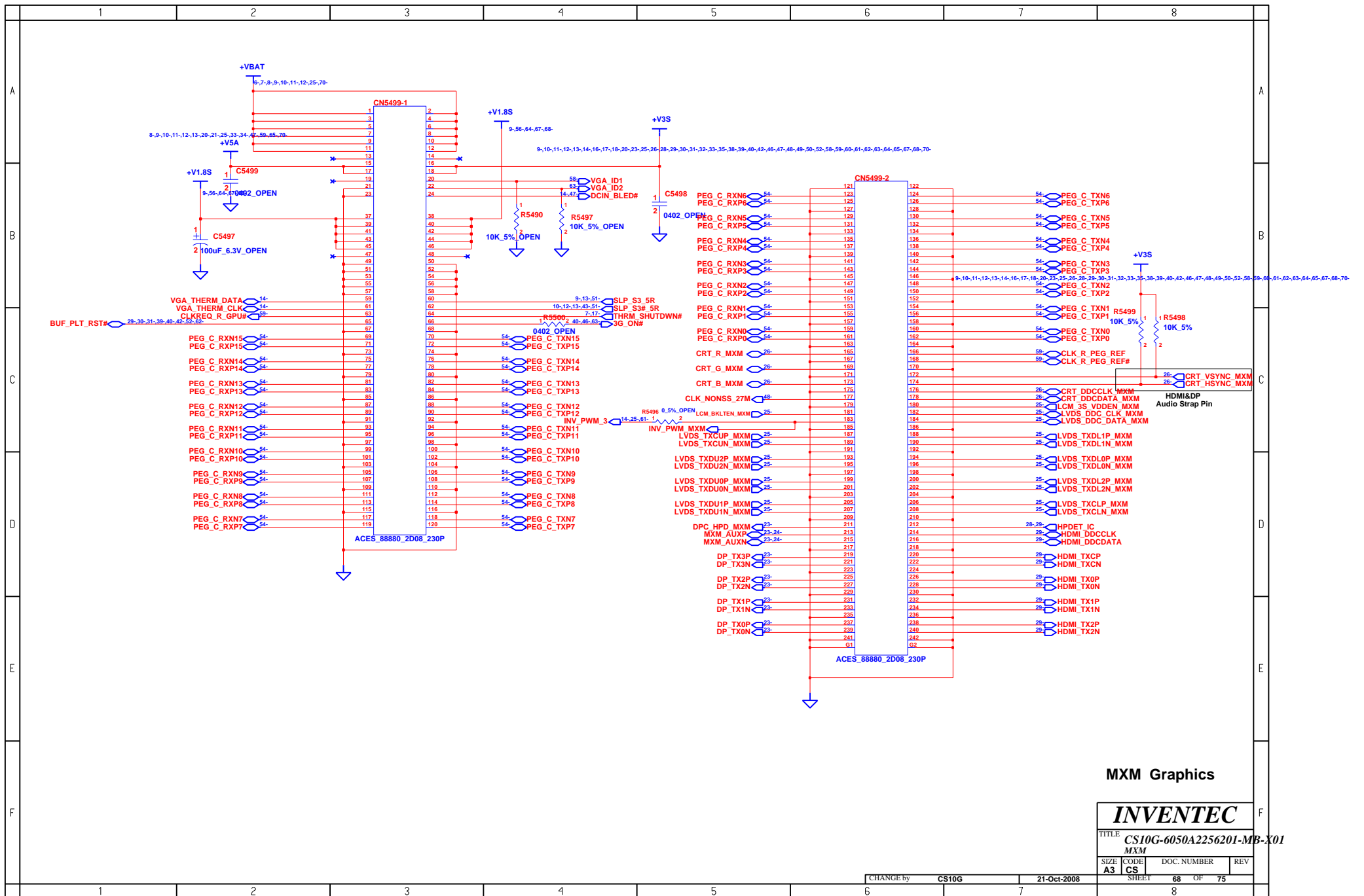
SIZE A3	CODE CS	DOC. NUMBER	REV
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The diagram illustrates the pinout for the Calpella 1 PCH 9, specifically focusing on the U4501-9 and U4501-10 components. The components are shown as integrated circuits with their pins connected to a common bus labeled ITL_IBEXPE_M_FCBGA_1071P. The pins are organized into two main sections: U4501-9 on the left and U4501-10 on the right. Each section lists the pin number, the component name, and the signal name. The signals are categorized into various groups, including VSS (ground), VDD (power), and various data and control signals. The diagram uses a color-coded system to group related signals: blue for VSS, red for VDD, and green for other signals. The components are connected to the bus via a series of pins, with the bus lines extending from the components to the right. The diagram is labeled with 'U4501-9' and 'U4501-10' at the top of each section. The bus label 'ITL_IBEXPE_M_FCBGA_1071P' is located at the bottom center of the diagram. The diagram is also labeled with 'CALPELLA_1 PCH 9' at the bottom right.

Pin	U4501-9	U4501-10
1	AA19 VSS_1	A17 VSS_159
2	AA20 VSS_2	B17 VSS_160
3	AA22 VSS_3	B19 VSS_161
4	AM19 VSS_4	B21 VSS_162
5	AA24 VSS_5	B23 VSS_163
6	AA26 VSS_6	B25 VSS_164
7	AA28 VSS_7	B27 VSS_165
8	AA30 VSS_8	B29 VSS_166
9	AA32 VSS_9	B31 VSS_167
10	AA34 VSS_10	B33 VSS_168
11	AA36 VSS_11	B35 VSS_169
12	AA38 VSS_12	B37 VSS_170
13	AA40 VSS_13	B39 VSS_171
14	AA42 VSS_14	B41 VSS_172
15	AA44 VSS_15	B43 VSS_173
16	AA46 VSS_16	B45 VSS_174
17	AA48 VSS_17	B47 VSS_175
18	AA50 VSS_18	B49 VSS_176
19	AA52 VSS_19	B51 VSS_177
20	AA54 VSS_20	B53 VSS_178
21	AA56 VSS_21	B55 VSS_179
22	AA58 VSS_22	B57 VSS_180
23	AA60 VSS_23	B59 VSS_181
24	AA62 VSS_24	B61 VSS_182
25	AA64 VSS_25	B63 VSS_183
26	AA66 VSS_26	B65 VSS_184
27	AA68 VSS_27	B67 VSS_185
28	AA70 VSS_28	B69 VSS_186
29	AA72 VSS_29	B71 VSS_187
30	AA74 VSS_30	B73 VSS_188
31	AA76 VSS_31	B75 VSS_189
32	AA78 VSS_32	B77 VSS_190
33	AA80 VSS_33	B79 VSS_191
34	AA82 VSS_34	B81 VSS_192
35	AA84 VSS_35	B83 VSS_193
36	AA86 VSS_36	B85 VSS_194
37	AA88 VSS_37	B87 VSS_195
38	AA90 VSS_38	B89 VSS_196
39	AA92 VSS_39	B91 VSS_197
40	AA94 VSS_40	B93 VSS_198
41	AA96 VSS_41	B95 VSS_199
42	AA98 VSS_42	B97 VSS_200
43	AA100 VSS_43	B99 VSS_201
44	AA102 VSS_44	B101 VSS_202
45	AA104 VSS_45	B103 VSS_203
46	AA106 VSS_46	B105 VSS_204
47	AA108 VSS_47	B107 VSS_205
48	AA110 VSS_48	B109 VSS_206
49	AA112 VSS_49	B111 VSS_207
50	AA114 VSS_50	B113 VSS_208
51	AA116 VSS_51	B115 VSS_209
52	AA118 VSS_52	B117 VSS_210
53	AA120 VSS_53	B119 VSS_211
54	AA122 VSS_54	B121 VSS_212
55	AA124 VSS_55	B123 VSS_213
56	AA126 VSS_56	B125 VSS_214
57	AA128 VSS_57	B127 VSS_215
58	AA130 VSS_58	B129 VSS_216
59	AA132 VSS_59	B131 VSS_217
60	AA134 VSS_60	B133 VSS_218
61	AA136 VSS_61	B135 VSS_219
62	AA138 VSS_62	B137 VSS_220
63	AA140 VSS_63	B139 VSS_221
64	AA142 VSS_64	B141 VSS_222
65	AA144 VSS_65	B143 VSS_223
66	AA146 VSS_66	B145 VSS_224
67	AA148 VSS_67	B147 VSS_225
68	AA150 VSS_68	B149 VSS_226
69	AA152 VSS_69	B151 VSS_227
70	AA154 VSS_70	B153 VSS_228
71	AA156 VSS_71	B155 VSS_229
72	AA158 VSS_72	B157 VSS_230
73	AA160 VSS_73	B159 VSS_231
74	AA162 VSS_74	B161 VSS_232
75	AA164 VSS_75	B163 VSS_233
76	AA166 VSS_76	B165 VSS_234
77	AA168 VSS_77	B167 VSS_235
78	AA170 VSS_78	B169 VSS_236
79	AA172 VSS_79	B171 VSS_237
80	AA174 VSS_80	B173 VSS_238
81	AA176 VSS_81	B175 VSS_239
82	AA178 VSS_82	B177 VSS_240
83	AA180 VSS_83	B179 VSS_241
84	AA182 VSS_84	B181 VSS_242
85	AA184 VSS_85	B183 VSS_243
86	AA186 VSS_86	B185 VSS_244
87	AA188 VSS_87	B187 VSS_245
88	AA190 VSS_88	B189 VSS_246
89	AA192 VSS_89	B191 VSS_247
90	AA194 VSS_90	B193 VSS_248
91	AA196 VSS_91	B195 VSS_249
92	AA198 VSS_92	B197 VSS_250
93	AA200 VSS_93	B199 VSS_251
94	AA202 VSS_94	B201 VSS_252
95	AA204 VSS_95	B203 VSS_253
96	AA206 VSS_96	B205 VSS_254
97	AA208 VSS_97	B207 VSS_255
98	AA210 VSS	B209 VSS_256



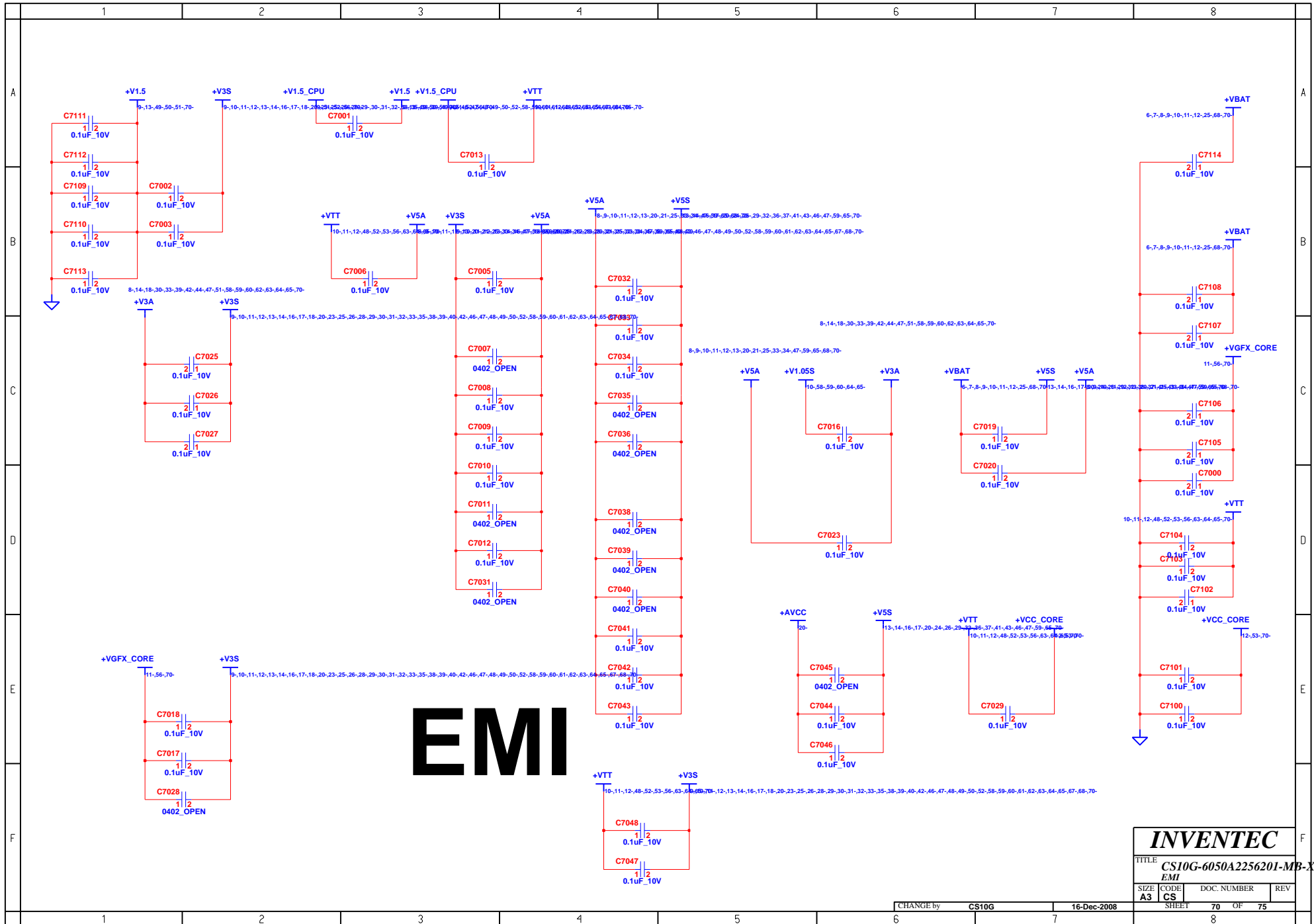
INVENTEC			
TITLE CALPELLA_I Braidwood			
SIZE A3	CODE CS	DOC. NUMBER	REV
CHANGE by		TESTBU2	25-Sep-2008
SHEET		67	OF 75
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	1	2	3	4	5	6	7	8	
A	<div>BLANK</div>								A
B									B
C									C
D									D
E									E
F									F
	1	2	3	4	5	6	7	8	

INVENTEC			
TITLE CS10G-6050A2256201-MB-X01			
Switchable Graphics			
SIZE	CODE	DOC. NUMBER	REV
A3	CS		
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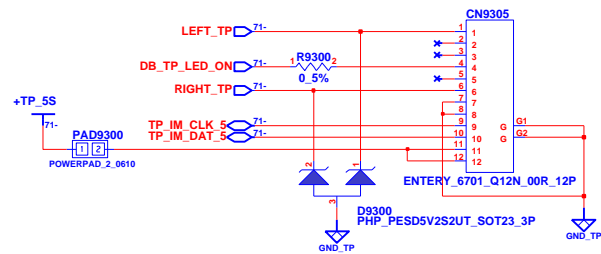
CHANGE by CS10G 7-Jul-2009



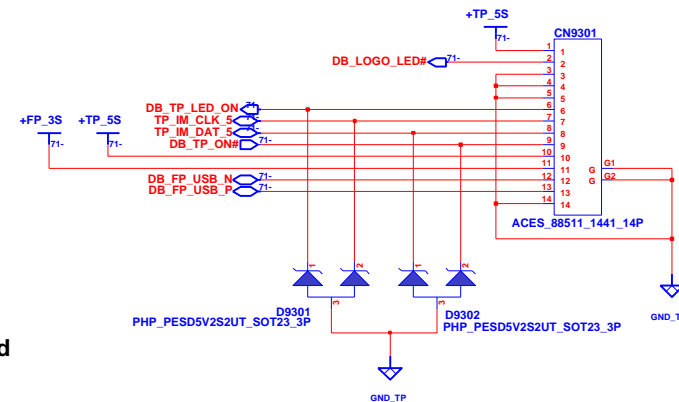
PICK BUTTON BOARD

(For - Touchpad Module, Touchpad Switch Board, Fingerprint Module)

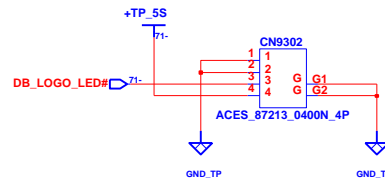
Connect to Touchpad Module



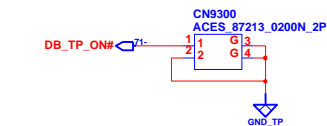
Connect to Mainboard's TP connector



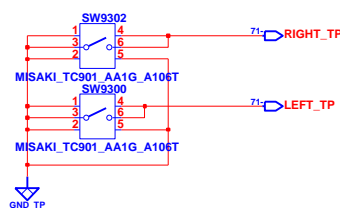
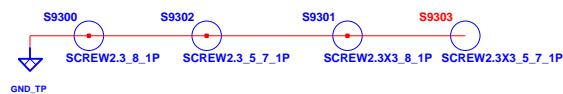
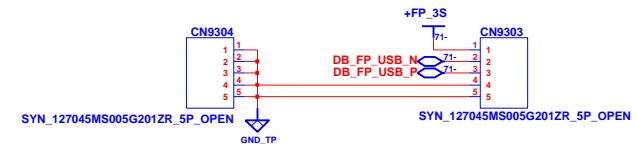
Connect to LOGO LED Board



Connect to Touchpad Switch Board



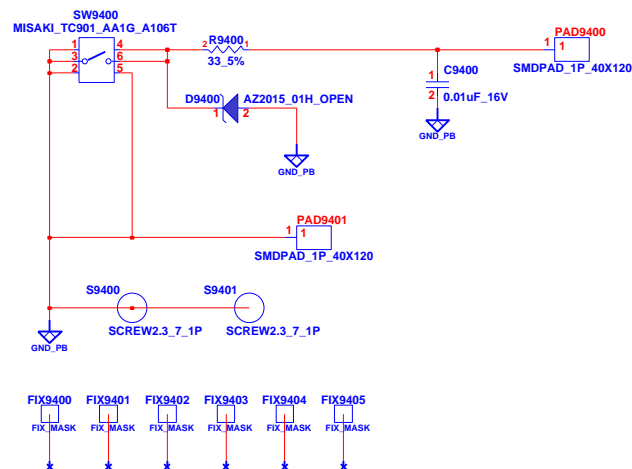
Connect to Fingerprint Module



INVENTEC			
TITLE CS10G-6050A2256201-MB-X01-ies			
SIZE A3	CODE CS	DOC. NUMBER	REV
SHEET 71		OF 75	

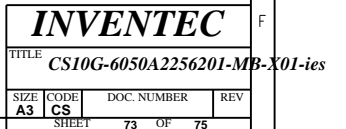
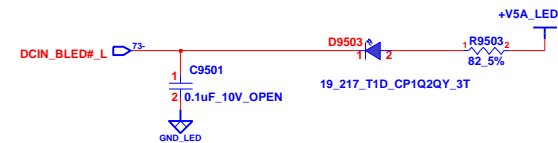
CHANGE by CS10G 8-Apr-2009

TOUCHPAD SWITCH BOARD

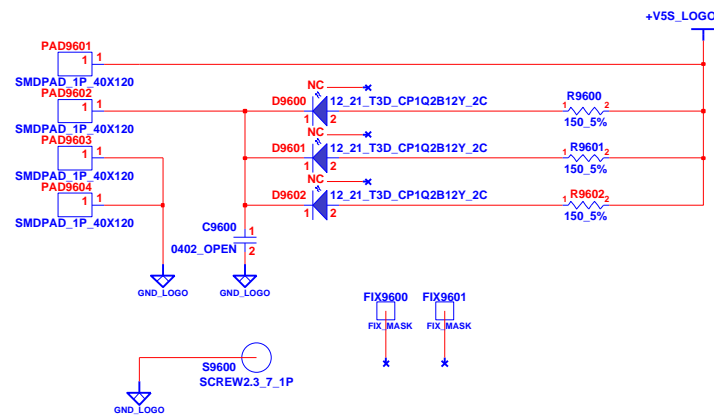


INVENTEC			
TITLE CS10G-6050A2256201-MB-X01-ies			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	SHEET 72 OF 75	

5IN1 LED



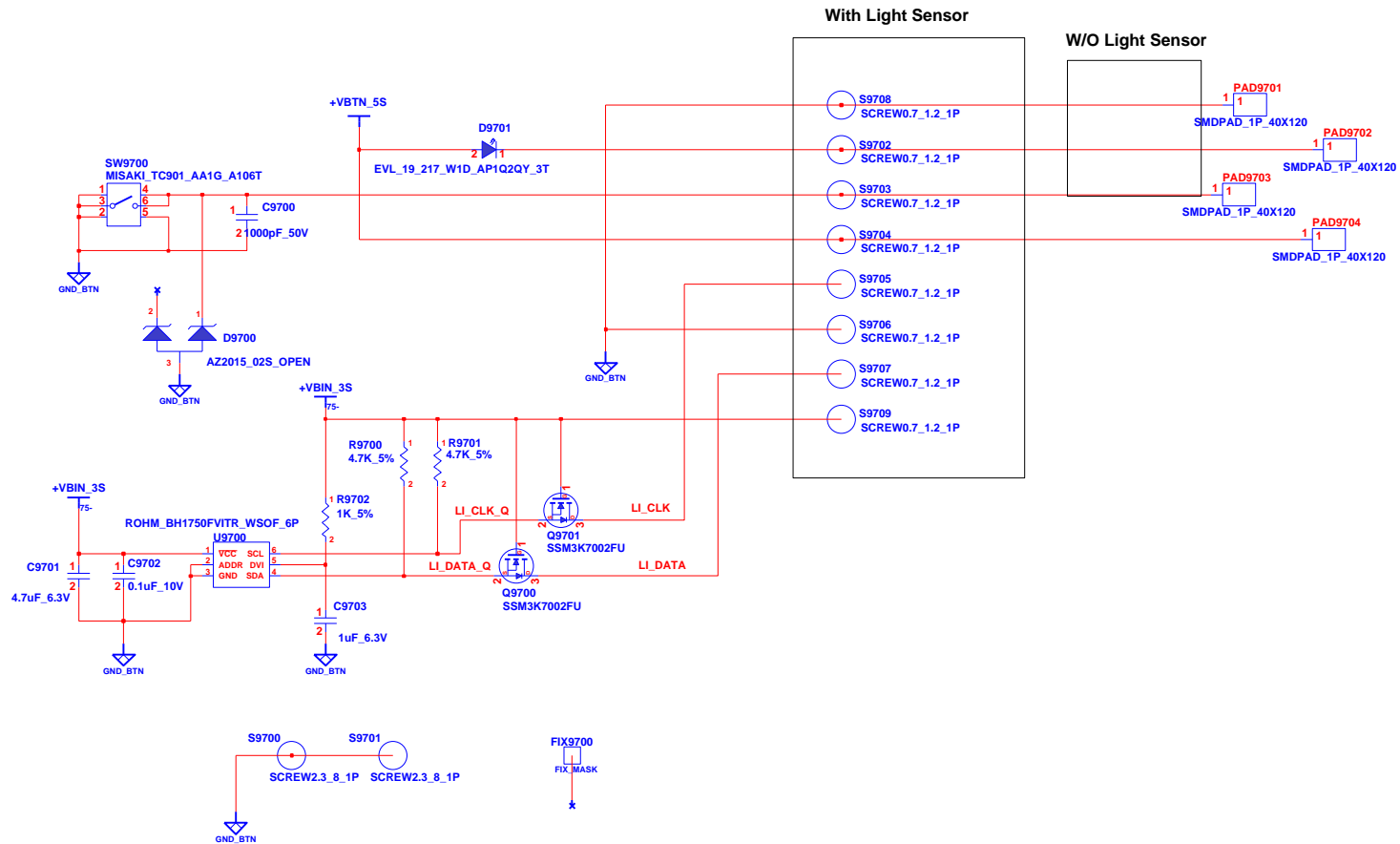
LOGO LED BOARD



INVENTEC			
TITLE CS10G-6050A2256201-MB-X01-ies			
SIZE A3	CODE CS	DOC. NUMBER	REV
SHEET 74		OF 75	

CHANGE by CS10G 16-Apr-2009

POWER BUTTON & LIGHT SENSOR BOARD



INVENTEC			
TITLE CS10G-6050A2256201-MB-X01-ies			
SIZE A3	CODE CS	DOC. NUMBER	REV
SHEET 75		OF 75	